# CONTROL DATA 6601-H/6613-A CENTRAL COMPUTER

WIRE LISTS
CABLE TABS



60148900	Record of Revisions
REVISION	NOTES
A	This is the original printing of the manual. Product
(10-8-65)	Designation H02.
В	Change Order 11540 which advanced the revision level
(3-16-66)	to B but did not affect the Product Designation.
C 34	Change Order 11602 which advanced the revision level
(3-16-66)	to C but did not affect the Product Designation.
D	Change Order 11665 which advanced the revision level
(3-16-66)	to D but did not affect the Product Designation.
E	Change Order 12287 which advanced the revision level
(3-16-66)	to E but did not affect the Product Designation.
F	Change Order 12450 which advanced the revision level
(3-16-66)	to F but did not affect the Product Designation.
G	Publication Change Order 12987 which reprints and
(3-16-66)	updates this publication. This edition obsoletes all
	previous editions.
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Address comments concerning this manual to:

Control Data Corporation Technical Publications Department 4201 North Lexington Avenue St. Paul, Minnesota 55112

or use Comment Sheet in back of this manual.

This manual lists connections of the coaxial logic cables in three sections:

- 1) Chassis location, source and destination, arranged by chassis number;
- 2) Wire connection, source and destination, arranged by chassis number, chassis location, and color;
- Wire connection, arranged by chassis, module and pin number.

The Dead Start Panel connections are listed only in parts one and two, by connection to the chassis.

Locations W39 and W40 do not physically exist. These numbers are used for sorting purposes. W39 and W40 are internal chassis connections.

Chassis Locations:

1-16 on Main Frame 6601

30 on Disk File

50 on Display Console

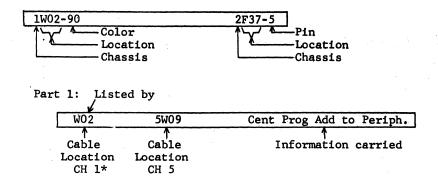
81 on 6681 Data Channel Converter

82 on 6682 Satellite Coupler

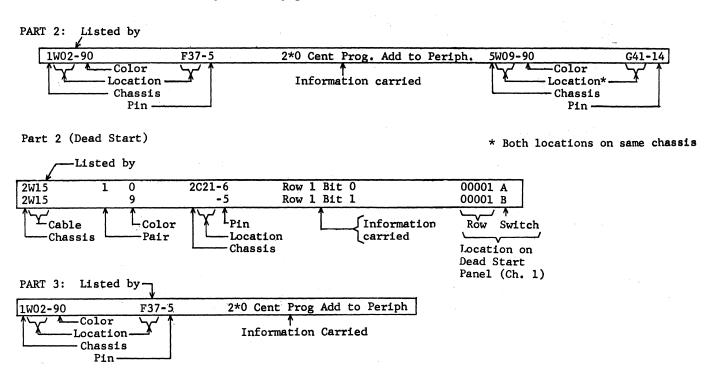
#### Reference Drawings:

63763600 <b>-</b> A	Wire listing - Dead Start Panel
63028800	Chassis Cable Tabs, 6601
63029000	Cable list, inter chassis
63028900	Cable list, inter cabinet
63037300	Cable assembly, wire list,
	intra chassis

#### Explanation of Listings



\* Chassis number listed at top of each page.



		CABLE CONNECTIONS FOR 6601 CHASSIS	1
ORIGIA	DEST	T REMARKS	
W01	3W21	CONTROL (1TO SVIA 3)	
W 0 2	5W09	CONTROL (1TO 5VIA 3) CENTRAL PROGRAM ADDRESS TO PERIPHERAL DEAD URITE SYSHANGS ADDRESS	
W03	3W22	KEBUSAKIIE EXCHANGE ADDULGO	
W04	3W03	CENTRAL MEMORY DATA TO PERIPHERAL	
W05	4W07	CENTRAL MEMORY DATA TO PERIPHERAL	
W06	9W11	CENTRAL MEMORY DATA TO PERIPHERAL	
W.Q.7	10W11	CENTRAL MEMORY DATA TO PERIPHERAL PERIPHERAL DATA TO MEMORY DISTRIBUTION	
W08	2W01	PERIPHERAL DATA TO MEMORY DISTRIBUTION	
W 0 9	2W02 2W03	PERIPHERAL DATA TO MEMORY DISTRIBUTION	
W10 W11	2W04	PERIPHERAL DATA TO MEMORY DISTRIBUTION	
W12	5W23	CONTROL TO PERIPHERAL	<del></del>
W13	J# # 0	CHANNEL O	
W14		CHANNEL O	
W15		CHANNEL 1	
W16		CHANNEL 1	
W17		CHANNEL 2	
W18		CHANNEL 2	
W19		CHANNEL 3	P 444 - 17 100 PH 444 - 141 PT 444 MA 444 AM
M30		CHANNEL 3	
W21		CHANNEL 4	
M22		CHANNEL 4	
W23 W24		CHANNEL 5 Channel 5	
W24 W25		CHANNEL 6	
W 2 5		CHANNEL 6	
W27		CHANNEL 7	
W28		CHANNEL 7	
W29		CHANNEL 10	
W30		CHANNEL 10	THE PARTY OF THE P
W31		CHANNEL 11	
W32		CHANNEL 11	
W33		CHANNEL 12	
W34		CHANNEL 12	
M22		CHANNEL 13	- representation was remarks when were
M36	D	CHANNEL 13	
W37	CABLE	5 DEAD START PANEL (SEE 63763600) MAYNTENANCE PANEL	
W38	W51	MAINTENANCE FANEL	

	F. 12	CABLE CONNECTIONS FOR 6601 CHASSIS 2
RIGIN	DEST	REMARKS
01	1W08	PERIPHERAL DATA TO MEMORY DISTRIBUTION
02	1W09	PERIPHERAL DATA TO MEMORY DISTRIBUTION
03	1W10	PERIPHERAL DATA TO MEMORY DISTRIBUTION
0.4	1W11	PERIPHERAL DATA TO MEMORY DISTRIBUTION
-	13W10	WRITE BUFFER TO DISTRIBUTOR
	14W10	WRITE BUFFER TO DISTRIBUTOR
	15W10	WRITE BUFFER TO DISTRIBUTOR
<b>—</b> — — — — — — — — — — — — — — — — — —	16W10	WRITE BUFFER TO DISTRIBUTOR
9	7W08	LOWER REGISTER TO MEMORY DISTRIBUTION
LO	7W09	LOWER REGISTER TO MEMORY DISTRIBUTION
ĬĬ	8W10	UPPER REGISTER TO MEMORY DISTRIBUTION
L 2	8W11	UPPER REGISTER TO MEMORY DISTRIBUTION
. 3	5W10	CENTRAL CONTROL TO MEMORY DISTRIBUTION
14	5W27	CENTRAL CONTROL TO MEMORY DISTRIBUTION
15	CABLE 1	DEAD START PANEL
16	CABLE 2	DEAD START PANEL
7	CABLE 3	DEAD START PANEL
18	CABLE 4	DEAD START PANEL
9		DEAD START TO CHANNEL (INPUT)
Ò		DEAD START TO CHANNEL (OUTPUT)
21	5W13	CONTROL TO DIVIDE
22	8W18	REGISTER TO DIVIDE (EXPONENT XK)
23	8W19	REGISTER TO DIVIDE (EXPONENT XJ)
24	6W20	DIVIDE TO MULTIPLY
15	6W21	DIVIDE TO MULTIPLY
26	6W22	DIVIDE TO MULTIPLY
7	6W23	MULTIPLY TO DÍVIDE:
28	6W24	MULTIPLY TO DIVIDE
29	6W25	MULTIPLY TO DIVIDE
30	6W26	MULTIPLY TO DIVIDE
31	6W27	MULTIPLY TO DIVIDE
32	6W28	MULTIPLY TO DIVIDE
33 34		
35 36		
7	1150	

## CABLE CONNECTIONS FOR 6601 CHASSIS 3

ORIGIN	DEST	REMARKS	
W01			
W02	9W13	MEMORY TO READ DISTRIBUTOR	
W03	1W04	MEMORY TO PERIPHERAL	
W04	10W13	MEMORY TO READ DISTRIBUTOR	
W 0 5	4W05	MEMORY TO READ DISTRIBUTOR	
W 0 6	4W06	MEMORY TO READ DISTRIBUTOR	
W07	9W07	MEMORY TO READ DISTRIBUTOR	
WO8	10W07	MEMORY TO READ DISTRIBUTOR	
W09	13W06	MEMORY TO READ DISTRIBUTOR	
W10	14W06	MEMORY TO READ DISTRIBUTOR	
W11	15W06	MEMORY TO READ DISTRIBUTOR	
W12	7W05	READ DISTRIBUTOR TO LOWER REGISTER	•
W13	16W06	MEMORY TO READ DISTRIBUTOR	
W14	5W02	READ DISTRIBUTOR TO CONTROL	•
W15	13W13	WRITE DISTRIBUTOR TO MEMORY	
W16	14W13	WRITE DISTRIBUTOR TO MEMORY	
W17	15W13	WRITE DISTRIBUTOR TO MEMORY	
W18	16W13	WRITE DISTRIBUTOR TO MEMORY	
W19			
W20	5W29	CONTROL TO MEMORY ADDRESS	
W21	1W01	CONTROL (1 TO 5 VIA 3)	
W22	1W03	READ-WRITE EXCHANGE ADDRESS	
W23	5W24	READ-WRITE EXCHANGE ADDRESS	
W24	5W21	CONTROL (1 TO 5 VIA 3)	
W25			
W26			
W27			
W28			
W29			
W30			
W31			
W32			
W33			
W34			
W35			
W36			
W37			
W38	W53	MAINTENANCE PANEL	
W39	3W39	MEMORY TO READ DISTRIBUTOR	

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13W12	WRITE DISTRIBUTOR TO MEMORY
14W12	WRITE DISTRIBUTOR TO MEMORY
3W05 3W06	MEMORY TO READ DISTRIBUTOR MEMORY TO READ DISTRIBUTOR
1W05	MEMORY TO PERIPHERAL
9W14	MEMORY TO READ DISTRIBUTOR
15W12	WRITE DISTRIBUTOR TO MEMORY
16W12	WRITE DISTRIBUTOR TO MEMORY
10W14	MEMORY TO READ DISTRIBUTOR
9008	MEMORY TO READ DISTRIBUTOR
10008	MEMORY TO READ DISTRIBUTOR
5W01	READ DISTRIBUTOR TO CONTROL
13W07	MEMORY TO READ DISTRIBUTOR MEMORY TO READ DISTRIBUTOR
14W07 15W07	MEMORY TO READ DISTRIBUTOR
7W06	READ DISTRIBUTOR TO LOWER REGISTER
5W18	READ DISTRIBUTOR TO CONTROL
5W28	CONTROL TO MEMORY ADDRESS
16W07	MEMORY TO READ DISTRIBUTOR
	The state of the s
	DISK SYNC PASS ON (OUTPUT)
	DISK SYNC PASS ON (INPUT)
	DISK SYNC TO CHANNEL (INPUT) DISK SYNC TO CHANNEL (OUTPUT)
30W03	DISK SYNC TO DISK FILE (OUTPUT)
30W04	DISK SYNC TO DISK FILE (INPUT)
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W54	MAINTENANCE PANEL
4W39	MEMORY TO READ DISTRIBUTOR
4502	MENOR! TO THE DISTRICT
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## CABLE CONNECTIONS FOR 6601 CHASSIS 5

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W10 2W13 W11 8W13 W12 8W12 W13 CONTROL TO UPPER REGISTER W12 8W12 W13 2W21 W14 6W16 W15 8W20 W16 7W17 W17 CONTROL TO UPPER REGISTER W17 8W21 W18 W19 READ TO LOWER REGISTER W19 READ TO LOWER REGISTER W19 7W18 W20 TW19 CONTROL TO LOWER REGISTER W20 TW19 CONTROL TO LOWER REGISTER W20 TW19 CONTROL TO LOWER REGISTER W21 3W24 W22 TW16 W20 TW19 W21 TW18 W22 TW16 W23 TW12 W24 TW23 W25 TW20 W26 8W22 W27 W16 W27 ZW14 W28 AW20 W27 ZW14 W28 AW20 W29 JW20 W29 JW20 W30 PW20 W30 PW20 W31 10W20 W31 10W20 W32 TW18 W33 TW18 CONTROL TO MEMORY ADDRESS W31 10W20 W32 TW18 W33 TW18 CONTROL TO MEMORY ADDRESS W31 10W20 W32 TW18 W33 TW18 CONTROL TO MEMORY ADDRESS W31 10W20 CONTROL TO MEMORY ADDRESS W31 10W20 CONTROL TO MEMORY ADDRESS W31 15W18 CONTROL TO MEMORY ADDRESS W31 15W18 CONTROL TO MEMORY ADDRESS W33 TW18 CONTROL TO MEMORY ADDRESS W31 15W18 CONTROL TO MEMORY ADDRESS W33 TW18 CONTROL TO MEMORY ADDRESS W33 TW18 CONTROL TO MEMORY ADDRESS W33 TW18 CONTROL TO MEMORY ADDRESS W34 TW22 W37 TW23 W36 TW22 W37 TW23 W37 TW23 W37 TW23 W37 TW23	W 0 9	1W02	PROGRAM ADDRESS TO PERIPHERAL
W12 8W12 CONTROL TO LONG ADD, AND SHIFT W13 2W21 CONTROL TO DIVIDE W14 6W16 CONTROL TO MULTIPLY W15 8W20 CONTROL TO UPPER REGISTER W16 7W17 CONTROL TO LOWER REGISTER W17 8W21 CONTROL TO UPPER REGISTER W18 4W19 READ DISTRIBUTOR TO CONTROL W19 7W18 CONTROL TO LOWER REGISTER W20 7W19 CONTROL TO LOWER REGISTER W21 3W24 CONTROL TO LOWER REGISTER W22 7W16 CONTROL TO LOWER REGISTER W23 1W12 CONTROL TO LOWER REGISTER W24 3W23 READ-WRITE EXCHANGE ADDRESS W25 7W20 CONTROL TO UPPER REGISTER W26 8W22 CONTROL TO UPPER REGISTER W27 2W14 CONTROL TO UPPER REGISTER W28 4W20 CONTROL TO MEMORY ADDRESS W29 3W20 CONTROL TO MEMORY ADDRESS W29 3W20 CONTROL TO MEMORY ADDRESS W30 9W20 CONTROL TO MEMORY ADDRESS W31 10W20 CONTROL TO MEMORY ADDRESS W32 13W18 CONTROL TO MEMORY ADDRESS W33 14W18 CONTROL TO MEMORY ADDRESS W34 15W18 CONTROL TO MEMORY ADDRESS W35 16W18 CONTROL TO MEMORY ADDRESS W35 16W18 CONTROL TO MEMORY ADDRESS W36 7W22 INCREMENT OPERAND W37 7W23 INCREMENT OPERAND	W10		CENTRAL CONTROL TO MEMORY DISTRIBUTION
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W38 W51 MAINTENANCE PANEL			
	W38	W51	MAINTENANCE PANEL

ORIGIN	DEST	REMARKS
W01		
W02		
W 0 3		
W 0 4		
W05		
W 0 6	and the second control of the second control	a physical processing and the state of the s
W 0 7	8W07	REGISTER TO MULTIPLY XK
N 0 8	8W08	MULTIPLY TO REGISTER
109	8409	REGISTER TO MULTIPLY XJ
110	7W10	MULTIPLY TO REGISTER
111	7W11	MULTIPLY TO REGISTER
112	7W12	REGISTER TO MULTIPLY XK
113	7W13	REGISTER TO MULTIPLY XJ
114	7W14	REGISTER TO MULTIPLY XJ
15	7W15	REGISTER TO MULTIPLY XK
116	5W14	CONTROL TO MULTIPLY
117		MATA-MONTH MATERIAL AND MATERIA
11.8		
120	2W24	DIVIDE TO MULTIPLY
21	2W25	DIVIDE TO MULTIPLY
122	2W26	DIVIDE TO MULTIPLY
123	2W27	MULTIPLY TO DIVIDE
24	2W28	MULTIPLY TO DIVIDE
25	2W29	MULTIPLY TO DIVIDE
126	2W30	MULTIPLY TO DIVIDE
127	2W31	MULTIPLY TO DIVIDE
28	2W32	MULTIPLY TO DIVIDE
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#### CABLE CONNECTIONS FOR 6601 CHASSIS 7 W02 W03 W04 3W12 READ DISTRIBUTOR TO LOWER REGISTER 4W18 READ DISTRIBUTOR TO LOWER REGISTER 9W12 READ DISTRIBUTOR TO LOWER REGISTER 2W09 LOWER REGISTER TO MEMORY DISTRIBUTION WOD W06 W07 2W09 2W10 6W10 W 0 8 LOWER REGISTER TO MEMORY DISTRIBUTION W 0 0 MULTIPLY TO REGISTER MULTIPLY TO REGISTER REGISTER TO MULTIPLY XK REGISTER TO MULTIPLY XJ W10 6W11 W11 W12 6W12 6W13 REGISTER TO MULTIPLY XJ W14 6W14 REGISTER TO MULTIPLY XK CONTROL TO LOWER REGISTER CONTROL TO LOWER REGISTER W15 6W15 5W22 W16 5W16 W17 CONTROL TO LOWER REGISTER 5W19 **118** CONTROL TO LOWER REGISTER CONTROL TO LOWER REGISTER 5W20 W19 W20 5W25 INCREMENT RESULT 5W08 W21 INCREMENT OPERAND W22 5W36 INCREMENT OPERAND 5W37 W23 REGISTER TO ADD REGISTER TO ADD ADD TO REGISTER ADD TO REGISTER W24 8W26 8W26 8W27 W25 8W24 W26 8W25 W27 REGISTER TO ADD W28 8W28 W29 8W29 REGISTER TO ADD D TO X SHIFT TO REGISTER W30 8W23 8W30 W31 W32 W33 W34 W35 W36

MAINTENANCE PANEL

W37

W38

IN DEST	REMARKS
9W21	READ DISTRIBUTOR TO UPPER REGISTER
10W12	READ DISTRIBUTOR TO UPPER REGISTER
6W07	REGISTER TO MULTIPLY XK
6W08	MULTIPLY TO REGISTER
6W09	REGISTER TO MULTIPLY XJ. Upper register to memory distribution
2W11 2W12	UPPER REGISTER TO MEMORY DISTRIBUTION
5W12	CONTROL TO LONG ADD, ADD AND SHIFT
5W11:	CONTROL TO UPPER REGISTER
2W22	REGISTER TO DIVIDE (EXPONENT XK)
2W23	REGISTER TO DIVIDE (EXPONENT XJ)
5W15 5W17	CONTROL TO UPPER REGISTER
5W26	CONTROL TO UPPER REGISTER
7W30	D'TO'X'
7W26	ADD TO REGISTER
7W27	ADD TO REGISTER
7W24	REGISTER TO ADD
7W25	REGISTER TO ADD
7W28	REGISTER TO ADD
7W29 7W31	REGISTER TO ADD Shift to register
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W54	MAINTENANCE PANEL
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## CABLE CONNECTIONS FOR 6601 CHASSIS 9

ORIG	N DEST	REMARKS	
W01			
W02			
W03	13W14	WRITE DISTRIBUTOR TO MEMORY	
W 0.4	14W14	WRITE DISTRIBUTOR TO MEMORY	• •
W 0 5	15W14	WRITE DISTRIBUTOR TO MEMORY	
W06	16W14	WRITE DISTRIBUTOR TO MEMORY	
W07	3W07	MEMORY TO READ DISTRIBUTOR	
W 0 8	4W12	MEMORY TO READ DISTRIBUTOR	
W 0 9	10W15	MEMORY TO READ DISTRIBUTOR	
W10	5W04	READ DISTRIBUTOR TO CONTROL	
M11	1W06	MEMORY TO PERIPHERAL	and the same of th
W12	7W07	READ DISTRIBUTOR TO LOWER REGISTER	
W13	3W02	MEMORY TO READ DISTRIBUTOR	
W14	4408	MEMORY TO READ DISTRIBUTOR	
W15	10009	MEMORY TO READ DISTRIBUTOR	
W16	13W08	MEMORY TO READ DISTRIBUTOR	
W17	14W08	MEMORY TO READ DISTRIBUTOR	
W18	15W08	MEMORY TO READ DISTRIBUTOR MEMORY TO READ DISTRIBUTOR	
W19	16W08	MEMORY TO READ DISTRIBUTOR CONTROL TO MEMORY ADDRESS	
W20	5W30 8W05	READ DISTRIBUTOR TO UPPER REGISTER	
W21 W22	TUA J11	626 (SEE 63037300)	
W23	TUA JOS	626 (SEE 63037300)	
W24	TUA J10	626 (SEE 63037300)	Marketine of the second of the second of the second of
W25	TUB J11	626 (SEE 63037300)	
W26	TUB J09	626 (SEE 63037300)	
W27	TUB J10	626 (SEE 63037300)	
W28	TUC J11	626 (SEE 63037300)	
W29	TUG J09	626 (SEE 63037300)	
W30	TUC JIO	626 (SEF 63037300)	
W31	TUD J11	626 (SEE 63037300)	
W32	TUD JOS	626 (SEE 63037300)	
W33	TUD J10	626 (SEE 63037300)	
W34		626 SYNC TO CHANNEL (INPUT)	
W35		626 SYNG TO CHANNEL (OUTPUT)	
W36		626 SYNC PASS ON (INPUT)	
W37		626 SYNC PASS ON (OUTPUT)	
W38	W51	MAINTENANCE PANEL	
W39	9W39	MEMORY TO READ DISTRIBUTOR	

	REMARKS
13W15	WRITE DISTRIBUTOR
14W15	WRITE DISTRIBUTOR
15W15	WRITE DISTRIBUTOR
16W15	WRITE DISTRIBUTOR
3W08	MEMORY TO READ DISTRIBUTOR
4W13	MEMORY TO READ DISTRIBUTOR
9W15	MEMORY TO READ DISTRIBUTOR
5W03	READ DISTRIBUTOR TO CONTROL
1W07	MEMORY TO PERIPHERAL
8W06	READ DISTRIBUTOR TO UPPER REGISTER MEMORY TO READ DISTRIBUTOR
3W04	MEMORY TO READ DISTRIBUTOR
4W11 9W09	MEMORY TO READ DISTRIBUTOR
13W09	MEMORY TO READ DISTRIBUTOR
14009	MEMORY TO READ DISTRIBUTOR
15W09	MEMORY TO READ DISTRIBUTOR
16W09	MEMORY TO READ DISTRIBUTOR
5W31	CONTROL TO MEMORY ADDRESS
30W04	DISK SYNC TO DISK FILE (INPUT)
30W03	DISK BYNC TO DISK FILE (OUTPUT)
	DISK SYNC PASS ON (OUTPUT)
	DISK SYNC PASS ON (INPUT)
	DISK SYNC TO CHANNEL (INPUT) DISK SYNC TO CHANNEL (OUTPUT)
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W52	MAINTENANCE PANEL
10439	MEMORY TO READ DISTRIBUTOR

		CABLE CONNECTIONS FOR 6601 CHASSIS 12	
RIGIN	DEST	REMARKS	
01		DISPLAY SYNC I TO CHANNEL (OUTPUT)	
2		DISPLAY SYNC 1 TO CHANNEL (INPUT)	
3		DISPLAY SYNC 1 PASSON (OUTPUT) DISPLAY SYNC 1 PASSON (INPUT)	
) 4 ) 5	50W01	DISPLAY SYNC TO CONSOLE 0 (OUTPUT)	
6	50W02		
7	50W01	DISPLAY SYNC TO CONSOLE 1 (OUTPUT)	
8 (	50W02	DISPLAY SYNC 1 TO CONSOLE 1 (INPUT)	
9		and the second s	
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L 4	THE RELEASE AND A PERSON AND AND AND AND AND SHEET SHEET SHEET SHEET.	DISPLAY SYNC 2 TO CHANNEL (OUTPUT)	
15		DISPLAY SYNC 2 TO CHANNEL (INPUT)	
1.6		DISPLAY SYNC 2 PASSON (DUTPUT) DISPLAY SYNC 2 PASSON (INPUT)	
17 18	50W01	DISPLAY SYNC 2 TO CONSOLE O (OUTPUT)	
19	50W02	DISPLAY SYNC 2 TO CONSOLE 0 (INPUT)	
20	50W01	DISPLAY SYNC 2 TO CONSOLE 1 (OUTPUT)	- 180
21	50W02	DISPLAY SYNC 2 TO CONSOLE 1 (INPUT)	
22 23			
24	# 11 0 PF . ( ) -		
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W02	W01		
NO	W02		
### ### ##############################	W03	14W11	
W06			WRITE DISTRIBUTOR TO MEMORY
WITH			
W08			
W09			
M10			
MI		and the second section of the second section in the second section in	
W12			
W13		4W03	
#14 9#03 WRITE DISTRIBUTOR TO MEMORY #15 10#03 WRITE DISTRIBUTOR TO MEMORY #17 16#03 WRITE DISTRIBUTOR TO MEMORY #18 5#32 CONTROL TO MEMORY ADDRESS #19 #20 #21 #22 TUA J11 626 (SEE 63037300) #23 TUA J09 626 (SEE 63037300) #24 TUA J10 626 (SEE 63037300) #25 TUB J11 626 #26 TUB J11 626 #27 TUB J10 626 #27 TUB J10 626 #30 TUC J10 626 #31 TUD J11 626 #32 TUD J09 626 #33 TUD J10 626 #33 TUD J10 626 #33 TUD J10 626 #34 626 SYNC TO CHANNEL (INPUT) #35 626 SYNC PASS ON (INPUT) #36 #37 626 SYNC PASS ON (INPUT)		3W15	WRITE DISTRIBUTOR TO MEMORY
W16 15W03 WRITE DISTRIBUTOR TO MEMORY W17 16W03 WRITE DISTRIBUTOR TO MEMORY W19 W20 W21 W22 TUA J11 626 (SEE 63037300) W23 TUA J09 626 (SEE 63037300) W24 TUA J10 626 (SEE 63037300) W25 TUB J11 626 W26 TUB J11 626 W27 TUB J10 626 W27 TUB J10 626 W28 TUC J11 626 W29 TUC J09 626 W30 TUC J10 626 W31 TUD J11 626 W32 TUD J09 626 W33 TUD J10 626 W34 626 SYNC TO CHANNEL (INPUT) W35 626 SYNC TO CHANNEL (OUTPUT) W36 W51 MAINTENANCE PANEL	W14	9W03	WRITE DISTRIBUTOR TO MEMORY
#17 16W03			
W18 5W32 CONTROL TO MEMORY ADDRESS W19 W20 W21 W22 TUA J11 626 (SEE 63037300) W23 TUA J09 626 (SEE 63037300) W24 TUA J10 626 W25 TUB J11 626 W26 TUB J09 626 W27 TUB J10 626 W28 TUC J11 626 W29 TUC J09 626 W30 TUC J10 626 W31 TUD J11 626 W32 TUD J11 626 W33 TUD J10 626 W33 TUD J10 626 W33 TUD J10 626 W34 626 SYNC TO CHANNEL (INPUT) W35 626 SYNC TO CHANNEL (OUTPUT) W36 W51 MAINTENANCE PANEL			WRITE DISTRIBUTOR TO MEMORY
W19 W20 W21 W22 TUA J11 626 (SEE 63037300) W23 TUA J09 626 (SEE 63037300) W24 TUA J10 626 W25 TUB J11 626 W26 TUB J09 626 W27 TUB J10 626 W28 TUC J11 626 W29 TUC J10 626 W30 TUC J10 626 W31 TUD J11 626 W32 TUD J10 626 W33 TUD J10 626 W33 TUD J10 626 W34 626 SYNC TO CHANNEL (INPUT) W35 626 SYNC TO CHANNEL (OUTPUT) W36 626 SYNC PASS ON (OUTPUT) W36 W51 MAINTENANCE PANEL			
W20 W21 W22 TUA J11 626 (SEE 63037300) W23 TUA J09 626 (SEE 63037300) W24 TUA J10 626 W25 TUB J11 626 W26 TUB J09 626 W27 TUB J10 626 W28 TUC J11 626 W29 TUC J09 626 W30 TUC J10 626 W31 TUD J11 626 W32 TUD J11 626 W33 TUD J10 626 W34 626 SYNC TO CHANNEL (INPUT) W35 626 SYNC TO CHANNEL (OUTPUT) W36 626 SYNC PASS ON (INPUT) W37 626 SYNC PASS ON (OUTPUT)		5W32	CONTROL TO MEMORY ADDRESS
W21 W22 TUA J11 626 (SEE 63037300) W23 TUA J09 626 (SEE 63037300) W24 TUA J10 626 (SEE 63037300) W25 TUB J11 626 W26 TUB J09 626 W27 TUB J10 626 W28 TUC J11 626 W29 TUC J09 626 W30 TUC J10 626 W31 TUD J11 626 W32 TUD J10 626 W33 TUD J10 626 W33 TUD J10 626 W34 626 SYNC TO CHANNEL (INPUT) W35 626 SYNC TO CHANNEL (OUTPUT) W36 626 SYNC PASS ON (INPUT) W37 626 SYNC PASS ON (OUTPUT) W38 W51 MAINTENANCE PANEL			
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TUA J09 626 (SEE 63037300)  124 TUA J10 626 (SEE 63037300)  125 TUB J11 626  127 TUB J10 626  128 TUC J11 626  130 TUC J10 626  131 TUD J11 626  132 TUD J11 626  133 TUD J10 626  134 626 SYNC TO CHANNEL (INPUT)  135 626 SYNC TO CHANNEL (OUTPUT)  136 626 SYNC PASS ON (INPUT)  137 626 SYNC PASS ON (OUTPUT)		Ψ11Δ 14.4	ADA JEEE AROTTENA
W24 TUA J10 626 (SEE 63037300) W25 TUB J11 626 W26 TUB J09 626 W27 TUB J10 626 W29 TUC J11 626 W30 TUC J10 626 W31 TUD J11 626 W32 TUD J11 626 W32 TUD J09 626 W33 TUD J10 626 W33 TUD J10 626 W34 626 SYNC TO CHANNEL (INPUT) W35 626 SYNC TO CHANNEL (OUTPUT) W36 626 SYNC PASS ON (INPUT) W37 626 SYNC PASS ON (OUTPUT) W38 W51 MAINTENANCE PANEL			
TUB J11 626 N26 TUB J09 626 N27 TUB J10 626 N28 TUC J11 626 N29 TUC J09 626 N31 TUD J11 626 N32 TUD J11 626 N33 TUD J10 626 N33 TUD J10 626 N34 626 SYNC TO CHANNEL (INPUT) N36 626 SYNC TO CHANNEL (OUTPUT) N36 626 SYNC PASS ON (INPUT) N37 626 SYNC PASS ON (OUTPUT) N38 W51 MAINTENANCE PANEL	24	TUA J10	
W26 TUB J09 626 W27 TUB J10 626 W28 TUC J11 626 W29 TUC J09 626 W30 TUC J10 626 W31 TUD J11 626 W32 TUD J09 626 W33 TUD J10 626 W33 TUD J10 626 W33 TUD J10 626 W33 TUD J10 626 W34 626 SYNC TO CHANNEL (INPUT) W35 626 SYNC TO CHANNEL (OUTPUT) W36 626 SYNC PASS ON (INPUT) W37 626 SYNC PASS ON (OUTPUT) W38 W51 MAINTENANCE PANEL			
#28	W 2 6	TUB J09	
#29 TUC J09 626 #30 TUC J10 626 #31 TUD J11 626 #32 TUD J09 626 #33 TUD J10 626 #34 626 SYNC TO CHANNEL (INPUT) #35 626 SYNC TO CHANNEL (OUTPUT) #36 626 SYNC PASS ON (INPUT) #37 626 SYNC PASS ON (OUTPUT) #38 W51 MAINTENANCE PANEL	127	7U8 J10	
W30 TUC J10 626 W31 TUD J11 626 W32 TUD J09 626 W33 TUD J10 626 W34 626 SYNC TO CHANNEL (INPUT) W35 626 SYNC TO CHANNEL (OUTPUT) W36 626 SYNC PASS ON (INPUT) W37 626 SYNC PASS ON (OUTPUT) W36 W51 MAINTENANCE PANEL		TUO J11	
W31 TUD J11 626 W32 TUD J09 626 W33 TUD J10 626 W34 626 SYNC TO CHANNEL (INPUT) W35 626 SYNC TO CHANNEL (OUTPUT) W36 626 SYNC PASS ON (INPUT) W37 626 SYNC PASS ON (OUTPUT) W38 W51 MAINTENANCE PANEL			
W32 TUD J09 626 W33 TUD J10 626 W34 626 SYNC TO CHANNEL (INPUT) W35 626 SYNC TO CHANNEL (OUTPUT) W36 626 SYNC PASS ON (INPUT) W37 626 SYNC PASS ON (OUTPUT) W38 W51 MAINTENANCE PANEL			
W33 TUD J10 626 W34 626 SYNC TO CHANNEL (INPUT) W35 626 SYNC TO CHANNEL (OUTPUT) W36 626 SYNC PASS ON (INPUT) W37 626 SYNC PASS ON (OUTPUT) W38 W51 MAINTENANCE PANEL			
W34 626 SYNC TO CHANNEL (INPUT) W35 626 SYNC TO CHANNEL (OUTPUT) W36 626 SYNC PASS ON (INPUT) W37 626 SYNC PASS ON (OUTPUT) W36 W51 MAINTENANCE PANEL			
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W37 626 SYNC PASS ON (OUTPUT) W36 W51 MAINTENANCE PANEL			
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W39 13W39 WRITE DISTRIBUTOR TO MEMORY			
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# CABLE CONNECTIONS FOR 6601 CHASSIS 14

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W01			
W 0 2			
W03 1	3W11	WRITE DISTRIBUTOR TO MEMORY	
	5W16	WRITE DISTRIBUTOR TO MEMORY	
W05 1	6W16	WRITE DISTRIBUTOR TO MEMORY	
W06	3W10	MEMORY TO READ DISTRIBUTOR	
NQ7	4W16	MEMORY TO READ DISTRIBUTOR	
108	9W17	MEMORY TO READ DISTRIBUTOR	
109 1	.0W17	MEMORY TO READ DISTRIBUTOR	
10	2W06	WRITE BUFFER TO DISTRIBUTOR	
11 1	3W03	WRITE DISTRIBUTOR TO MEMORY	
12	4W04	WRITE DISTRIBUTOR TO MEMORY	
13	3W16	WRITE DISTRIBUTOR TO MEMORY	
14	9W04	WRITE DISTRIBUTOR TO MEMORY	
15 1	LOW04	WRITE DISTRIBUTOR TO MEMORY	
16 1	5W04	WRITE DISTRIBUTOR TO MEMORY	
17 1	6W04	WRITE DISTRIBUTOR TO MEMORY	
18	5W33	CONTROL TO MEMORY ADDRESS	
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38	W52	MAINTENANCE PANEL	
39 1	4W39	WRITE DISTRIBUTOR TO MEMORY	

RIGIN	DEST	REMARKS
01		
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	3W16	WRITE DISTRIBUTOR TO MEMORY
	4W16	WRITE DISTRIBUTOR TO MEMORY
	6W17 3W11	WRITE DISTRIBUTOR TO MEMORY MEMORY TO READ DISTRIBUTOR
	4W17	MEMORY TO READ DISTRIBUTOR
	9W18	MEMORY TO READ DISTRIBUTOR
	0W18	MEMORY TO READ DISTRIBUTOR
	2W07	WRITE BUFFER TO DISTRIBUTOR
	3W04 4W09	WRITE DISTRIBUTOR TO MEMORY WRITE DISTRIBUTOR TO MEMORY
	3W17	WRITE DISTRIBUTOR TO MEMORY
	9405	WRITE DISTRIBUTOR TO MEMORY
15 1	0W05	WRITE DISTRIBUTOR TO MEMORY
	4W04	WRITE DISTRIBUTOR TO MEMORY
	6W05	WRITE DISTRIBUTOR TO MEMORY CONTROL TO MEMORY ADDRESS
18 19	2#34·	CONTROL TO MEMORI ADDRESS
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37 38	W53	MAINTENANCE PANEL
-	5W39	WRITE DISTRIBUTOR TO MEMORY
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ORIGI	N DEST	REMARKS	
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W01			
W02			The second secon
W03	13W17	WRITE DISTRIBUTOR TO MEMORY	
W 0 4	14W17	WRITE DISTRIBUTOR TO MEMORY	
W05	15W17	WRITE DISTRIBUTOR TO MEMORY	
W06	3W13	MEMORY TO READ DISTRIBUTOR Memory to read distributor	
W07	4W21	MEMORY TO READ DISTRIBUTOR	The second secon
W 0 9	10W19	MEMORY TO READ DISTRIBUTOR	
W10	2W08	WRITE BUFFER TO DISTRIBUTOR	
W11	13005	WRITE DISTRIBUTOR TO MEMORY	
W12	4W10	WRITE DISTRIBUTOR TO MEMORY	The state of the s
W13	3W18	WRITE DISTRIBUTOR TO MEMORY	
W14	9W06	WRITE DISTRIBUTOR TO MEMORY	
W15	10W06	WRITE DISTRIBUTOR TO MEMORY	
W16	14W05	WRITE DISTRIBUTOR TO MEMORY	
W17	15W05	WRITE DISTRIBUTOR TO MEMORY	THE THE STATE OF THE STATE OF THE STATE OF
W18	5W35-	CONTROL TO MEMORY ADDRESS	
W19 W20		A CONTRACTOR OF THE CONTRACTOR	
W21			
W22			
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W24		DISK SYNC PASS ON (OUTPUT)	18 Juni 19 Aug. 1 and 1 Aug. 1 Aug. 1 Aug. 1 Aug. 19 A
W25		DISK SYNC PASS ON (INPUT)	
W26		DISK SYNC TO CHANNEL (INPUT)	
W27		DISK SYNC TO CHANNEL (OUTPUT)	
W28	30W03	DISK SYNC TO DISK FILE (OUTPUT) DISK SYNC TO DISK FILE (INPUT)	
W29	30W04	DISK SYNC TO DISK FILE (INPUT)	
W30 W31			
W31			
W33			
W34			
W35			
W36			
W37			
W38	W54	MAINTENANCE PANEL	
W39	16W39	WRITE DISTRIBUTOR TO MEMORY	

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1W01	00		CONTROL (1 TO 5 VIA 3)	JW21 00
1W01	90			3W21 00
1401	91			3w21 <b>9</b> 1
1001	92		•	3W21 <b>92</b>
1W01	93			3W21 98
1W01	94			3W21 04
1W01	95			3W21 99
1W01	96			JW21 96
1W01	97	The second secon	THE PROPERTY OF THE PROPERTY O	3w21 •7
1W01	98			3W21 98
1W01	99			JW21 00
1W01	900	H02 12	CLOCK	3W21 900 J37 27
1W01	901	130 27	RD GO	3W21 901 J39 10
1W01	902	130 25	WR GO	3W21 902 J39 9
1W01	903	130 8	EXCH GO	3W21 983 J39 8
1W01	904			3W21 904
1W01	905	* *************************************	And the second s	3W21 905
1W01	906			JW21 906
1W01	907			3W21 907
1W01	908			3W21 908

1W02	0.0		PERIPHERAL FROM CENTRAL PROGRAM ADDRESS	5W09	00	
1W02	90	F37 5	2+0 CENTRAL PROGRAM ADDRESS	5w09	90	G41 4
1W02	91	F37 7	2+1 CENTRAL PROGRAM ADDRESS	5W09	91	G41 6
1W02	92	F37 10	2+2 CENTRAL PROGRAM ADDRESS	5W09	92	G41 8
1002	93	F37 21	2+3 CENTRAL PROGRAM ADDRESS	5W09	93	041 10
1W02	94	F37 24	2+4 CENTRAL PROGRAM ADDRESS	5W09	94	G41 12
1W02	95	F37 26	2+5 CENTRAL PROGRAM ADDRESS	5W09	95	G41 19
102	96	F38 5	2+6 CENTRAL PROGRAM ADDRESS	5W09	96	641 21
1W02	97	F38 7	2+7 CENTRAL PROGRAM ADDRESS	5W09	97	641 23
1402	98	F38 10	2+8 CENTRAL PROGRAM ADDRESS	5W09	98	G41 25
1W02	99	F38 21	2.9 CENTRAL PROGRAM ADDRESS	5W09	99	G41 27
1W02	900	F38 24	2+10 CENTRAL PROGRAM ADDRESS	5W09	900	642 4
1W02	901	F38 26	2+11 CENTRAL PROGRAM ADDRESS	5W09	901	042 4
1W02	902	F39 5	2+12 CENTRAL PROGRAM ADDRESS	5w09	902	G42 8
LW02	903	F39 7	2+13 CENTRAL PROGRAM ADDRESS	5W09	903	G42 10
102	904	F39 10	2+14 CENTRAL PROGRAM ADDRESS	5W09	904	042 12
1W02	905	F39 21	2+15 CENTRAL PROGRAM ADDRESS	5W09	905	042 19
1W02	906	F39 24	2+16 CENTRAL PROGRAM ADDRESS	5W09	906	642 21
1W02	907	F39 26	2+17 CENTRAL PROGRAM ADDRESS	5w09	907	G42 23
1W02	908		make a mark of the	5W09	908	474 #3

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W03	90			3w22	90	
LW03	91	F41 27	BIT 9	3W22	91	J41 10
W03	92	F41 25	BIT 10	3W22	92	J41 9
LWO3	93	F41 23	BIT 11	3W22	93	J41 8
W03	94	F42 8	81T 12	3W22	94	J42 23
LW03	95	F42 6	BIT 13	3W22	95	J42 22
W03	96	G42 4	BIT 14	3W22	96	J42 21
WO3	97	F42 27	Bit 15	3W22	97	J42 10
LW03	98	F42 25	BÎT 16	3w22	98	J42 9
WO3	99	F42 23	BIT 17	3W22	99	J42 8
WO3	900	F41 4	BIT 8	3W22	900	J41 21
WD3	901	F40 8	BIT 0	3W22	901	J40 23
WO3	902	F40 6	BÍŤ 1	3W22	902	J40 22
.W03	903	F40 4	817 2	3W22	903	J40 21
W03	904	F40 27	BiŤ 3	3W22	904	J40 10
WOS	905	F40 25	BITA	3W22	905	J40 9
W03	906	F40 23	·BÍŤ·5	3W22	906	J40 8
WO3	907	F41 8	Bij 6	3W22	907	J41 23
WD3	908	F41 6	BÎŤſŢ	3W22	908	J41 22

1W04	0.0		PE	RIPHERAL	FRO	M CENTRAL MI	EMORY	3W03	0.0		
.wo4	90	G33				PERIPHERAL		3W03	90	125	1
W04	91	G33	7 2.	1 CENTRAL	TO	PERIPHERAL	DATA	3W03	91	126	1
W04	92	G33 1	2 *	2 CENTRAL	ŤO	PERIPHERAL	DATA	3W03	92	127	1
WO4	93	G33 2	1 2.	3 CENTRAL	TO	PERIPHERAL	DATA	3W03	93	128	1
LWO4	94	G33 2		4 CENTRAL	ŤΟ	PERIPHERAL	DATA	3 W D 3	94	129	1
LWO4	95	G33 2	6 2+	5 CENTRAL	TO	PERIPHERAL	DATA	3W03	95	132	1
WO4	96	G34	5 2*	6 CENTRAL	TO	PERIPHERAL	DATA	3W03	96	133	1
W04	97	G34	7 2+	7 CENTRAL	TO	PERIPHERAL	DATA	3W03	97	134	1
WQ4	98	G34 1	2+	8 CENTRAL	TO	PERIPHERAL	DATA	3W03	98	135	1
WO4	99	G34 2	2 .	9 CENTRAL	ŤO	PERIPHERAL	DATA	3W03	99	136	1
LWO4	900	G34 2		10 CENTRA	LT	O PERIPHERAL	LDATA	3W03	900	138	1
WO4	901	G34 2		11 CENTRA	LT	O PERIPHERAL	L DATA	3w03	901	139	1
LWO4	902	G35	5 2+	12 CENTRA	LT	O PERIPHERAL	L DATA	3W03	902	140	1
LWO4	903	CALL PROPERTY FOR AMERICAN PROPERTY.	7 2.	13 CENTRA	LT	O PERIPHERAL	LEDATÁ	3W03	903	141	1
LWO4	904	G35 1	0 2+	14 CENTRA	LT	O PERIPHERA	L DATA	3W03	904	142	1
LWO4	905	H29 2		MORY MARG			-	3W03	905	C14	14
WO4	906							3 W D 3	906		
LWO4	907							3W03	907		
1W04	908							3W03	908		

1W05	0.0		PERIPHERAL FROM CENTRAL MEMORY	4W07 00	
1805	9.0	G35 21	2+15 CENTRAL TO PERIPHERAL DATA	4W07 90	101 1
1W05	91	G35 24	2+16 CENTRAL TO PERIPHERAL DATA	4W07 91	102 1
1W05	92	G35 26	2+17 CENTRAL TO PERIPHERAL DATA	4807 92	103 1
1W05	93	036 5	2+18 CENTRAL TO PERIPHERAL DATA	4W07 93	I04 1
1W05	94	G36 7	2+19 CENTRAL TO PERIPHERAL DATA	4807 94	105 1
1W05	95	G36 10	2+20 CENTRAL TO PERIPHERAL DATA	4W07 95	108 1
1W05	96	G36 21	2+21 CENTRAL TO PERIPHERAL DATA	4807 96	109 1
1W05	97	G36 24	2+22 CENTRAL TO PERIPHERAL DATA	4W07 97	110 1
1W05	98	G36 26	2+23 CENTRAL TO PERIPHERAL DATA	4W07 98	111 1
1W05	99	G37 5	2+24 CENTRAL TO PERIPHERAL DATA	4W07 99	112 1
1W05	900	G37 7	2+25 CENTRAL TO PERIPHERAL DATA	4807 900	114 1
1W05	901	G37 10	2+26 CENTRAL TO PERIPHERAL DATA	4W07 901	115 1
1W05	902	G37 21	2+27 CENTRAL TO PERIPHERAL DATA	4W07 902	116 1
1W05	903	G37 24	2+28 CENTRAL TO PERIPHERAL DATA	4W07 903	117 1
1W05	904	G37 26	2-29 CENTRAL TO PERIPHERAL DATA	4W07 904	118 1
1W05	905	125 11	RESUME CENTRAL READ	4W07 905	124 7
1W05	906	129 1	RESUME CENTRAL READ	4W07 906	124 9
1W05	907	H29 22	STORAGE MARGIN	4W07 907	C14 14
1W05	908			4W07 908	

1W06	00			PERIPHERAL FROM CENTRAL MEMORY	9W11	. 000		
1406	90	G39	10	2+38 CENTRAL TO PERIPHERAL DATA	9W11	90	135	1
1W06	91	G39	21	2+39 CENTRAL TO PERIPHERAL DATA	9W11	91	136	1
1W06	92	G39	24	2+40 CENTRAL TO PERIPHERAL DATA	9W11	92	138	. 1
1W06	93	G39	26	2+41 CENTRAL TO PERIPHERAL DATA	9W11	93	139	1
1406	94	G40	5	2+42 CENTRAL TO PERIPHERAL DATA	9W11	94	140	1
1W06	95	G40	7	2+43 CENTRAL TO PERIPHERAL DATA	9W11	95	141	1
1W06	96	G40	10	2+44 CENTRAL TO PERIPHERAL DATA	9W11	96	142	1
1W06	97	H29	18	MEMORY MARGIN	9W11	97	C14	14
1W06	98				9W11	98		
1W06	99				9W11	99		
1W06	900	G38	5	2+30 CENTRAL TO PERIPHERAL DATA	9W11	900	125	1
1W06	901	G38	7	2+31 CENTRAL TO PERIPHERAL DATA	9W11	901	126	1
1W06	902	G38	10	2+32 CENTRAL TO PERIPHERAL DATA	9W11	902	127	ĩ
1W06	903	G38	21	2+33 CENTRAL TO PERIPHERAL DATA	9W11	903	128	1
1W06	904	G38	24	2+34 CENTRAL TO PERIPHERAL DATA	9W11	904	129	ĩ
1W06	905	G38	26	2+35 CENTRAL TO PERIPHERAL DATA	9W11	905	132	· 1
1W06	906	G39	5	2#36 CENTRAL TO PERIPHERAL DATA	9W11	906	133	1
1W06	907	G39	7	2+37 CENTRAL TO PERIPHERAL DATA	9W11	907	134	ī
1W06	908				9W11	908		-

1W07	00	nin riigiga diginin priigiga viitiin digiga spirin, moone millige, spirin siirine spirine.	PERIPHERAL FROM CENTRAL MEMORY	10W11 00
1W07	: <b>9</b> 0	641 26	2+53 CENTRAL TO PERIPHERAL DATA	10W11 90 I11 1
1W07	91	042 5	2+54 CENTRAL TO PERIPHERAL DATA	10W11 91 112 1
W07	92	G42 7	2+55 CENTRAL TO PERIPHERAL DATA	10W11 92 I14 1
1W07	93	G42 10	2+56 CENTRAL TO PERIPHERAL DATA	10W11 93 115 1
WO7	94	G42 21	2.57 CENTRAL TO PERIPHERAL DATA	10W11 94 I16 1
LW07	95	G42 24	2+58 CENTRAL TO PERIPHERAL DATA	10W11 95 I17 1
WO7	96	G42 26	2+59 CENTRAL TO PERIPHERAL DATA	10W11 96 I18 1
LW07	97	H29 16		10W11 97 C14 14
LWO7	98			10W11 98
LWO7	99			10W11 99
W07	900	G40 21	2+45 CENTRAL TO PERIPHERAL DATA	10W11 900 TO1 1
WO7	901	G40 24	2+46 CENTRAL TO PERIPHERAL DATA	10W11 901 102 1
LWO7	902	G40 26	2+47 CENTRAL TO PERIPHERAL DATA	10W11 902 103 1
W07	903	G41 5	2+48 CENTRAL TO PERIPHERAL DATA	10W11 903 104 1
WO7	904	G41 7	2+49 CENTRAL TO PERIPHERAL DATA	10W11 904 IO5 1
WO7	905	G41 10	2.50 CENTRAL TO PERIPHERAL DATA	10W11 905 108 1
W07	906	641 21	2+51 CENTRAL TO PERIPHERAL DATA	10W11 906 109 1
W07	907	G41 24	2+52 CENTRAL TO PERIPHERAL DATA	10W11 907 T10 1
LW07	908			10W11 908

W 0 8	0.0		PERIPHERAL DATA TO MEMORY DISTRIBUTION	2W01	0.0		
W 0 8	90	J33 8	2+48 PERIPHERAL TO CENTRAL DATA	2w01	90	809	:5
WOB	91	J33 6	2+49 PERIPHERAL TO CENTRAL DATA	2W01	91	809	7
108	92	J33 4	2+50 PERIPHERAL TO CENTRAL DATA	2W01	92		LÓ
108	93	J33 27	2+51 PERIPHERAL TO CENTRAL DATA	2W01	93	B09 2	21
108	94	J33 25	2+52 PERIPHERAL TO CENTRAL DATA	2W01	94	B09 2	14
08	95	J33 23	2+53 PERIPHERAL TO CENTRAL DATA	2W01	95	B09 2	
08	96	J34 8	2+54 PERIPHERAL TO CENTRAL DATA	2W01	96	B10	5
ÖB	97	J34 6	2+55 PERIPHERAL TO CENTRAL DATA	2W01	97	B10	7
08	98	J34 4	2+56 PERIPHERAL TO CENTRAL DATA	2W01	98	B10 1	LO
08	99	J34 27	2.57 PERIPHERAL TO CENTRAL DATA	2W01	99	B10 2	?1
08	900	J34 25	2+58 PERIPHERAL TO CENTRAL DATA	2W01	900	B10 2	3.4
08	901	J34 23	2+59 PERIPHERAL TO CENTRAL DATA	2W01	901	B10 2	26
08	902	J35 8	2+36 PERIPHERAL TO CENTRAL DATA	2W01	902	B07	5
08	903	J35 6	2+37 PERIPHERAL TO CENTRAL DATA	2W01	903	B07	7
08	904	J35 4	2+38 PERIPHERAL TO CENTRAL DATA	2W01	904	B07 1	. 0
08	905	129 21	RESUME CENTRAL WRITE	2W01	905	C03	8
08	906			2W01	906		
08	907		· · · · · · · · · · · · · · · · · · ·	2W01	907		
08	908			2W01	908		

1409	00			PERIPHERAL DATA	TO	MEMORY DISTRIBUTION	2W02	00		
1009	90	J35	27	2+39 PERIPHERAL	TO	CENTRAL DATA	2W02	90	B07 21	
1409	91	J35	25	2+40 PERIPHERAL	TO	CENTRAL DATA	2W02	91	B07 24	
1009	92	J35	23	2+41 PERIPHERAL	TO		2002	92	B07 26	
1809	93	J36	8	2+42 PERIPHERAL	TO		2W02	93	B08 5	
1409	94	J36	6	2+43 PERIPHERAL	TO	CENTRAL DATA	2W02	94	808 7	
1W09	95	J36	4	2+44 PERIPHERAL	TO	CENTRAL DATA	2W02	95	808 10	
1W09	96	J36	27	2+45 PERIPHERAL	TO	CENTRAL DATA	2W02	96	B08 21	
1409	97	J36	25	2+46 PERIPHERAL	TO	CENTRAL DATA	2W02	97	B08 24	
1W09	98	J36	23	2+47 PERIPHERAL	TO	CENTRAL DATA	2W02	98	B08 26	
1W09	99	J37	8	2+24 PERIPHERAL	TO	CENTRAL DATA	2W02	99	B05 5	
1W09	900	J37	6	2+25 PERIPHERAL	TO	CENTRAL DATA	2402	900	B05 7	
1809	901	J37	4	2+26 PERIPHERAL	TO	CENTRAL DATA	2W02	901	B05 10	
1409	902	J37	27	2+27 PERIPHERAL	TO	CENTRAL DATA	2W02	902	B05 21	
1009	903	J37	25	2+28 PERIPHERAL	TO	CENTRAL DATA	2W02	903	B05 24	
1W09	904	J37	23	2+29 PERIPHERAL	TO	CENTRAL DATA	2W02	904	B05 26	
1W09	905						2W02	905		
1409	906						2W02	906		
1409	907						2W02	907		
1W09	908						2W02	908		

1W10	0,0			PERIPHERAL DATA	10	MEMORY DISTRIBUTION	2W03	0 0		
LW10	90	J38	8	2+30 PERIPHERAL	TO	CENTRAL DATA	2W03	90	B06	5
1W10	91	J38	6	2+31 PERIPHERAL	TO	CENTRAL DATA	2W03	91	B06	7
1W10	92	J38	4	2+32 PERIPHERAL		CENTRAL DATA	2W03	92	B06	10
1W10	93		27	2+33 PERIPHERAL			2W03	93	B06	21
LW10	94	J38	25	2+34 PERIPHERAL			2W03	94	806	24
1W10	95	J38	23	2+35 PERIPHERAL			2W03	95	B06	26
1W10	96	J39	8	2+12 PERIPHERAL			2W03	96	B 0 3	5
	97	J39	4	2+13 PERIPHERAL			2W03	97	B03	7
1W10	98	J39	9	2+14 PERIPHERAL			2W03	98	B03	10
LW10	99	J39	27	2+15 PERIPHERAL			2W03	99	B03	21
LW10							2W03	900	B03	24
LW10	900	J39	25				2W03	901	B03	26
1W10	901	J39	23		-		2w03	902	B04	5
LW10	902	J40	8	2+18 PERIPHERAL			2W03	903	B04	
LW10	903	J40	6	2+19 PERIPHERAL			-		B n 4	
1W10	904	J40	. 4	2+20 PERIPHERAL	. TO	CENTRAL DATA	2W03	904	דטם	10
1W10	905						2W03	905		
1W10	906						2w03	906		
1w10	907						2W03	907		
1w10	908						2W03	908		

1411	00			PERIPHERAL DATA TO MEMORY DISTRIBUTION	2W04	00	er differ filled som vinns vorus versa space space soms soms analy solder filled som vipag from vic
LW11	90	J40	27	2421 PERIPHERAL TO CENTRAL DATA	2W04	90	804 21
1W11	91	J40	25	2+22 PERIPHERAL TO CENTRAL DATA	2W04	91	804 24
1W11	92	J40	23	2+23 PERIPHERAL TO CENTRAL DATA	2W04	92	B04 26
1W11	93	J41	8	2+0 PERIPHERAL TO CENTRAL DATA	2W04	93	801 5
1W11	94	J41	- 6	2+1 PERIPHERAL TO CENTRAL DATA	2W04	94	801 7
1W11	95	J41	4	2#2 PERIPHERAL TO CENTRAL DATA	2W04	95	801 10
1W11	96	J41	27	243 PERIPHERAL TO CENTRAL DATA	2W04	96	B01 21
1W11	97	J41	25	2+4 PERIPHERAL TO CENTRAL DATA	2W04	97	801 24
1W11	98	J41:	23	2.5 PERIRHERAL TO CENTRAL DATA	2W04	98	B01 26
1W11	99	J42	8	2+6 PERIPHERAL TO CENTRAL DATA	2W04	99	802 5
1W11	900	J42	-6	2+7 PERIPHERAL TO CENTRAL DATA	2W04	900	B02 7
1W11	901	J42	4	2+8 PERIPHERAL TO CENTRAL DATA	2W04	901	802 10
1W11	902	J42	27	249 PERIPHERAL TO CENTRAL DATA	2W04	902	B02 21
1W11	903	J42	25	2+10 PERIPHERAL TO CENTRAL DATA	2404	903	802 24
1W11	904	J42	23	2-11 PERIPHERAL TO CENTRAL DATA	2W04	904	B02 26
1W11	905				2W04	905	tin men till till til som som som som som till som som at som
1W11	906				2W04	906	
1W11	907	100 100 100	**********	\$ 100 Miles Man	2W04	907	THE RESIDENCE OF THE PARTY OF THE PARTY OF THE PARTY.
1W11	908				2W04	908	

.5	00	PERIPHERAL FROM CONTROL	5w23	00	there will be an experience out that the test will will be a test to the test	
2	90		5w23	90		
.2	91	A CONTRACTOR AND A CONT	5w23	91		
2	92	•	5W23	92		
2	93		5w23	93		
2	94		5W23	94		
2	95		5w23	95		
2	96		5w23	96		
2	97	The second section of the second second second second second second second second second section second section second se	5w23	97	<del>nett van Printer des de Partings, et det van de versen </del>	of colores . Minore
2	98		5W23	98		
2	99	The second section of the second section of the second section of the second section s	5w23	99	to the time that the time and the same and the same that the same the same that the sa	** *** *** ***
2	900	129 5 EXCHANGE RESUME OUT	5w23	900	040 2	
2	901	130 23 MC	5w23	901	042 26	
2	902	130 4 CLOCK	5W23	902	122 12	
2	903	The second secon	5w23	903		THE PERSONNEL PROPERTY OF THE PERSONNEL PROP
2	904		5w23	904		
2	905	The state of the s	5W23	905		
2	906		5w23	906		
2	907	en de la companya de La companya de la co	5w23	907		
	908		5w23	908		

1W13	0.0			CHANNEL 0
1W13	90	M36	- 6	2+0·INPUT DATA
1W13	91	M36	4	2+1 INPUT DATA
1W13	92	M36	23	2±2 INPUT DATA
1W13	93	M36	27	2+3 INPUT DATA
1W13	94	M37	-6	2+4 INPUT DATA
1W13	95	M37	4	more all companies and the second of the sec
1W13	96	M37	23	2+6 INPUT DATA
1W13	97	M37	27	2+7 INPUT DATA
1W13	98	M38	-6	2+8 INPUT DATA
1W13	99	M38	4	2+9 INPUT DATA
1W13	900	M38	23	2+10 INPUT DATA
1W13	901	M38	27	2+11 INPUT DATA
1W13	902	M35		ACTIVE
1013	903	M35		INACTIVE
1W13	904	M35		FULL
1W13	905	M35	- 8	EMPTY
1W13	906	M36	1	
1W13	907	M37	1	The state of the s
1W13	908	1107	-	
THIS	700	T. a. Serve - School of Company		

1W14	00			CHANNEL O	
1W14	90	M36	:5	2+0 OUTPUT DATA	
1W14	91	M36	11	2+1 OUTPUT DATA	
1W14	92	M36	28	2#2 OUTPUT DATA	
1W14	93	M36	24	2+3 OUTPUT DATA	
1W14	94	M37	5	2+4 OUTPUT DATA	
1W14	95	M37	11	2+5 OUTPUT DATA	
1W14	96	M37	28	2+6 OUTPUT DATA	
1W14	97	M37	24	2.7 OUTPUT DATA	
1W14	98	M38	5	2+6 OUTPUT DATA	
1W14	99	M38	11	2.9 OUTPUT DATA	
1W14	900	M38	28	2+10 OUTPUT DATA	
1W14	901	M38	24	2+11 OUTPUT DATA	
1w14	902	M35	22	ACTIVE	
1114	903	M35	18	INACTIVE	
1W14	904	M35	13	FULL	
1W14	905	M35	9	EMPTY	
1W14	906	M35	3	FUNCTION	
1W14	907	M38	4	MASTER CLEAR	
1W14	908	,,,,,	+	HAVIED VEHICLE	
***	, , ,		are to are to a	The Part of the Control of the Contr	

1W15	0.0		mo see me s me -	CHANNEL 1
1W15	90	M40	6	2+0 INPUT DATA
1W15	91	M40	4	2+1 INPUT DATA
1W15	92	M40	23	2+2 ÎNPŮT DAŤA
1W15	93	M40	27	2+3 INPUT DATA
1W15	94	M41	6	2+4 INPUT DATA
1415	95	M41	4	2+5 INPUT DATA
1W15	96	M41	23	2+6 INPUT DATA
1W15	97	M41		2+7 INPUT DATA
1W15	98	M42	-	2+8 INPUT DATA
1W15	99	M42	4	2+9 INPUT DATA
1W15	900	M42		2+10 INPUT DATA
1W15	901		27	2+11 INPUT DATA
1W15	902	M39		ACTIVE
1W15	903	M39	14	INACTIVE
1W15	904	M39	15	PULL
1W15	905	M39	ě	HPTY
1W15	906	M40	1	CLOCK (100NSEC)
1W15	907	M41		CLOCK (1US)
1W15	908	., .	*	-800. (800.

1 W 1 6	00			CHANNEL 1
				-6. à Aumania sa agus
1W16	90	M40	5	2+0 OUTPUT DATA
1W16	91	M40	11	'2+1' OUTPUT :DATA
1W16	92	M40	28	'2₩2 OUTPUT :DATA
1W16	93		24	2+3 OUTPUT DATA
	94		~ T	
1W16 .		M41	2	
1W16	95	M41		2+5 OUTPUT DATA
1W16	96	M41	28	2+6 OUTPUT DATA
1W16	97	M41	24	2+7 OUTPUT DATA
1W16	98	M42	-5	2+8 QUTPUT DATA
1W16	99		11	2+9 OUTPUT DATA
1W16	900		28	2+10 OUTPUT DATA
		200 1 201 100		
1W16	901		24	2+11 OUTPUT DATA
1W16	902	M39	22	ACTIVE
1W16	903	M39	18	INACTIVE
1W16	904	M39	13	FULL
1W16	905	M39	9	EMPTY
1W16	906	M39	3	FUNCTION
		11.7	. 7	
1W16	907	M42	1	MASTER CLEAR
1W16	908			

 $(g_{ij})_{i\in I} =$ 

```
1W17 00
                           CHANNEL 2
          90
                N36
                       6
                           240 INPUT BATA
          91
92
                           201 INPUT DATA
1W17
                N36
                           2+2 INPUT DATA
2+3 INPUT DATA
                N36 23
1W17
1W17
          93
                     27
                N36
                           2±4 INPUT DATA
2±5 INPUT DATA
2±6 INPUT DATA
1W17
          94
                N37
                       6
          95
               N37
1W17
          96
1W17
                N37
          97
                           2+7 INPUT DATA
                N37
                          2+8 INPUT DATA
2+9 INPUT DATA
2+10 INPUT DATA
2+11 INPUT DATA
          98
               N38
          99
                N38
1W17
1W17
               N38
                     23
        900
        901
1W17
               N38 27
                           ACTIVE
INACTIVE
FULL
                N35 25
1W17
        902
               N35 14
N35 15
        903
1W17
1W17
        904
                N35
                           EMPTY
        905
1W17
                      8
               N36
                           GLOCK (100NSEC)
1W17
        906
                      1
                           CLOCK (1US)
1W17
        907
                N37
1W17
        908
```

1W18	00		no 100 -	CHANNEL 2	
1W18	90 91	N36	5	200 OUTPUT DATA 201 OUTPUT DATA	
1W18	92	N36	28	2+2 OUTPUT DATA 2+3 OUTPUT DATA	
1W18	94 95	N37 N37	5 11	2+4 OUTPUT DATA 2+5 OUTPUT DATA	
1W18	96 97	N37	28 24	2*6 OUTPUT DATA 2*7 OUTPUT DATA	AM ANTO IA
1W18 1W18	98	N38	5 11	2+8 OUTPUT DATA 2+9 OUTPUT DATA	
1W18	900	N38	28	2+10 OÙTPUT DATA 2+11 OÙTPUT DATA	
1W18	902	N35	18	ACTIVE	and a figure — if
1W18 1W18	904	N35	13	FULL EMPTY	
1W18	906 907 908	N35 N38	3	FUNCTION MASTER CLEAR	
1W18	Año			A COLUMN TO THE PROPERTY OF TH	

1W19	0.0		OHANNEL' 3
1W19	90	N40:	. °2+Ö İNŘÚT∵DAŤA
1W19	91	N40	· 2+1 INPUT DATA
1W19	92	N40 2	2 + 2 INPUT DATA
1W19	93	N40 2	2+3 INPUT DATA
1419	94	N41	2+4 INPUT DATA
1W19	95	N41	2.5 INPUT DATA
1W19	96	N41 2	그 말이지 않았는지요. 일정되었
1W19	97	N41 2	
1W19	9.6	N42	298 INPUT DATA
1W19	99	N42	
1W19	900	N42 2	그는 없어 하다 하면 되었다. 전혀 있다.
1W19	901	N42 2	2+11 INPUT DATA
	902	N39 2	
1W19	-		
1W19	903	N39 1	
1W19	904	N39 1	The state of the s
1W19	905	N39	EMPTY
1W19	906	N40 :	CLOCK (100NSEC)
1W19	907	N41	GLOCK (1USEC)
1W19	908		

40 5 40 11 40 28 40 24 41 5 41 11 41 28	2+0 OUTPUT DATA 2+1 OUTPUT DATA 2+2 OUTPUT DATA 2+3 OUTPUT DATA 2+4 OUTPUT DATA 2+5 OUTPUT DATA 2+5 OUTPUT DATA
40 28 40 24 41 5 41 11	2+2 OUTPUT DATA 2+3 OUTPUT DATA 2+4 OUTPUT DATA 2+5 OUTPUT DATA 2+5 OUTPUT DATA
40 24 41 5 41 11	2+3 OUTPUT DATA 2+4 OUTPUT DATA 2+5 OUTPUT DATA
41 5 41 11	2+4 OUTPUT DATA 2+5 OUTPUT DATA
41 11	2+5 QUTPUT DATA
	- Lore - Delet Dit - Building
41 28	O. A. Allimbile Miles
	2+6 OUTPUT DATA
41 24	2+7 OUTPUT DATA
42 5	2+8 QUTPUT DATA
42 11	2+9 OUTPUT DATA
42 28	2+10 OUTPUT DATA
42 24	2+11 OUTRUT DATA
39 22	ACTIVE
39 18	INACTIVE
39 13	FULL
39 9	EMPTY
39 3	FUNCTION
42 1	MASTER :CLEAR
1 1 1 1 1 1 1 1 1 1 1 1 1	42 11 42 28 42 24 39 22 39 18 39 13 39 9

```
CHANNEL 4
1W21 00
   1W21
             90
                  036
                             2+0 INPUT DATA
                         6
             91
   1W21
                  036
                             2+1 INPUT DATA
                            2+2 INPUT DATA
2+3 INPUT DATA
2+4 INPUT DATA
2+5 INPUT DATA
2+5 INPUT DATA
2+6 INPUT DATA
   1W21
             92
                  036 23
             93
                  036 27
   1W21
   1W21
             94
                  037
             95
                  037
   1W21
                  037
             96
   1W21
                  037
                             2+7 INPUT DATA
   1W21
             97
                       27
             98
                  038
                             2+8 INPUT DATA
   1W21
                             2+9 INPUT DATA

2+10 INPUT DATA

2+11 INPUT DATA

ACTIVE

INACTIVE
   1W21
             99
                  038
                  038
   1W21
           900
                  038 27
   1W21
           901
                  035 25
   1421
           902
                  035
           903
   1W21
                       14
   1W21
1W21
                             FULL
EMPTY
           904
                  035
                       15
           905
                  035
                             CLOCK (100NSEC)
   1W21
           906
                  036
                         1
                             CLOCK (1USEC)
   1W21
           907
                  037
                         1
           908
   1W21
```

```
CHANNEL 4
1W22
         00
1W22
         90
             036
                   - 5
                        2+0
                            OUTPUT DATA
                        2+1 OUTPUT DATA
             036 11
1422
        91
                            OUTPUT DATA
        92
                        2+2
             036 28
1W22
1W22
                  24
                        2+3 OUTPUT DATA
             036
                        2+4 OUTPUT DATA
2+5 OUTPUT DATA
             037
1W22
         94
                   5
         95
             037
1W22
                  11
                       2+6 OUTPUT DATA
2+7 OUTPUT DATA
                  28
1W22
        96
             037
        97
             037 24
1W22
             038
                        2+8 OUTPUT DATA
1W22
        98
                   5
                        2+9 OUTPUT DATA
        99
             038
1W22
                  11
                       2+10 OUTPUT DATA
2+11 OUTPUT DATA
             038 28
       900
1W22
1W22
       901
             038
                  24
                        ACTIVE
INACTIVE
             035 22
       902
1W22
1W22
       903
             035
                  18
             035 13
                       FULL
       904
1W22
       905
             035
                       EMPTY
1W22
                    9
                       FUNCTION
1W22
       906
             035
                   3
                        MASTER CLEAR
             038
       907
1W22
1W22
       908
```

23	0.0	are with early stars with read cars and midd agen diss are	CHANNEL'S
LW23	90	040 6	22+0 INPUT DATA
1W23	91	040 4	2+1 INPUT DATA
1W23	92	040 23	242 ÎNPUT DÂTÂ
1W23	93	040 27	2#3 INPUT DATA
LW23	94	041 6	2+4 INPUT DATA
LW23	95	041 4	2-5 INPUT DATA
LW23	96	041 23	246 ÎNPUT DATA
LW23	97	041 27	2W7 INPUT DATA
LW23	98	042 6	2+8 INPUT DATA
LW23	99	042 4	2+9 INPUT DATA
LW23	900	042 23	2+10 INPUT DATA
LW23	901	042 27	2+11 INPUT DATA
W23	902	039 25	ACTÎVE
W23	903	039 14	INACTIVE
W23	904	039 15	FULL
W23	905	039 8	EMPTY
W23	906	040 1	CLOCK (100NSEC)
W23	907	041 1	CLOCK (1US)
W23	908		

LW24	00			CHANNEL 5
LW24	90	040	:5	2+0 OUTPUT DATA
LW24	91	040	11	2+1 OUTPUT DATA
LW24	92	040	28	2+2 OUTPUT DATA
W24	93	040	24	2+3 OUTPUT DATA
W24	94	041	5	2+4 OUTPUT DATA
W24	95	041	11	2+5 OUTPUT DATA
W24	96	041	28	2.6 OUTPUT DATA
W24	97	041	24	2+7 OUTPUT DATA
W24	98	042	. 5	2+8 OUTPUT DATA
W24	99	042	11	2+9 OUTPUT DATA
W24	900	042	28	2+10 OÙTPUT DATA
420	901	042	24	2+11 OUTPUT DATA
m24	902	039	22	ACTIVE
W24	903	039	18	INACTIVE
W24	904	039 :	13	FULL
W24	905	039	9	EMPTY
W24	906	039	3	FUNCTION
W24	907	042	1	MASTER CLEAR
W24	908			

```
CHANNEL 6
1W25 00
        90
                     2+0 INPUT DATA
            P36
                     2+1 INPUT DATA
        91
 1W25
            P36
 1W25
        92
            P36 23
                     2+2 INPUT DATA
                     2+3 INPUT DATA
 1W25
        93
            P36 27
                     244 INPUT DATA
        94
            P37 6
 1W25
                    2+5 INPUT DATA
2+6 INPUT DATA
        95
            P37
 1W25
            P37
 1W25
        96
                     2+7 INPUT DATA
 1W25
        97
            P37
                27
                    2+6 INPUT DATA
2+9 INPUT DATA
        98
99
            P38
 1W25
            P38
 1W25
                    2+10 INPUT DATA
2+11 INPUT DATA
ACTIVE
 1W25
       900
            P38
                23
 1W25
       901
            P38
                27
 1W25
       902
            P35
                25
 1W25
            P35
                     INACTIVE
       903
                14
            P35
                    FULL
EMPTY
                15
 1W25
       904
       905
            P35
                 8
 1W25
                    CLOCK (100NSEC)
 1W25
       906
            P36
                 1
       907
            P37
 1W25
 1W25
       908
```

```
CHANNEL 6
          00
1W26
                           2+0 OUTPUT DATA
2+1 OUTPUT DATA
2+2 OUTPUT DATA
2+3 OUTPUT DATA
1W26
          90
               P36
                      5
1W26
          91
                P36 11
          92
                P36 28
1W26
          93
                P36
1W26
                     24
                           2+4 OUTPUT DATA
2+5 OUTPUT DATA
1W26
          94
                P37
                      5
                P37
1W26
          95
                     11
                           2+6 OUTPUT DATA
2+7 OUTPUT DATA
2+8 OUTPUT DATA
2+9 OUTPUT DATA
               P37
          96
1W26
                     28
               P37
1W26
          97
                     24
          98
               P38
1W26
                      5
          99
                P38
1W26
                     11
                           2+10 OUTPUT DATA
1W26
        900
                P38
                     28
                P38
                           2+11 OUTPUT DATA
        901
                     24
1W26
                           ACTIVE
INACTIVE
1W26
        902
                P35
                     22
        903
                P35
                     18
1W26
1W26
        904
                P35
                     13
                           FULL
        905
                P35
1W26
                       9
                           EMPTY
                P35
1W26
         906
                       3
                           FUNCTION
1W26
        907
                P38
                           MASTER CLEAR
        908
1W26
```

1W27	0.0	one from Arine Arine from the speed Arine bles, man bons and	CHANNEL 7
1W27	90	P40 6	2+0°INPÚT°DÄŤA
1W27	91	P40 4	2+1 INPUT DATA
1W27	92	P40 23	202 INPUT DATA
1W27	93	P40 27	203 INPUT DATA
1W27	94	P41 6	2+4 INPUT DAŤA
1W27	95	P41 4	2±5- ÎNPUT':DAŤA
1W27	96	P41 23	2+6 INPUT DATA
1W27	97	P41 27	2+7 INPUT DATA
1W27	98	P42 6	2+6 INPUT DATA
1W27	99	P42 4	249 INPUT DATA
1W27	900	P42 23	2+10 INPUT DATA
1W27	901	P42 27	2+11 INPUT DATA
1W27	902	P39 25	ACTIVE
1W27	903	P39 14	INACTIVE
1W27	904	P39 15	FULL
1W27	905	P39 8	EMPTY
1W27	906	P40 1	CLOCK (100NSEC)
1W27	907	P41 1	CLOCK (1US)
1W27	908	_	

1W28	0,0			CHANNEL 7
1W28	90	P40	5	2+0 OUTPUT DATA
1W28	91	P40	11	2+1 OUTPUT DATA
1W28	92	P40	28	2+2 OUTPUT DATA
1W28	93	P40	24	2+3 OUTPUT DATA
1W28	94	P41		2+4 QUTPUT DATA
1W28	95	P41	11	2+5 OUTPUT DATA
1W28	96	P41		2+6 OUTPUT DATA
	97		11 11 11 11 11	்து வெள்ள ஆகு 75 ∰கண்டிரு இடி அன்றத் கூடர் பாரி 76 பி. சி. பாரி பாரி பி. இன்ற பாரி வரியாரிய வரியாரிய வரிய வரிய
1W28		P41	24	247 OUTPUT DATA
1W28	98	P42	5	2+8 OUTPUT DATA
1W28	99	P42	11	2+9 OUTPUT DATA
1W28	900	P42	28	2+10 OÜTPUT DAȚA
1W28	901	P42	24	2+11 OÚTPUT DATA
1W28	902	P39	22	ACTIVE
1W28	903	P39	18	INACTIVE
1W28	904	P39	13	FULL
1W28	905	P39	9	· EMPTY
1W28	906	P39	3	FUNCTION
1W28	907	P42	. 4	MASTER CLEAR
1W28	908		-	WAG LET CHEST
7×50	700			Name and the contract of the c

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00
                      CHANNEL 10
1W29
        90
            Q36
                      2+0
                         INPUT DATA
1W29
1W29
        91
            036
                      2+1 INPUT
                                 DATA
        92
93
                      2+2
2+3
1W29
            036 23
                          INPUT DATA
                          INPUT DATA
1W29
            036
                 27
                         INCUT DATA
INCUT DATA
INCUT DATA
1429
        94
            037
                      2+4
1W29
        95
            037
                      2 * 5
            Q37
                      2+6
1W29
        96
                23
        97
1W29
            037
                      2+7
                          INPUT DATA
                 27
                      2+8 INPUT DATA
        98
1W29
            038
                     2+9 INPUT DATA
2+10 INPUT DATA
2+11 INPUT DATA
        99
1W29
            038
1W29
       900
            038
                 27
1W29
       901
            038
                      ACTIVE
INACTIVE
1W29
       902
            035
                25
            035
1W29
       903
                 14
            Q35
                      FULL
1W29
       904
                15
                      EMPTY
1W29
       905
            Q35
                  8
                      CLOCK (100NSEC)
1W29
       906
            036
1W29
       907
            Q37
                      CLOCK (1US)
1W29
       908
```

```
CHANNEL 10
1W30
1W30
         90
              036
                    5
                         2+0 OUTPUT DATA
                         2+1 OUTPUT DATA
1W30
         91
              Q36 11
                        2+2 OUTPUT DATA
         92
              Q36 28
1W30
                        2+3 OUTPUT DATA
2+4 OUTPUT DATA
2+5 OUTPUT DATA
2+6 OUTPUT DATA
2+7 OUTPUT DATA
         93
                   24
1W30
              Q36
1W30
         94
              037
                    - 5
              Q37
         95
1W30
                   11
1W30
         96
              Q37
                   28
              Q37
         97
                   24
1W30
              038
         98
                         2+8 OUTPUT DATA
1W30
                    5
                         2+9 OUTPUT DATA
         99
              Q38
1W30
                   11
              Q38 28
                        2+10 OUTPUT DATA
       900
1W30
                        2+11 OUTPUT DATA
ACTIVE
1W30
       901
902
              038
                   24
              035
                   22
1W30
                         INACTIVE
1W30
        903
              035
                   18
              Q35
       904
                         FULL
1W30
                  13
       905
              035
                    9
                        EMPTY
1W30
                        FUNCTION
1W30
       906
              035
                    3
1W30
       907
              038
                         MASTER CLEAR
1W30
       908
```

1W31	00			OHANNEL 11
1W31	90	Q40	. 6	'2+Ö INÁÚŤ°DÁŤA
1W31	91	040	4	2+1 INPUT DATA
1W31	92	040	23	202 ÎNPUT DATA
1W31	93	040	27	2+3 INPUT DATA
1W31	94	041	:6	244 INPUT DATA
1W31	95	041	4	245 INPUT DATA
1W31	96	041	23	2+6 INPUT DATA
1W31	97	041	27	247 INPUT DATA
1W31	98	042	- A	2+8 INPUT DATA
1W31	99	042		2.9 INPUT DATA
1W31	900	042	23	2-10 INPUT DATA
1W31	901	042		2+11 INPUT DATA
1W31	902	039	25	ACTIVE
1W31	903	039	14	INACTIVE
1W31	904	039	15	FULL
1W31	905	039		ENPTY
1W31	906	040		GLOCK (100US)
1W31	907	041		CLOCK (1US)
1W31	908	w - I	- 4	#PDD# (1200)
TMOT	700			

1W32	0.0		CHANNEL 11	
1W32	90	Q40	S 2+0 OUTPUT DATA	
1W32	91	040 1	2+1 OUTPUT DATA	a sparter
1W32	92	040 2	2 + 2 OUTPUT DATA	
1W32	93	Q40 2	2+3 DUTPUT DATA	
1W32	94	041	S 2+4 DÚŤPUT DÁTA	
1W32	95	Q41 1	2+5 OUTPUT DATA	
1W32	96	041 2	3 2+6 OUTPUT DATA	
1W32	97	041 2	2+7 OUTPUT DATA	
1W32	98	042	S 2+8 OUTPUT DATA	
1W32	99	Q42 1		
1W32	900	042 2	3 2+10 OÙTPUT DAŤA	
1W32	901	042 2	2+11 OUTPUT DATA	
1W32	902	039 2	2 ACŤÍVE	
1W32	903	039 1	NACTIVE	PROFESSION S
1W32	904	039 1	3 FULL	
1W32	905	039	) EMPTY	
1W32	906	Q39	B FUNCTION .	
1W32	907	042	MASTER CLEAR	
1W32	908			
1832	908			

2	1w33	0.0		CHANNEL 12
	1W33	90	R36 6	2+0 INPUT DATA
1	1833	91	R36 4	2+1 INPUT DATA
	1833	92	R36 23	2+2 INPUT DATA
	1833	93	R36 27	2+3 INPUT DATA
	1W33	94	R37 6	2+4 INPUT DATA
	1W33	95	R37 4	2+5 INPUT DATA
		-		- 일 위한 - 급한된 후표 - 일 단순인
	1W33	96	R37 23	2+6 INPUT DATA
	1W33	97	R37 27	2+7 INPUT DATA
	1W33	98	R38 6	2+6 INPUT DATA
	1W33	99	R38 4	2849 INPUT DATA
	1433	900	R38 23	22+10 INPUT DATA
	1W33	901	R38 27	2+11 INPUT DATA
	1433	902	R35 25	ACTIVE .
	1833	903	R35 14	INACTIVE
	1833	904	R35 15	·Füll:
	1833	905	R35 8	EMPTY
	1W33	906	R36 1	CLOCK (100 NSEC)
	1W33	907	R37 1	CLOCK (1US)
	1W33	908	RO/ 1	APORE (IND)
	7.400	700	and the second second second second second	

1 W 3 4 9 0 1 W 3 4 9 1 1 W 3 4 9 2 1 W 3 4 9 3 1 W 3 4 9 5 1 W 3 4 9 6 1 W 3 4 9 8 1 W 3 4 9 8 1 W 3 4 9 8 1 W 3 4 9 9	R36 R36 R36 R37 R37 R37	5 11 28 24 5 11 28 24	2+0 OUTPUT DATA 2+1 OUTPUT DATA 2+2 OUTPUT DATA 2+3 OUTPUT DATA 2+4 OUTPUT DATA 2+5 OUTPUT DATA 2+6 OUTPUT DATA 2+6 OUTPUT DATA 2+7 OUTPUT DATA
1W34 92 1W34 93 1W34 94 1W34 95 1W34 96 1W34 97 1W34 98	R36 R36 R37 R37 R37	28 24 5 11 28 24	2+2 OUTPUT DATA 2+3 OUTPUT DATA 2+4 OUTPUT DATA 2+5 OUTPUT DATA 2+6 OUTPUT DATA 2+7 OUTPUT DATA
1W34 92 1W34 93 1W34 94 1W34 95 1W34 96 1W34 97 1W34 98	R36 R36 R37 R37 R37	24 5 11 28 24	2+2 OUTPUT DATA 2+3 OUTPUT DATA 2+4 OUTPUT DATA 2+5 OUTPUT DATA 2+6 OUTPUT DATA 2+7 OUTPUT DATA
1W34 93 1W34 94 1W34 95 1W34 96 1W34 97 1W34 98	R36 R37 R37 R37	24 5 11 28 24	2+3 OUTPUT DATA 2+4 OUTPUT DATA 2+5 OUTPUT DATA 2+6 OUTPUT DATA 2+7 OUTPUT DATA
1W34 94 1W34 95 1W34 96 1W34 97 1W34 98	R37 R37 R37	5 11 28 24	2+4 OUTPUT DATA 2+5 OUTPUT DATA 2+6 OUTPUT DATA 2+7 OUTPUT DATA
1W34 95 1W34 96 1W34 97 1W34 98	R37 R37 R37	11 28 24	2+5 OUTPUT DATA 2+6 OUTPUT DATA 2+7 OUTPUT DATA
1W34 96 1W34 97 1W34 98	R37	28 24	2+6 OUTPUT DATA 2+7 OUTPUT DATA
1W34 97 1W34 98	R37	24	2+7 OUTPUT DATA
1W34 98			
	N K 30		
		. 5	2+8 OUTPUT DATA
		11	2+9 OUTPUT DATA
1W34 900	1 1 75	28	2+10 OUTPUT DATA
1W34 901	. R38	24	2+11 OUTPUT DATA
1W34 902	R35	22	ACTIVE
1W34 903	R35	18	INACTIVE
1W34 904	R35	13	FULL
1W34 905	A35	9	· · · · · · · · · · · · · · · · · · ·
1W34 906	R35	3	FUNCTION
1W34 907		4	MASTER CLEAR
1W34 908		*	

1	W35	0.0			CHANNEL 13
1	.W35	90	R40	. 6	2+0 INPUT DATA
.1	.W35	91	R40	4.	2a1 INPUT DATA
. 1	W35	92	R40	23	2w2 INPUT DATA
1	W35	93	R40	27	2+3 INPUT DATA
	W35	94	R41	- 6	2+4 INPUT DATA
	W35	95	R41	4	2+5 INPUT DATA
	W35	96	R41		206 INPUT DATA
	W35	97	R41		2#7 INPUT DATA
	W35	98	R42		256 ÎNPUT: DÂTÂ
	W35	99	R42		209 INPUT DATA
-	W35	900	R42		2+10 INPUT DATA
	W35	901	R42		2011 INPUT DATA
	W35	902	R39		ACTIVE
	W35	903	R39		INACTIVE
-					= ₹1₽1
	W35	904	R39		· PULL:
-	W35	905	R39		EMPTY
	W35	906	R40		CLOCK (100NS)
-	W35	907	R41	1	CLOCK (1US)
1	W35	908			

1W36	00		AT 25 MAY 100 PAR 100 A	CHANNEL 13	
1W36	90	R40	5	2+0 OUTPUT DATA	
1W36	91	R40	11	2+1 OUTPUT DATA	and projection or many supports to
1W36	92	R40	28	2+2 OŬŤPUŤ DÃŤA	
1W36	93	R40	24	2+3 OUTPUT DATA	The same state of the same state of
1W36	94	R41	- 6	2+4 OUTPUT DATA	
1W36	95	R41	11	2.5 OUTPUT DATA	* *
1W36	96	R41		2+6 OUTPUT DATA	
1W36	97	R41		2+7 OUTPUT DATA	10 West 10 10 10
1W36	98	R42	- 5	2+8 OUTPUT DATA	
1W36	99			2.9 OUTPUT DATA	
1W36	900	R42		2+10 OUTPUT DATA	
1W36	901	R42		2+11-0UTPUT DATA	
1W36	902	R39	22	ACTIVE	
1W36	903	R39	18	INACTIVE	
1W36	904	R39	13	<u>FULL</u>	
1W36	905	R39	9	EMPTY	
1W36	906	R39	3	FUNCTION	
1W36	907	R42	1	MASTER : CLEAR	
1W36	908			NO ANTIGORNATION CONTINUES OF A TOTAL PROPERTY AND A STREET AND A STRE	

1W37 CABLE 5 DEAD START PANEL

CABLE	PAIR	COLOR	DEST.		SOURCE
1W37	1	0	1129-GND	SO1-ON	S01-1
1W37	_	9	1129-GND	SO1-ON	S01-1
1W37	2	2	1129-16	S01-OFF	S01-2
1W37	_	9			
1W37	3	4	1129-2	SO2-SWEEP	S02-1
1W37		9			
1W37	4	5	1130-GND	S02-LOAD	S02-2
1W37		9	1I30-GND	S02-LOAD	S02-2
1W37	5	6	1129-4	S02-DUMP	S02-3
1W37		9			
1W37	6	90	1 +6V	S03-HIGH	S03-1
1W37		9	1 +6V	S03-HIGH	S03-1
1W37	7	91	1G10-16	S01-NORMAL	S03-2
1W37		9			
1W37	8	92	1130-GND	S03-LOW	S03-3
1W37		9	1130-GND	S03-LOW	S03-3
1W37	9	93	1 +6V	S04-HIGH	S04-1
1W37		9	1 +6V	S04-HIGH	S04-1
1W37	10	94	1H2 <b>9-</b> 26	S04-NORMAL	S04-2
1W37		9	24	S04-NORMAL	S04-2
1W37	11	95	GND	S04-LOW	S04-3
1W37	1.0	9	GND	S04-LOW	S04-3
1W37	12	96			
1W37	1.0	9			
1W37	13	97			
1W37	1 /	9 98			
1W37 1W37	14	90			
1W37	15	900			
1W37	13	9			
1W37	16	910			
1W37	10	9			
1W37	17	92Ó			
1W37		9			
1W37	18	930			
1W37		9			
1W37	19	940			
1W37		9			
1W37	20	950			
1W37		9			
1W37	21	960			
1W37		9			
1W37	22	970			
1W37		9			
1W37	23	980			
1W37		9			
1W37	24	990			
1W37		9			

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W01	0.0			MEMORY DISTRIBUTION FROM PERIPHERAL	1W08	00		
W01	90	B 0 9	5	2+48 PERIPHERAL TO CENTRAL DATA	1W08	90	J33	8
W01	91	B09	7	2+49 PERIPHERAL TO CENTRAL DATA	1W08	91	J33	6
WO1	92	B 0 9	10	2+50 PERIPHERAL TO CENTRAL DATA	1W08	92	<b>J33</b>	4
W 0 1	93	B09	21	2+51 PERIPHERAL TO CENTRAL DATA	1W08	93	J33	27
W 0.1	94	B09	24	2+52 PERIPHERAL TO CENTRAL DATA	1W08	94	133	25
W 0 1	95	B 0 9	26	2+53 PERIPHERAL TO CENTRAL DATA	1W08	95	J33	23
WO1	96	B10	5	2+54 PERIPHERAL TO CENTRAL DATA	1W08	96	J34	8
W01	97	<b>B10</b>	7	2+55 PERIPHERAL TO CENTRAL DATA	1408	97	J34	6
W 0 1	98	B10	10	2+56 PERIPHERAL TO CENTRAL DATA	1W08	98	J34	4
W 0 1	99	<b>B</b> 10	21	2+57 PERIPHERAL TO CENTRAL DATA	1408	99	J34	27
WO1	900	B10	24	2+58 PERIPHERAL TO CENTRAL DATA	1408	900	J34	25
W01	901	<b>B1</b> 0	26	2+59 PERIPHERAL TO CENTRAL DATA	1W08	901	J34	23
W01	902	B07	:5	2+36 PERIPHERAL TO CENTRAL DATA	1008	902	J35	8
WO1	903	B 0 7	7	2+37 PERIPHERAL TO CENTRAL DATA	1008	903	J35	6
W 0 1	904	B07	10	2+38 PERIPHERAL TO CENTRAL DATA	1,008	904	J35	-4
W01	905	C03	8	RESUME CENTRAL WRITE	1W08	905	129	21
W 0 1	906				1w08	906		
W01	907				1w08	907		
W01	908				1w08	908		

2W02	0 0			MEMO	RY DISTRIBU	TIO	N FROM PI	ERIPHERAL	1W09	00_			
2W02	90	B07	21	2+39	PERIPHERAL	70	CENTRAL	DATA	1W09	90	J35	27	
2W02	91	B07	24	2+40	PERIPHERAL	ŤO	CENTRAL	DATÄ	1009	91	J35	25	
2W02	92	B07	26	2+41	PERIPHERAL	TO	CENTRAL	DATA	1W09	92	J35		
2W02	93	808	5		PERIPHERAL	ŤΟ	CENTRAL	DATA	1009	93	J36	8	
2W02	94	808	7	2+43	PERIPHERAL	TO	CENTRAL	DATA	1409	94	J36	6	
2W02	95	B08	10	2+44	PERIPHERAL	TO	CENTRAL	DATÄ	1W09	95	<b>J</b> 36	4	
2W02	96	B08	21	2+45	PERIPHERAL	TO	CENTRAL	DATA	1W09	96	J36	27	
2W02	97	B08	24	2+46	PERIPHERAL	ŤO	CENTRAL	DATA	1W09	97	J36	25	
2W02	98	808	26	2+47	PERIPHERAL	ŢO	CENTRAL	DATA	1009	98	J36	23	
2W02	99	B 0 5	5	2+24	PERIPHERAL	ŤO	CENTRAL	DATA	1W09	99	J37	8	
2W02	900	B05	7	2+25	PERIPHERAL	TO	CENTRAL	DATA	1W09	900	J37	6	
2W02	901	B05	10	2+26	PER PHERAL	TO	CENTRAL	DATA	1009	901	J37	4	
2W02	902	B05	21	2 * 27	PERIPHERAL	TO	CENTRAL	DATA	1409	902	J37	27	
2W02	903	B05	24	2+28	PERIPHERAL	ŤO	CENTRAL	DATA	1009	903	J37		
2W02	904	B05	26	2 * 29	PER!PHERAL	TO	CENTRAL	DATA	1009	904	J37	23	
2W02	905								1409	905			
2W02	906								1409	906			
2W02	907								1409	907			
2W02	908								1.409	908			

2W03	0.0			MEMORY DISTRIBUTION FROM PERIPHERAL	1W10	00			and the larger to the street of the street o
:2W03	90	B 0 6	.5	2+30 PERIPHERAL TO GENTRAL DATA	1W10	90	J38	- 8	
2W03	91	B06	7	2+31 PERIPHERAL TO CENTRAL DATA	1W10	91	J38	- 6	<u> </u>
2W03	92	B06	10	2+32 PERIPHERAL TO CENTRAL DATA	1W10	92	J38	. 4	
2W03	93	B06	21	2+33 PERIPHERAL TO CENTRAL DATA	1W10	93	J38	27	
2W03	94	806	24	2034 PERIPHERAL TO CENTRAL DATA	1W10	94	J38		
2W03	95	B06	26	2+35 PERIPHERAL TO CENTRAL DATA	1W10	95	J38		
2W03	96	B03	5	2+12 PERIPHERAL TO CENTRAL DATA	1W10	96	J39	- 8	
2W03	97	B03-	7	2-13 PERIPHERAL TO CENTRAL DATA	1W10	97	J39	6	**************
2W03	98	B03	10	2-14 PERIPHERAL TO CENTRAL DATA	1W10	98	J39	-4	
2W03	99	B03	21	2415 PERIPHERAL TO CENTRAL DATA	1W10	99	J39	27	N 400 MW. Mark 2100 1777 1144 1147 1147
2W03	900	B03	24	2+16 PERIPHERAL TO CENTRAL DATA	1W10	900	J39	25	
2W03	901	B03	26	2+17 PERIPHERAL TO CENTRAL DATA	1W10	901	J39	23	
2W03	902	B 0 4	5	2+18 PERIPHERAL TO CENTRAL DATA	1W10	902	J40	Ä	
2W03	903	B04	7	2+19 PERIPHERAL TO CENTRAL DATA	1W10	903	J40	6	processor than some products on the con-
2W03	904	B 0 4	10	2+20 PERIPHERAL TO CENTRAL DATA	1W10	904	J40	4	
2W03	905				1W10	905			
2W03	906				1W10	906			
2W03	907	11 11 11 11 11 11		The second section of the second seco	1W10	907			
2W03	908				1W10	908			

2W04	0.0		MEMORY DISTRIBUTION FROM PERIPHE	RAL 1W11	00	
2W04	90	B04 2	2#21 PERIPHERAL TO CENTRAL DATA	1W11	90	J40 27
2W04	91	B04 2	2+22 PERIPHERAL TO CENTRAL DATA	1W11	91	J40 25
2W04	92	B04	2+23 PERIPHERAL TO CENTRAL DATA	1W11	92	J40 23
2W04	93	B01	2+0 PERIPHERAL TO CENTRAL DATA	1W11	93	J41 8
W04	94	B01	2+1 PERIPHERAL TO CENTRAL DATA		94	J41 6
WO4	95	B01 :	2+2 PERIPHERAL TO CENTRAL DATA	1W11	95	J41 4
2W04	96	801	2+3 PERIPHERAL TO CENTRAL DATA	1W11	96	J41 27
WO4	97	B01	2+4 PERIPHERAL TO CENTRAL DATA	1W11	97	J41 25
W04	98	B01	2+5 PERIPHERAL TO CENTRAL DATA	1W11	98	J41 23
WO4	99	B02	2+6 PERIPHERAL TO CENTRAL DATA	1W11	99	J42 8
WO4	900	B 0 2	2#7 PERIPHERAL TO CENTRAL DATA	1W11	900	J42 6
WÕ4	901	B02	2+8 PERIPHERAL TO CENTRAL DATA	1W11	901	J42 4
2W04	902	B02	2+9 PERIPHERAL TO CENTRAL DATA	1W11	902	J42 27
WO4	903	B02	2+10 PERIPHERAL TO CENTRAL DATA	1W11	903	J42 25
WD4	904	B02	2+11 PERIPHERAL TO CENTRAL DATA		904	J42 23
2W04	905				905	
W04	906				906	
2W04	907		A MANAGE ME CONTROL OF THE CONTROL O		907	
2W04	908			1W11	908	

W05	00		WRITE BUFFER TO DISTRIBUTOR	13W10	0.0		
W05	90	812 8	2+0 DATA TO MEMORY FAN OUT	13W10	90	139	4
W05	91	B12 6	2+1 DATA TO MEMORY FAN OUT	13W10	91	139	6
W05	92	B12 4	2+2 DATA TO MEMORY FAN OUT	13W10	92	139	7
WOS	93	B12 27	2+3 DATA TO MEMORY FAN OUT	13W10	93		27
WQ5	94	B12 25	2+4 DATA TO MEMORY FAN OUT	13W10	94	139	25
W05	95	B12 23	2+5 DATA TO MEMORY FAN OUT	13W10	95	139	22
WO5	96	813 8	2+6 DATA TO MEMORY FAN OUT	13W10	96	140	4
W 0 5	97	B13 6	2+7 DATA TO MEMORY FAN OUT	13W10	97	140	6
W Q 5	98	B13 4	2+8 DATA TO MEMORY FAN OUT	13W10	98	140	7
W 0 5	99	813 27	249 DATA TO MEMORY FAN OUT	13W10	99	140	27
W05	900	B13 25	2+10 DATA TO MEMORY FAN OUT	13W10	900	140	25
W 0 5	901	B13 23	2+11 DATA TO MEMORY FAN OUT	13W10	901	140	22
W 0 5	902	B14 8	2+12 DATA TO MEMORY FAN OUT	13W10	902	141	4
W05	903	B14 6	2+13 DATA TO MEMORY FAN OUT	13W10	903	141	6
W 0 5	904	B14 4	2+14 DATA TO MEMORY FAN OUT	13W10	904	141	7
W 05	905	B22 22	MEMORY MARGIN	13W10	905	C14	14
W 0 5	906			13W10	906		
W05	907			13W10	907		
W05	908			13W10	908		

				Hatic Buffer to Block tolledo	4.4114.6	8.0	
2W06	00			WRITE BUFFER TO DISTRIBUTOR	14W10	0.0	
2W06	90	B14	27	2+15 DATA TO MEMORY FAN OUT	14W10	90	102 4
2W06	91	B14	25	2+16 DATA TO MEMORY FAN OUT	14W10	91	102 6
2W06	92	B14	23	2+17 DATA TO MEMORY FAN OUT	14W10	92	102 7
2W06	93	B15	8	2+18 DATA TO MEMORY FAN OUT	14W10	93	102 27
2W06	94	B15	6	2+19 DATA TO MEMORY FAN OUT	14W10	94	102 25
2W06	95	B15	4	2+20 DATA TO MEMORY FAN OUT	14W10	95	102 22
2W06	96	B15	27	2+21 DATA TO MEMORY FAN OUT	14W10	96	103 4
2W06	97	B15	25	2+22 DATA TO MEMORY FAN OUT	14W10	97	103 6
2W06	98	B15	23	2+23 DATA TO MEMORY FAN OUT	14W10	98	103 7
2W06	99	<b>B</b> 16	8	2+24 DATA TO MEMORY FAN OUT	14W10	99	103 27
2W06	900	<b>B</b> 16	6	2#25 DATA TO MEMORY FAN OUT	14W10	900	103 25
2W06	901	B16	4	2+26 DATA TO MEMORY FAN OUT	14W10	901	103 22
2W06	902	B16	27	2+27 DATA TO MEMORY FAN OUT	14W10	962	104 4
2W06	903	<b>B</b> 16	25	2+28 DATA TO MEMORY FAN OUT	14W10	903	104 6
2W06	904	B16	23	2#29 DATA TO MEMORY FAN OUT	14W10	904	104 7
2W06	905	B22	20	MEMORY MARGIN	14W10	905	C14 14
2W06	906				14W10	906	
2W06	907				14W10	907	
2W06	908			and the second second	14W10	908	and which will be a series of the

2W07	0.0	ng drips agus agus agus agus dinn men arap eura anu binn n	WRITE BUFFER TO DISTRIBUTOR	15W10 00	'r
2W07	90	817 8	2430 DATA TO MEMORY FAN OUT	15W10 90 139 4	
2W07	91	B17 6	2+31 DATA TO MEMORY FAN OUT	15W10 91 139 6	
2W07	92	817 4	2+32 DATA TO MEMORY PAN OUT	19W10 92 139 7	
2W07	93	B17 27	2633 DATA TO MEMORY FAN OUT	15W10 93 139 27	
2W07	94	B17 25	2434 DATA TO MEMORY FAN OUT	15W10 94 139 25	
2W07	95	B17 23	2#35 DATA TO MEMORY FAN OUT	15W10 95 139 22	
2W07	96	818 8	2+36 DATA TO MEMORY FAN OUT	15W10 96 140 4	
2W07	97	B18 6	2-37 DATA TO MEMORY FAN OUT	15W10 97 140 6	
2W07	98	818 4	2.38 DATA TO MEMORY FAN OUT	15W10 98 140 7	
2W07	99	818 27	2439 DATA TO MEMORY FAN OUT	15W10 99 140 27	
2W07	900	B18 25	2+40 DATA TO MEMORY PAN OUT	15W10 900 I40 25	
2W07	901	B18 23	2-41 DATA TO MEMORY FAN OUT	15W10 901 140 22	
2W07	902	B19 8	2442 DATA TO MEMORY FAN OUT	15W10 902 I41 4	
2W07	903	819 6	2+43 DATA TO MEMORY FAN OUT	15W10 903 141 6	
2W07	904	B19 4	2544 DATA TO MEMORY FAN OUT	15W10 904 141 7	
2W07	905	B22 18	MEMORY MARGIN	19W10 905 C14 14	
2W07	906			15W10 906	
2W07	907			15W10 907	
2W07	908			15W10 908	

2W08	00		WRITE BUFFER TO DISTRIBUTOR	16W10 00
2W08	90	B19 27	2+45 DATA TO MEMORY FAN OUT	16W10 90 102 4
2W08	91	B19 25	2+46 DATA TO MEMORY PAN OUT	16W10 91 102 6
2W08	92	B19 23	2+47 DATA TO MEMORY FAN OUT	16W10 92 102 7
2W08	93	B20 8	2+48 DATA TO MEMORY FAN OUT	16W10 93 I02 27
2W08	94	B20 6	2+49 DATA TO MEMORY FAN OUT	16W10 94 I02 25
2 W O 8	95	820 4	2+50 DATA TO MEMORY FAN OUT	16W10 95 102 22
2 W O 8	96	B20 27	2451 DATA TO MEMORY FAN OUT	16W10 96 103 4
WOB	97	B20 25	2+52 DATA TO MEMORY FAN OUT	16W10 97 IO3 6
WO8	98	B20 23	2+53 DATA TO MEMORY FAN OUT	16W10 98 I03 7
WOB	99	B21 8	2+54 DATA TO MEMORY FAN OUT	16W10 99 I03 27
WO8	900	B21 6	2+55 DATA TO MEMORY FAN OUT	16W10 900 I03 25
WOB	901	B21 4	2+56 DATA TO MEMORY FAN OUT	16W10 901 103 22
2W08	902	B21 27	2457 DATA TO MEMORY FAN OUT	16W10 902 104 4
2 W 0 B	903	B21 25	2.58 DATA TO MEMORY FAN OUT	16W10 903 104 6
8 0 W S	904	B21 23	2+59 DATA TO MEMORY FAN OUT	16W10 904 I04 7
WO8	905	B22 16	MEMORY MARGIN	16W10 905 C14 14
80W	906			16W10 906
SWOB	907		A COLUMN TO THE SECOND SECURITION OF THE SECOND SEC	16W10 907
2W08	908			16W10 908

2W09	00			MEMORY DISTRIBUTION FROM LOWER REGISTER	7W08	00	ern 1996 ands anno 1888 quae anno 1890 anno 1800 ann
2W09	90	A 0 9	10	DATA 248 REG. TO MEM.	7W08	90	F28 21
2W09	91	A09	21	DATA 2+9 REG. TO MEM.	7W08	91	F29 3
2W09	92	A09	24	DATA 2+10 REG. TO MEM.	7W08	92	F29 25
2W09	93	A09	26	DATA 2+11 REG. TO MEM.	7W08	93	F29 21
2W09	94	A10	5	DATA 2+12 REG. TO MEM.	7W08	94	F30 3
2W09	95	A10	7	DATA 2+13 REG. TO MEM.	7 W D 8	95	F30 25
2W09	96	A10	10	DATA 2+14 REG. TO MEM.	7W08	96	F30 21
2W09	97	A10	21	DATA 2+15 REG. TO MEM.	7W08	97	F31 3
2 W D 9	98	A10	24	DATA 2+16 REG. TO MEM.	7W08	98	F31 25
2W09	99	A10	26	DATA 2+17 REG. TO MEM.	7W08	99	F31 21
2W09	900	A 0 8	5	DATA 2+0 REG. TO MEM.	7W08	900	F26 3
2W09	901	A 0 8	7	DATA 2+1 REG. TO MEM.	7W08	901	F26 25
2W09	902	A () 8	10	DATA 2+2 REG. TO MEM.	7W08	902	F26 21
2W09	903	AD8	21	DATA 2+3 REG. TO MEM.	7W08	903	F27 3
2W09	904	A () 8	24	DATA 2+4 REG. TO MEM.	7W08	904	F27 25
2W09	905	A D B	26	DATA 2+5 REG. TO MEM.	7W08	905	F27 21
2W09	906	A09	- 5	DATA 2+6 REG. TO MEM.	7W08	906	F28 3
2W09	907	A 0 9	· 7	DATA 247 REG. TO MEM.	7W08	907	F28 25
W09	908				7W08	908	

2W10	0 0			MEMORY D	STRÍB	ÙŢI	ON FROM LOWER REGISTER	7W09	0.0		
2W10	90	A12	10	DATA 2+26	REG.	TO	MEM.	7W09	90	F34	21
2W10	91	A12	21	DATA 2+27	REG.	TO	MEM.	7409	91	F35	3
2W10	92	A12		DATA 2+28				7W09	92	F35	25
2W10	93	A12	26	DATA 2+29	REG.			7W09	93	F35	21
2W10	94	A13	5	DATA 2+30		TO	MEM.	7W09	94	E26	3
2W10	95	A13	7	DATA 2+31		ŤO		7W09	95	E26	25
2W10	96	A13	10	DATA 2+32		ŤÕ	· · · · · · · · · · · · · · · · · · ·	7W09	96		21
2W10	97	A13		DATA 2+33		TO		7W09	97		3
2W10	98	A13	24	DATA 2+34		TO		7W09	98	_	25
2W10	99	A13	26	DATA 2+35				7W09	99		21
2W10	900	A11	5	DATA 2+18				7W09	900		3
2W10	901	A11	7	DATA 2+19			MEM.	7W09	901	F32	
2W10	902	A11	10	DATA 2+20		TO	MEM.	7W09	902	F32	
2W10	903	A11	1 1797 1807 1	DATA 2+21		TO	MEM.	7W09	903	F33	3
2W10	904	A11	24	DATA 2+22		TO		7W09	904		25
2W10	905	A11	26	DATA 2423		TO	MEM.	7W09	905		21
2W10	906	A12	- 5	DATA 2+24		TO	MEM,	7W09	906	F34	3
2W10	907	A12	7	DATA 2+25			MEM.	7W09	907	F34	-
2W10	908		•				•	7W09	908		-

2W11	0.0		MEMORY DISTRIBUTION FROM UPPER REGISTE	R 8W10	0.0	and their field and and any fine was put year may may have also been seen as a first of the second
2W11	90	A14:1	2#38 REGISTER TO DISTRIBUTOR	:8W10	90	A09 25
2W11	91	A14 2	2+39 REGISTER TO DISTRIBUTOR	8W10	91	A10 3
2W11	92	A14-2	2+40 REGISTER TO DISTRIBUTOR	8W10	92	A10 21
2W11	93	A14 2	2.41 REGISTER TO DISTRIBUTOR	8W10	93	A10 25
2W11	94	A15	2+42 REGISTER TO DISTRIBUTOR	8W10	94	A11 3
2W11	95	A15	2+43 REGISTER TO DISTRIBUTOR	8W10	95	A11 21
2W11	96	A15 1		8W10	96	A11 25
2W11	97			8W10	97	
2W11	98			8W10	98	
2W11	99			8W10	99	
2W11	900			8W10	900	
2W11	901			8W10	901	
2W11	902			8W10	902	
2W11	903			8W10	903	
	904			8W10	904	
2W11				8W10	905	
2W11	905	444	G. T. AGAIGTER TA DISTRIPTOR			400 7
2W11	906	A14	2+36 REGISTER TO DISTRIBUTOR	8W10	906	A09 3
2W11	907	A14	2+37 REGISTER TO DISTRIBUTOR	8W10	907	A09 21
2W11	908			-8W10	908	

2W12	0.0		MEMORY DISTRIBUTION FROM UPPER REGISTER	8W11	00		
2W12	90	A16 26	2+53 REGISTER TO DISTRIBUTOR	8W11	90	810 25	
2W12	91	A17 5	2+54 REGISTER TO DISTRIBUTOR	8W11	91	B11 3	tering of a term of the special contractions.
2W12	92	A17 7	2+55 REGISTER TO DISTRIBUTOR	8W11	92	B11 21	
2W12	93	A17 10	2.56 REGISTER TO DISTRIBUTOR	8W11	93	B11 25	tion on the second second second
2W12	94	A17 21	2+57 REGISTER TO DISTRIBUTOR	8W11	94	B12 3	
2W12	95	A17 24	2.58 REGISTER TO DISTRIBUTOR	8W11	95	B12 21	
2W12	96	A17 26	2.59 REGISTER TO DISTRIBUTOR	8W11	96	B12 25	
2W12	97		AND THE RESERVE OF A STATE OF THE PROPERTY OF	8W11	97		Autoria de Calculo III de Calculo
2W12	98			8W11	98		
2W12	99			8W11	99		
2W12	900	A15 21	2+45 REGISTER TO DISTRIBUTOR	8W11	900	A12 3	
2W12	901	A15 24	2+46 REGISTER TO DISTRIBUTOR	8W11	901	A12 21	
2W12	902	A15 26	2+47 REGISTER TO DISTRIBUTOR	8W11	902	A12 25	
2W12	903	A16 5	2.48 REGISTER TO DISTRIBUTOR	8W11	903	809 3	er prominent and
2W12	904	A16 7	2+49 REGISTER TO DISTRIBUTOR	8W11	904	B09 21	
2W12	905	A16 10	2450 REGISTER TO DISTRIBUTOR	8W11	905	809 25	
2W12	906	A16 21	2+51 REGISTER TO DISTRIBUTOR	8W11	906	B <sub>1</sub> 0 3	
2W12	907	A16 24	2.52 REGISTER TO DISTRIBUTOR	8W11	907	B10 21	
	•	~ ~ E4	THE NEGRO IN 10 TO INC.				
2W12	908	Touris Consultation and Matthews Laborations		8W11	908	**************************************	and the second second

The second secon

2W13	00			MEMORY	DISTRI	BUTI	ON FROM	CENTRAL	CONTROL	5W10_	0.0		
2W13	90	A03 :	10	STORE	CONTROL	BIT	2+44			5W10	90	139	4
2W13	91	A03	21	STORE	CONTROL	BIT	2+45	and the second s		5W10	91	139	27
2W13	92	A03	24	STORE	CONTROL	BIT	2+46			5W10	92	139	25
2W13	93	A03	26		CONTROL	BIT	2+47			5W10	93	139	23
2W13	94	A 0.4	5		CONTROL	BIT	2+48			5W10	94	139	21
2W13	95	A 0 4	7	STORE	CONTROL	BIT	2+49			5W10	95	139	19
2W13	96	A04 :	10	STORE	CONTROL	BIT	2+50	100 - 11000 100 100 1		5W10	96	140	4
2W13	97	A04	21	STORE	CONTROL	BIT	2+51			5W10	97	I 40	27
2W13	98	A04 1	24	STORE	CONTROL	BIT	2+52			5W10	98	140	23
2W13	99	A04 2	26	STORE	CONTROL	BIT	2 * 5 3			5W10	99	140	23
2W13	900	A 0 3	7	STORE	CONTROL	BIT	2+43			5W10	900	139	6
2W13	901	A 0 2	5	STORE	CONTROL	BIT	2+36			5W10	901	138	25
2W13	902	A 0 2	7	STORE	CONTROL	BIT	2+37			5W10	902	138	23
2W13	903	A02 :	10	STORE	CONTROL	BIT	2+38			5W10	903	138	21
2W13	904	A 0 2	21	STORE	CONTROL	BIT	2*39			5W10	904	138	19
2W13	905	A02 2	24	STORE	CONTROL	BIT	2+40			5W10	905	139	12
2W13	906	A02 2	26	STORE	CONTROL	BIT	2+41			5W10	906	139	10
2W13	907	A 0 3	5	STORE	CONTROL	BIT	2+42		•	5W10	907	139	8
2W13	908	-								5W10	908		

2W14	0.0			MEMORY	DISTRIBUTION FROM	CENTRAL	CONTROL	5W27	0.0		
2W14	90	A01	-5	2+30				5W27	90	138	12
2W14	91	A01	7	2+31	and the second s			5W27	91	138	10
2W14	92	A01	10	2+32				5W27	92	138	8
2W14	93	A01	21	2+33				5W27	93	138	6
W14	94	A01		2+34				5W27	94	138	4
W14	95	A01	26	2+35	4			5W27	95	138	
W14	96	A 11 5		2+56				5W27	96	140	
W14	97		¥	= = = •			14.4	5w27	97	W L T	
W14	98							5W27	98		
W14	99							5W27	99		
W14	900							5w27	900		
W14	901	122	12	CLOCK				5W27	901	J39	8
W14	902	A07	7		CONTROL			5W27	902	K38	23
W14	903	A07	16		EGISTERS			5W27	903	K38	25
W14	904	A07	5		PERIPHERAL			5W27	904	K38	27
W14	905	E01	25		777 ERROR			5W27	905	N23	10
_					1777 ERROR			5W27	906	N23	7
2W14	906	E01	£ 3	EXP -	D/// ERNUN			5W27	907	1420	′
2W14 2W14	907 908							9W27	908		

2W15 CABLE 1 DEAD START PANEL

CABLE	PAIR	COLOR	DEST.			SOURCE
2W15 2W15 2W15 2W15	1 2	0 9 2 9	2C21- 6 5 7 22	Row 1	Bit 0 1 2 3	0001 A B C D
2W15 2W15	3	4 9	24 23		4 5	E F
2W15 2W15	4	5	2C22- 6 5		6 7	G H
2W15 2W15	5	6 9	7 22		8 9	I J
2W15 2W15	6	90 9	24 23 ·		10 11	K L
2W15 2W15	7	91 9	2C23- 6 5	Row 2	Bit 0	0002 A B
2W15 2W15	8	92 9	7 22		2 3	C D
2W15 2W15 2W15	9	93 9 94	24 23 2C24- 6		4 5 6	E F G
2W15 2W15	11	9 95	5		7 8	H I
2W15 2W15	12 .	9 96	22 24		9 10	J K
2W15 2W15	13	9 97	23 2C25- 6	Row 3	11 Bit 0	0003 A
2W15 2W15	14	9 98 9	5 7 <b>22</b>		1 2 3	B C
2W15 2W15 2W15	15	900 <b>9</b>	24 24 23		3 4 5	D E F
2W15 2W15	16	910 9	2C26- 6 5		6 7	G H
2W15 2W15	17	920 9	7 22		8	I J
2W15 2W15	18	930 9	24 23		10 11	K L
2W15 2W15	19	940 9	2C27- 6 5	Row 4	Bit 0	0004 A B
2W15 2W15 2W15	20 21	950 9 960	7 22 24		2 3 4	C D E
2W15 2W15 2W15	22	9 970	23 2C28- 6		5 6	F G
2W15 2W15	23	9 980	5 7		7 8	H I
2W15 2W15	24	9 990	22 24		9 10	J K
2W15	•	9	23		11	L

2W16 CABLE 2 DEAD START PANEL

CABLE	PAIR	COLOR	DEST.			SOURCE
2W16	1	0	2C21- 4	Row 5	Bit O	0005 A
2W16		9	3		1	В
2W16	2	2	13		2	С
2W16		9	18		3	D
2W16	3	4	26		4	E
2W16		9	25		5	F
2W16	4	5	2C22- 4		6	G
2W16		9	3		7	Н
2W16	5	6	13		8	I
<b>2W1</b> 6		9	18		9	J
2W16	6	90	26		10	K
<b>2W1</b> 6		9	25		11	L
2W16	7	91	2C23- 4	Row 6	Bit O	0006 A
<b>2</b> W <b>1</b> 6		9	3		1	В
2W16	8	92	13		2	С
2W16		9	18		3	D
<b>2W1</b> 6	9	93	26		4	E
2W16		9	25		5	F
2W16	10	94	2C24- 4		6	G
2W16		9	3		7	H
2W16	11	95	13		8.	I
2W16		9	18		9	J
2W16	12	96	26		10	K
2W16	1.0	9	25	D 7	11	L
2W16	13	97	2C25- 4	Row 7	Bit 0	0007 A
2W16	<b>1</b> /.	9	3 13		1 2	В
2W16	14	98 9	18		3	C D
2W16 2W16	15	900	26		4	E
2W16	1.7	900	25 25		5	F
2W16	16	910	2C26- 4		6	G
2W16	10	9	3		7	H
2W16	17	920	13		8	I
2W16		9	18		9	J
2W16	18	930	<b>2</b> 6		10	K
2W16		9	25		11	L
2W16	19	940	2C27- 4	Row 10	Bit O	0010 A
2W16		9	3		1	В
<b>2W1</b> 6	20	950	13			С
2W16		9	18		2 3	D
<b>2W1</b> 6	21	960	26		4	E
2W16		9	25		5	F
2W16	22	970	2C28- 4		6	G
2W16		9	3		7	Н
2W16	23	980	13		8	I
2W16		9	18		9	J
2W16	24	990	26		10	K
<b>2W1</b> 6		9	25		11	L

2W17 CABLE 3 DEAD START PANEL

CABLE	PAIR	COLOR	DEST.		SOURCE
2W17	1	0	2C21- 2	Row 11 Bit 0	0011 A
2W17		9	1	1	В
2W17	2	2	12	2	С
2W17		9	17	3	D
2W17	3	4	28	4	E
2W17		9	27	5	F
2W17	4	5	2C22- 2	6	G
2W17		9	1	7	Н
2W17	5	6	12	8	I
2W17		9	17	9	J
2W17	6	90	28	10	K
2W17		9	27	11	L
2W17	7	91	2C23- 2	Row 12 Bit 0	0012 A
2W17		9	1	1	В
2W17	8	92	12	2	С
2W17		9	17	3	D
2W17	9	93	28	4	E
2W17		9	27	5	F
2W17	10	94	2C24- 2	6	G
2W17		9	1	7	Н
2W17	11	95	12	8	I
<b>2</b> W17		9	17	9	J
2W17	12	96	28	10	K
2W17		9	27	11	L
2W17	13	97	2C25- 2	Row 13 Bit 0	0013 A
2W17		9	1	1	В
2W17	14	98	12	2	С
2W17		9	17	3	D
2W17	15	900	28	4	E
2W17		9	27	5	F
2W17	16	910	2C26- 2	6	G
2W17		9	1	7	Н
2W17	17	920	12	8	I
2W17	•	9	17	9	J
2W17	18	930	28	10	K
2W17	1.0	9	27	11	L
2W17	19	940	2C27- 2	Row 14 Bit 0	0014 A
2W17	00	9	1	1	В
2W17	20	950	12	2	C
2W17	0.1	9	17	3	D
2W17	21	960	28	4	E
2W17	0.0	9	27	5	F
2W17	22	970	2C28- 2	6	G
2W17	22	9	1	7	H
2W1.7	23	980	12	8	I
2W17	27.	9 990	17		J
2W17	24		28	10	K
2W17		9	27	11	L

2W18 CABLE 4 DEAD START PANEL

CABLE	PAIR	COLOR	DEST.	SOURCE
2W18	1	0	2A01 GND	GND BUS
2W18	_	9		
2W18	2	2	2AO1 GND	
2W18	_	9	ZHOI GND	
2W18	3	4	2AO2 GND	
2W18	,	9	2102 GND	
2W18	4	5	2AO2 GND	
2W18	7	9	ZAOZ GND	
2W18	5	6	2AO3 GND	
2W18	J	9	ZAOS GND	
2W18	6	90	2AO3 GND	
2W18	U	9	ZAOS GND	
2W18	7	91	2AO4 GND	
2W18	,	9	2104 GND	
2W18	8	92	2AO4 GND	
2W18	U	9	21104 GND	
2W18	9	93	2AO5 GND	
2W18	,	9	ZIIOS GND	
2W18	10	94	2AO5 GND	
2W18	10	9	2105 0115	
2W18	11	95	2AO5 GND	
2W18		9	2105 0113	
2W18	12	96	2 +6V	+6V BUS
2W18		9	2	
2W18	13	97	2 +6V	
2W18		9	_	
2W18	14	<b>9</b> 8	2 +6V	
2W18		9	_	
2W18	15	900	2 +6V	
2W18		9		
2W18	16	910	2 +6V	
2W18		9		
2W18	17	920	2 +6V	
2W18		9		
2W18	18	930	2 +6V	
2W18		9		
<b>2W1</b> 8	19	940	2 +6V	
<b>2W1</b> 8		9		
2W18	20	950	2 +6V	
2W18		9		
<b>2W1</b> 8	21	960	2 +6V	
2W18		9		
2W18	22	970	2 +6V	
2W18		9		
2W18	23	980	2B 22-26	S04-2
2W18		9		
2W18	24	990	2B 22-24	S04-2
<b>2W1</b> 8		9		

2W19	00	ery 1877 1882 1882 1882 1882 1884 1884 1884 1884	DEAD START TO CHANNEL (INPUT)
2W19	90	C33: 8	2+0 INPUT DATA
2W19	91	C33 6	2+1 INPUT DATA
2W19	92	C33 4	2+2 ÎNPUT DATA
2W19	93	C33 27	2+3 INPUT DATA
2W19	94	C33 25	2+4 INPUT DATA
2W19	95	C33 23	2+5 INPUT DATA
2W19	96	C34 8	2+6 INPUT DATA
2W19	97	C34 A	2+7 INPUT DATA
2W19	98	C34 4	2+8 INPUT DATA
2W19	99	C34 27	2+9 INPUT DATA
2W19	900	C34 25	2+10 INPUT DATA
2W19	901	C34 23	
2W19	902		ACTIVE
2W19	903	C32 6	INACTIVE
2W19	904	C32: 8	
2W19	905		B M P T Y
2W19	906		
2W19	907		
2W19	908		

2W20 90	2W20	0.0		DEAD START TO CHANNEL (OUTPUT)
2W20 91 2+1 OUTPUT DATA 2W20 93 2+3 OUTPUT DATA 2W20 94 2+4 OUTPUT DATA 2W20 95 2+5 OUTPUT DATA 2W20 96 2+6 OUTPUT DATA 2W20 97 2+7 OUTPUT DATA 2W20 98 2+8 OUTPUT DATA 2W20 99 2+9 OUTPUT DATA 2W20 99 2+9 OUTPUT DATA 2W20 90 2+10 OUTPUT DATA 2W20 901 2+11 OUTPUT DATA 2W20 902 ACTIVE 2W20 903 INACTIVE 2W20 904 FULL 2W20 905 C17 12 EMPTY 2W20 906 FUNCTION	. 25. 12.			
2W20 93 2+3 OUTPUT DATA 2W20 94 2+4 OUTPUT DATA 2W20 95 2+5 OUTPUT DATA 2W20 96 2+6 OUTPUT DATA 2W20 97 2+7 OUTPUT DATA 2W20 98 2+8 OUTPUT DATA 2W20 98 2+8 OUTPUT DATA 2W20 99 2+9 OUTPUT DATA 2W20 99 2+9 OUTPUT DATA 2W20 900 2+10 OUTPUT DATA 2W20 901 2+11 OUTPUT DATA 2W20 902 ACTIVE 2W20 903 INACTIVE 2W20 904 FULL 2W20 905 C17 12 EMPTY 2W20 906 FUNCTION	2W20	90		2+0 OUTPUT DATA
2W20 93 2+3 OUTPUT DATA 2W20 94 2+4 OUTPUT DATA 2W20 95 2+5 OUTPUT DATA 2W20 96 2+6 OUTPUT DATA 2W20 97 2+7 OUTPUT DATA 2W20 98 2+8 OUTPUT DATA 2W20 98 2+8 OUTPUT DATA 2W20 99 2+9 OUTPUT DATA 2W20 99 2+9 OUTPUT DATA 2W20 900 2+10 OUTPUT DATA 2W20 901 2+11 OUTPUT DATA 2W20 902 ACTIVE 2W20 903 INACTIVE 2W20 904 FULL 2W20 905 C17 12 EMPTY 2W20 906 FUNCTION	2W20	91	NOTE YOU BUILDING WHILE STREET	2+1 OUTPUT DATA
2W20 93 2+3 OUTPUT DATA 2W20 94 2+4 OUTPUT DATA 2W20 95 2+5 OUTPUT DATA 2W20 96 2+6 OUTPUT DATA 2W20 97 2+7 OUTPUT DATA 2W20 98 2*8 OUTPUT DATA 2W20 99 2+9 OUTPUT DATA 2W20 99 2+9 OUTPUT DATA 2W20 900 2+10 OUTPUT DATA 2W20 901 2+11 OUTPUT DATA 2W20 902 ACTIVE 2W20 903 INACTIVE 2W20 904 FULL 2W20 905 C17 12 EMPTY 2W20 906 FUNCTION	2W20	92		
2W20 95				and the companies of th
2W20 95	2420	94		2+4 OÚŤPUŤ DÂTA
2W20 97 2+7 OUTPUT DATA 2W20 98 2+8 OUTPUT DATA 2W20 99 2+9 OUTPUT DATA 2W20 900 2+10 OUTPUT DATA 2W20 901 2+11 OUTPUT DATA 2W20 902 ACTIVE 2W20 903 INACTIVE 2W20 904 FULL 2W20 905 C17 12 EMPTY 2W20 906 FUNCTION		the second second second		
2W20 97 2+7 OUTPUT DATA 2W20 98 2+8 OUTPUT DATA 2W20 99 2+9 OUTPUT DATA 2W20 900 2+10 OUTPUT DATA 2W20 901 2+11 OUTPUT DATA 2W20 902 ACTIVE 2W20 903 INACTIVE 2W20 904 FULL 2W20 905 C17 12 EMPTY 2W20 906 FUNCTION	2W20	96		2*6 OÙTPUT DÂTA
2W20 99 2+9 OUTPUT DATA 2W20 900 2+10 OUTPUT DATA 2W20 901 2+11 OUTPUT DATA 2W20 902 ACTIVE 2W20 903 INACTIVE 2W20 904 FULL 2W20 905 C17 12 EMPTY 2W20 906 FUNCTION		97	ar Paring and Anglande in Contract and Anglander States and Anglander Anglan	
2W20 99	2W20	98		2*8 OUTPUT DÄTÄ
2W20 901 2+11 OUTPUT DATA 2W20 902 ACTIVE 2W20 903 INACTIVE 2W20 904 FULL 2W20 905 C17 12 EMPTY 2W20 906 FUNCTION		99		
2W20 902 ACTIVE 2W20 903 INACTIVE 2W20 904 FULL 2W20 905 C17 12 EMPTY 2W20 906 FUNCTION	2W20	900		'2+1g -OÚTPUT DATA
2W20 902 ACTIVE 2W20 903 INACTIVE 2W20 904 FULL 2W20 905 C17 12 EMPTY 2W20 906 FUNCTION	2W20	901		2+11 OUTPUT DATA
2W20 903 INACTIVE 2W20 904 FULL 2W20 905 C17 12 EMPTY 2W20 906 FUNCTION				
2W20 904 FULL 2W20 905 C17 12 EMPTY 2W20 906 FUNCTION	· · · · · · · · · · · · · · · · · · ·	OF THE PERSON NAMED IN COLUMN 1 ISSUED AND ADDRESS OF THE	January Company Company Company Company	A COLUMN TO A COLU
2W20 905 C17 12 EMPTY 2W20 906 FUNCTION				
2W20 906 FUNCTION			C17 19	
			01, 12	
SUUUN ON/ NIA 10 MARTHU CIPAU				
	3M50	907	C16 12	MASTER CLEAR
2W20 908	2W20	908	er om state et a stragenheide en en engangen eine de en en en	

2W21	00			DIVIDE FROM CONTROL	5W13	0.0		
2W21	90				5W13	90		
2W21	91	J02	7	ROUND	5W13	91	E40	12
2W21	92			BOOLEAN	5W13	92		
2W21	93	J01	5	SUM	5W13	93	E38	8
2W21	94	J01	7	DIFFERENCE	5W13_	94	E38	10
2W21	95	J01	10	PRODUCT	5W13	95	E38	
2W21	96	J02	10	POP. COUNT	5W13	96	E40	
2W21	97	J01	24	COMPLEMENT	5W13	97	E38	6
2W21	98	J01	21	TRANSFER	5W13	98	E38	4
2W21	99	L11	18	TRANSMIT BOOLEAN	5W13	99	J40	10
2W21	900	L11	14	TRANSMIT DIVIDE	5W13	900	J40	27
2W21	901			•	5W13	901		
2W21	902	L11	16	GO BOOLEAN	5W13	902	E40	21
2W21	903	L11	12	GO DIVIDE	5W13	903	J40	8
2W21	904			CLEAR	5W13	904		-
2W21	905	E01	8	DIVIDE REQUEST RELEASE	5W13	905	J24	19
2W21	906	E01	6	BOOLEAN REQUEST RELEASE	5W13	906	J23	19
2W21	907	009	7	TRANSMIT MUIT 1	5W13	907	035	10
2W21	908	009	21	TRANSMIT MUIT 2	5W13	908	035	12

2W22	0.0			DIVIDE FROM REGISTER	8W18	0.0	and Milliands and part the sale and the
2W22	90	MOB	12	2+48 X++K (EXPONENT)	8W18	90	D09 3
2W22	91	MOB	14	2+49 X++K (EXPONENT)	8W18	91	D09 21
2W22	92	M 0 8	16	2+50 X++K (EXPONENT)	8W18	92	D09 25
2W22	93	M08	18	2+51 X++K (EXPONENT)	8W18	93	D10 3
2W22	94	M09	12	2+52 X++K (EXPONENT)	8W18	94	D10 21
2W22	95	M09	14	2+53 X++K (EXPONENT)	8W18	95	D10 25
2W22	96	M 0 9	16	2+54 X++K (EXPONENT)	8W18	96	D11 3
2W22	97	M09	18	2+55 X++K (EXPONENT)	8W18	97	D11 21
2W22	98	M10	12	2+56 X++K (EXPONENT)	8W18	98	D11 25
2W22	99	M10	14	2+57 X++K (EXPONENT)	8W18	99	D12 3
2W22	900	M10	16	2+58 X++K (EXPONENT)	8W18	900	D12 21
2W22	901	M10	18	2+59 X++K (EXPONENT)	8W18	901	D12 23
2M25	902	K13	1	2+48 EXPONENT RESULT-REG.	8W18	902	003 2
2W22	903	K13	6	2.49 EXPONENT RESULT-REG.	8W18	903	C03 26
2W22	904	K13	23	2+50 EXPONENT RESULT-REG.	8W18	904	C04 2
2W22	905	K <sub>1</sub> 3	28	2+51 EXPONENT RESULT-REG.	8W18	905	CO4 26
2W22	906	K14	1	2+52 EXPONENT RESULT=REG.	8W18	906	C05 2
2W22	907	K14	6	2+53 EXPONENT RESULT-REG.	8W18	907	C05 26
2W22	908	E 0 2	5	ERROR (EXP = 1777 FROM CHASSIS 7)	8W18	908	H06 19

SM33	0.0		DIVIDE FROM REGISTER	8W19	00	چه محمد محمد الله الله الله الله الله الله الله الل
2W23	90	L08 12	2+48 X++J (EXPONENT)	8W19	90	F09 3
2W23	91	L08 14	2+49 X++J (EXPONENT)	8W19	91	F09 21
2W23	92	L08 16	2+50 X++J (EXPONENT)	8W19	92	F09 25
2W23	93	L08 18	2+51 X++J (EXPONENT)	8W19	93	F10 3
2W23	94	L09 12	a dit i to i la	8W19	94	F10 21
2W23	95	L09 14		8W19	95	F10 25
2W23	96	L09 16	2+54 X++J (EXPONENT)	8W19	96	F11 3
2W23	97	L09 18	2+55 X++J (EXPONENT)	8W19	97	F11 21
2W23	98	L10 12	2+56 X++J (EXPONENT)	8W19	98	F11 25
2W23	99	L10 14		8W19	99	F12 3
2W23	900	L10 16	2+58 X++J (EXPONENT)	8W19	900	F12 21
2W23	901	L10 18	2+59 X++J (EXPONENT)	8W19	901	F12 23
2W23	902	K14 23		8W19	902	C06 2
2W23	903	K14 28		8W19	903	C06 26
2W23	904	K15 1	2+56 EXPONENT RESULT-REG	8W19	904	C07 2
2W23	905	K15 6	2+57 EXPONENT RESULT=REG	8W19	905	C07 26
2W23	906	K15 23		8W19	906	C08 2
2W23	907	K15 28	2+59 EXPONENT RESULT-REG	8W19	907	C08 26
2W23	908	E02 7	ERROR (EXP = 3777 FROM CHASSIS 8)	8W19	908	H06 21

2W24	0.0			DÍVÍDE TO MULTIPLY	6W20	00	er della passa passa, prince princip diven service princip passa, etter
2W24	90	K01	1	2+0 DIVIDE TO MULTIPLY RESULT	6W20	90	K01 11
2W24	91	K01	6	2+1 DIVIDE TO MULTIPLY RESULT	6W20	91	K01 10
2W24	92	K01	23	2+2 DIVIDE TO MULTIPLY RESULT	6W20	92	K02 11
2W24	93	K01	28	2+3 DIVIDE TO MULTIPLY RESULT	6W20	93	K02 10
2W24	94	K02	1	2+4 DIVIDE TO MULTIPLY RESULT	6W20	94	K03 11
2W24	95	K02	6	2*5 DIVIDE TO MULTIPLY RESULT	6W20	95	K03 10
2W24	96	K02	23	2+6 DIVIDE TO MULTIPLY RESULT	6W20	96	K04 11
2W24	97	KO2	28	2+7 DIVIDE TO MULTIPLY RESULT	6W20	97	K04 10
2W24	98	K03	1	2+8 DIVIDE TO MULTIPLY RESULT	6W20	98	K05 11
2W24	99	K03	6	2*9 DIVIDE TO MULTIPLY RESULT	6W20	99	K05 10
2W24	900	K03	23	2+10 DIVIDE TO MULTIPLY RESULT	6W20	900	K06 11
2W24	901	KO3	28	2+11 DIVIDE TO MULTIPLY RESULT	6W20	901	K06 10
2W24	902	KO4	1	2+12 DIVIDE TO MULTIPLY RESULT	6W20	902	K07 11
2W24	903	K04	6	2*13 DIVIDE TO MULTIPLY RESULT	6W20	903	K07 10
2W24	904	K04	23	2+14 DIVIDE TO MULTIPLY RESULT	6W20	904	L01 11
2W24	905	KO4	28	2-15 DIVIDE TO MULTIPLY RESULT	6W20	905	L01 10
2W24	906	K05	1	2+16 DIVIDE TO MULTIPLY RESULT	6W20	906	L02 11
2W24	907	KO5	6	2+17 DIVIDE TO MULTIPLY RESULT	6W20	907	L02 10
2W24	908				6W20	908	

2W25	0.0		DIVIDE TO MULTIPLY	6W21 00
2W25	90	K05 23	2+18 DIVIDE TO MULTIPLY RESULT	6W21 90 L03 1
2W25	91	K05 28	2+19 DIVIDE TO MULTIPLY RESULT	6W21 91 L03 1
2W25	92	K06 1	2+20 DIVIDE TO MULTIPLY RESULT	6W21 92 L04 1
2W25	93	K06 6	2-21 DIVIDE TO MULTIPLY RESULT	6W21 93 L04 1
2W25	94	K06 23	2+22 DIVIDE TO MULTIPLY RESULT	6W21 94 L05 1
2W25	95	K06 28	2+23 DIVIDE TO MULTIPLY RESULT	6W21 95 L05 1
2W25	96	K07 1	2+24 DIVIDE TO MULTIPLY RESULT	6W21 96 L06 1
2W25	97	K07 6	2+25 DIVIDE TO MULTIPLY RESULT	6W21 97 L06 1
2W25	98	K07 23	2+26 DIVIDE TO MULTIPLY RESULT	6W21 98 L07 1
2W25	99	K07 28	2+27 DIVIDE TO MULTIPLY RESULT	6W21 99 L07 1
2W25	900	K08 1	2+28 DIVIDE TO MULTIPLY RESULT	6W21 900 M01 1
2W25	901	K08 6	2+29 DIVIDE TO MULTIPLY RESULT	6W21 901 M01 1
2W25	902	K08 23	2+30 DIVIDE TO MULTIPLY RESULT	6W21 902 M02 1
2W25	903	K08 28	2+31 DIVIDE TO MULTIPLY RESULT	6W21 903 M02 1
2W25	904	K09 1	2+32 DIVIDE TO MULTIPLY RESULT	6W21 904 M03 1
2W25	905	K09 6	2+33 DIVIDE TO MULTIPLY RESULT	6W21 905 M03 1
2W25	906	K09 23	2+34 DIVIDE TO MULTIPLY RESULT	6W21 906 M04 1
2W25	907	K09 28	2+35 DIVIDE TO MULTIPLY RESULT	6W21 907 M04 1
2W25	908	Q11 7	REDUCE MULT. 1	6W21 908 F07

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2W26	00			DIVIDE TO MULTIPLY	6W22	0 0	
2W26	90	K10	1	2+36 DÍVIDE TO MULTIPLY RESULT	6W22	90	M05 11
2W26	91	K10	6	2+37 DIVIDE TO MULTIPLY RESULT	6W22	91	M05 10
2W26	92	K10	23	2+38 DIVIDE TO MULTIPLY RESULT	6W22	92	M06 11
2W26	93	K10	28	2+39 DIVIDE TO MULTIPLY RESULT	6W22	93	M06 10
2W26	94	K11	. 1	2+40 DIVIDE TO MULTIPLY RESULT	6W22	94	M07 11
2W26	95	K11	6	2+41 DIVIDE TO MULTIPLY RESULT	6W22	95	M07 10
2W26	96	K11	23	2+42 DIVIDE TO MULTIPLY RESULT	6W22	96	NO1 11
2W26	97	K11	28	2+43 DIVIDE TO MULTIPLY RESULT	6W22	97	N01 10
2W26	98	K12	. 1	2+44 DIVIDE TO MULTIPLY RESULT	6W22	98	N02 11
2W26	99	K12	6	2+45 DIVIDE TO MULTIPLY RESULT	6W22	99	NO2 10
2W26	900	K12	23	2+46 DÎVÎDE TO MULTIPLY RESULT	6W22	900	N03 11
2W26	901	K12	28	2+47 DIVIDE TO MULTIPLY RESULT	6W22	901	NO3 10
2W26	902	Q12	7	REDUCE MULT 2	6W22	902	F06 1
2W26	903			The second secon	6W22	903	
2W26	904			•	6W22	904	
2W26	905	M11	14	GO MULT 1	6W22	905	H10 1
2W26	906	M11	16	GO MULT 2	6W22	906	K10 1
2W26	907	011	5	D P MULT 1	6W22	907	NO7 12
2W26	908	012	. 5	D P MULT 2	6W22	908	N07 19

2W27	0.0		DIVIDE FROM MULTIPLY	6W23	00		
2W27	90	R06 12	2+0 MULTIPLY TO DIVIDE (OPERAND) X++K	6W23	90	K01	1
2W27	91	R06 14	2+1 MULTIPLY TO DIVIDE (OPERAND) X++K	6W23	91	K01	3
2W27	92	R06 16	2+2 MULTIPLY TO DIVIDE (OPERAND) X++K	6W23	92	K01	2
2W27	93	R06 18	2+3 MULTIPLY TO DIVIDE (OPERAND) X++K	6W23	93	K02	1
2W27.	94	R07 12	2+4 MULTIPLY TO DIVIDE (OPERAND) X++K	6W23	94	K02	3_
2W27	95	R07 14	2+5 MULTIPLY TO DIVIDE (OPERAND) X++K	6W23	95	K02	2
2W27	96	R07 16	2+6 MULTIPLY TO DIVIDE (OPERAND) X++K	6W23	96	K03	1
2W27	97	R07 18	2+7 MULTIPLY TO DIVIDE (OPERAND) X++K	6W23	97	K03	3
2W27	98	R08 12	2+8 MULTIPLY TO DIVIDE (OPERAND) X++K	6W23	98	K03	2
2W27	99	R08 14	2+9 MULTIPLY TO DIVIDE (OPERAND) X++K	6W23	99	K04	1
2W27	900	R08 16	2+10 MULTIPLY TO DIVIDE (OPERAND) X**K	6W23	900	K04	3
2W27	901	R08 18	2+11 MULTIPLY TO DIVIDE (OPERAND) X++K	6W23	901	K04	2
2W27	902	R09 12	2+12 MULTIPLY TO DIVIDE (OPERAND) X++K	6W23	902	K05	1
2W27	903	R09 14	2+13 MULTIPLY TO DIVIDE (OPERAND) X++K	6W23	903	K05	3
2W27	904	R09 16	2+14 MULTIPLY TO DIVIDE (OPERAND) X++K	6W23	904	K05	2
2W27	905	R09 18	2+15 MULTIPLY TO DIVIDE (OPERAND) X++K	6W23	905	K06	1
2W27	906	R10 12	2+16 MULTIPLY TO DIVIDE (OPERAND) X++K	6W23	906	K06	3
2W27	907	R10 14	2+17 MULTIPLY TO DIVIDE (OPERAND) X++K	6W23	907	K06	2
2W27	908			. 6W23	908		

2W28	0.0		DIVIDE FROM MULTIPLY	6W24	0.0			
2W28	90	R10 16	,	6W24	90	K07	1	
2W28	91	R10 18		6W24	91	K07	3	
2W28	92	P06 12	2+20 MULTIPLY TO DIVIDE (OPERAND) X++K	6W24	92	K07	2	
2W28	93	P06 14	2+21 MULTIPLY TO DIVIDE (OPERAND) X++K	6W24	93	L01	1	
2W28	94	P06 16	2+22 MULTIPLY TO DIVIDE (OPERAND) X++K	6W24	94	LO1	3	
2W28	95	P06 18	2+23 MULTIPLY TO DIVIDE (OPERAND) X++K	6W24	95	L01	9	
2W28	96	P07 12	2+24 MULTIPLY TO DIVIDE (OPERAND) X**K	6W24	96	L02	4	
2W28	97	P07 14	2+25 MULTIPLY TO DIVIDE (OPERAND) X++K	6W24	97	L02	3	The second second second
2W28	98	P07 16	2+26 MULTIPLY TO DIVIDE (OPERAND) X++K	6W24	98	F05	9	
	99	P07 18			99			
2W28		•		6W24		L03	1	
2W28	900	P08 12	2+28 MULTIPLY TO DIVIDE (OPERAND) X++K	6W24	900	L03		
2W28	901	P08 14	2+29 MULTIPLY TO DIVIDE (OPERAND) X**K	6W24	901	L03	2	
2W28	902	P08 16	2+30 MULTIPLY TO DIVIDE (OPERAND) X**K	6W24	902	L04	1	
2W28	903	P08 18	2+31 MULTIPLY TO DIVIDE (OPERAND) X++K	6W24	903	L04	3	
2W28	904	P09 12	2+32 MULTIPLY TO DIVIDE (OPERAND) X++K	6W24	904	L04	2	
2W28	905	P09 14	2+33 MULTIPLY TO DIVIDE (OPERAND) X++K	6W24	905	L05	4	
2W28	906	P09 16	2+34 MULTIPLY TO DIVIDE (OPERAND) X++K	6W24	906	L05	ž	
2W28	907	P09 18	2+35 MULTIPLY TO DIVIDE (OPERAND) X++K	6W24	907	L05	2	
2W28	908	F-U - 10	MAGNINGERIEF TO REALPH FAIRWARDS MAIN	6W24	908	- 0 -	E	

2W29	0.0			DIVI	DE	FRO	M A	1ULT	1P	LY.							6W25	0.0		
2W29	90	P10	12	2+36	5 MŮ	LTI	PL	70	D	iv	DE	(0	PE	RAÑD	) X=	•+K	6W25	90	L06	1
2W29	91	P10	14	2+37	7 MÚ	LTI	PL	TC	D	IV:	DE	(0	PE	RAND	) X	**K	6W25	91	L06	3
2W29	92	P10	16	2+38	3 MÙ	LT1	PL	TO	D	İ۷	DE	(0	PEI	RAND	) X+	**K	6W25	92	L06	2
2W29	93	P10	18	2+39	) MÚ	LTI	PL	TC	D	Ì۷	DE	(0	PEI	RAND	) X1	**K	6W25	93	LO7	1
2W29	94	M06	12	2+4	a Mù	LTI	PL	Ť	D	I۷	DE	(0	PE	RAND	) X:	++K	6W25	94	L07	3
2W29	95	M06	14	2+4			PL		D	١V	DE	(0	PE	RAND	) X+	**K	6W25	95	LO7	2
2W29	96	M06	16	2+42	,		PL				DE		PE	RAND	) X+	• <b>*</b> K	6W25	96	MO1	1
2W29	97	M06	18	2+43		LTI	PL	ŤC	D	1 7	DE	(0	PE	RAND	) X	++K	6W25	97	M01	3
W29	98	Mo7	12	2+44	4 MÚ	LŤI	PL	Ť			DE		PER	RAND	) X.	**K	6W25	98	MO1	2
W29	99	MO7	14	2+4	5 MÙ	LT	PL	TC			DE		PEI	RAND	) X+	+K	6W25	99	M02	1
2W29	900	MO7	16	2+46	5 MÚ	ĒŦi	PL	7 70	D	١V	DE	(0	PER	RAND	) X+	• <b>*</b> K	6W25	900	MO2	3
2W29 2W29	901 902	M07	18	2+4	7 MÚ 8 SÍ	GΝ	PĹ	ŤC			DÉ		PE	RAND	) X+	**K	6W25	901 902	M02	2
2W29	903	006	12	2+0	MUL	TIF	LY		DI			COP	ER/	(D)	X×	J	6W25	903	M03	1
2W29	904	006	14	2+1	MUL	TIF	LY	TO	DI	V I I	DE	(OP	ER	AND	X **	J	6W25	904	M03	3
2W29	905	006	16	2+2	MUL	ŤĬF	LŸ	TO	DI	VII	Œ	COP	ER/	AND)	X * 1	J	6W25	905	M03	2
2W29	906	006	18	2+3	MUL	TIF	LY	TO	DI	V I I	Œ	(OP	ER	AND)	X **	, J	6W25	906	M 0 4	1
2W29 2W29	907 908	007	12	2 + 4	MUL	ŤĬF	LŸ	TO	Dİ	V I I	Þ	(OP	ER	( DNA	X**	J	6W25	907	M 0 4	3

2W30	0 0			DIVIDE FROM MULTIPLY	6W26	0.0		
2W30	90	007	14	2+5 MULTIPLY TO DIVIDE OPERAND X++J	6W26	90	M 0 4	2
2W30	91	007	16	2+6 MULTIPLY TO DIVIDE OPERAND X++J	6W26	91	M 0 5	1
2W30	92	007	18	2+7 MULTIPLY TO DIVIDE OPERAND X++J	6W26	92	M 0 5	3
2W30	93	008	12	2+8 MULTIPLY TO DIVIDE OPERAND X++J	6W26	93	M 0 5	2
2W30	94	008	14	2+9 MULTIPLY TO DIVIDE OPERAND X++J	6W26	94	M06	. 1
2W30	95	008	16	2+10 MULTIPLY TO DIVIDE OPERAND X++J	6W26	95	M06	3
2W30	96	008	18	2+11 MULTIPLY TO DIVIDE OPERAND X++J	6W26	96	M06	2
2w30	97	009	12	2+12 MULTIPLY TO DIVIDE OPERAND X++J	6W26	97	M07	1
2W30	98	009	14	2+13 MULTIPLY TO DIVIDE OPERAND X**J	6W26	98	MO7	3
2W30	99	009	16	2+14 MULTIPLY TO DIVIDE OPERAND X++J	6W26	99	M07	2
2W30	900	009	18	2+15 MULTIPLY TO DIVIDE OPERAND X++J	6W26	900	N01	1
2W30	901	010	12	2+16 MULTIPLY TO DIVIDE OPERAND X++J	6W26	901	N01	3
2W30	902	010	14	2+17 MULTIPLY TO DIVIDE OPERAND X++J	6W26	902	NO1	2
2W30	903	Q10	16	2+18 MULTIPLY TO DIVIDE OPERAND X++J	6W26	903	N02	1
2W30	904	Q10	18	2+19 MULTIPLY TO DIVIDE OPERAND X++J	6W26	904	N02	3
2W30	905	N06	12	2+20 MULTIPLY TO DIVIDE OPERAND X++J	6W26	905	NO2	2
2W30	906	NO6	14	2+21 MULTIPLY TO DIVIDE OPERAND X++J	6W26	906	NO3	1
2W30	907	N06	16	2+22 MULTIPLY TO DIVIDE OPERAND X++J	6W26	907	N 0 3	3
2W30	908			*** <b>***</b>	6W26	908		

2W31	00	or was was 2000 from some order over 1000 from 1000 from	DIVIDE FROM MULTIPLY	6W27	0.0		s design common company and some common common of common of the common o
2W31	90	N06 18	2003 MULTIPEY TO DIVIDE OPERAND X000	6W27	90	NO3	2
2W31	91	N07 12	2+24 MULTIPLY TO DIVIDE OPERAND X++J	6W27	91	N04	8
2W31	92	N07 14	2+25 MULTIPLY TO DIVIDE OPERAND X++J	6W27	92	N04	6
2W31	93	N07 16	2+26 MULTIPLY TO DIVIDE OPERAND X++J	6W27	93	N04	4
2W31	94	N07 18	2+27 MULTIPLY TO DIVIDE OPERAND X++J	6W27	94	NO4	27
2W31	95	N08 12	2428 MULTIPLY TO DIVIDE OPERAND X++J	6W27	95	NO4	25
2W31	96	NO8 14	2+29 MULTIPLY TO DIVIDE OPERAND X++J	6W27	96	N04	23
2W31	97	N08 16	2+30 MULTIPLY TO DIVIDE OPERAND X++J	6W27	97	N05	8
2W31	98	NO8 18	2+31 MULTIPLY TO DIVIDE OPERAND X++J	6W27	98	N05	6
2W31	99	N09 12	2+32 MULTIPLY TO DIVIDE OPERAND X++J	6W27	99	N05	4
2W31	900	N09 14	2+33 MULTIPLY TO DIVIDE OPERAND X++J	6W27	900	N05	27
2W31	901	N09 16	2+34 MULTIPLY TO DIVIDE OPERAND X++J	6W27	901	N05	25
2W31	902	N09 18	2+35 MULTIPLY TO DIVIDE OPERAND X++J	6W27	902	N05	23
2W31	903	N10 12	2+36 MULTIPLY TO DIVIDE OPERAND X++J	6W27	903	N06	8
2W31	904	N10 14	2+37 MULTIPLY TO DIVIDE OPERAND X++J	6W27	904	N06	6
2W31	905	N10 16	2+38 MULTIPLY TO DIVIDE OPERAND X++J	6W27	905	N08	4
2W31	906	N10 18	2+39 MULTIPLY TO DIVIDE OPERAND X++J	6W27	906	N06	27
2W31	907	L06 12	2+40 MULTIPLY TO DIVIDE OPERAND X++J	6W27	907	N06	25
2W31	908		-	6W27	908		

2W32	0 0		DIVIDE FROM MULTIPLY	6W28	0.0	man Priliter states allegen allegen sjonen jenner gering, prince somme, stema som och
W32	90	L06 14	2+41 MULTIPLY TO DIVIDE X*+J	6W28	90	N06 23
W32	91	L06 16	2+42 MULTIPLY TO DIVIDE X++J	6W28	91	N07 8
W32	92	L06 18	2+43 MULTIPLY TO DIVIDE X*+J	6W28	92	NO7 6
W32	93	L07 12	2+44 MULTIPLY TO DIVIDE X++J	6W28	93	N07 4
W32	94	L07 14	2+45 MULTIPLY TO DIVIDE X*+J	6W28	94	NO7 27
W32	95	L07 16	2+46 MULTIPLY TO DIVIDE X++J	6W28	95	NO7 25
W32	96	L07 18	2+47 MULTIPLY TO DIVIDE X*+J	6W28	96	NO7 23
W32	97		2+48 MULTIPLY TO DIVIDE X++J	6W28	97	- The state of the same of the state of the
W32	98	J03 1	ERROR MULT. 1	6W28	98	107 23
W32	99	J03 23	ERROR MULT. 2	6W28	99	J07 23
W32	900	J03 6	ERROR MULT. I	6W28	900	108 27
W32	901	J03 28	ERROR MULT. 2	6W28	901	J08 27
W32	902			6W28	902	
W32	903	C strength of particular management and the particular particular and the particular par		6W28	903	
W32	904			6W28	904	
W32	905			6W28	905	and the same to the part and the same to t
W32	906			6W28	906	
W32	907			6W28	907	William Committee of the Committee of th
W32	908			6W28	908	

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3W02	0.0			MEMORY TO READ DISTRIBUTOR	9W13	00		
3w02	90	H34	28	2*38	9W13	90	135	5
3W02	91	H35	4	2+39	9W13	91	136	5
3W02	92	H35	8	2+40	9W13	92	138	5
3W02	93	H35	28	2+41	9W13	93	139	5
3W02	94	H36	1	2+42	9W13	94	140	5
3w02	95	H36	8	2+43	9W13	95	141	5
3W02	96	H36	28	2+44	9W13	96	142	5
3W02	97			and the state of t	9W13	97		
3W02	98				9W13	98		
3W02	99				9W13	99		
3W02	900	H31	1	2+30	9W13	900	125	5
3W02	901	H31	8	2+31	9W13	901	126	5
3W02	902	H31	28	2+32	9W13	902	127	5
3W02	903	H32	1	2+33	9W13	903	128	5
3W02	904	H32	8	2+34	9W13	904	129	5
3W02	905	H32	28	2+35	9W13	905	132	5
3W02	906	H34	1	2+36	9W13	906	133	. 5
3W02	907	H34	8	2+37	9W13	907	134	5
3w02	908	-	_		9W13	908		

3W03	0.0			CENTRAL MEMORY TO PERIPHERAL	1W04	0.0		
3w03	90	125	1	2+0 CENTRAL TO PERIPHERAL DATA	1W04	90	G33	-5
3W03	91	126		2+1 CENTRAL TO PERIPHERAL DATA	1W04	91	G33	7
3W03	92	127	4	2+2 CENTRAL TO PERIPHERAL DATA	1W04	92	G33	10
3W03	93	128		2+3 CENTRAL TO PERIPHERAL DATA	1W04	93	G33	21
3W03	94	129	-	2+4 CENTRAL TO PERIPHERAL DATA	1W04	94	G33	24
3W03	95	132	4	2-5 CENTRAL TO PERIPHERAL DATA	1W04	95		26
3W03	96	133	•	2+6 CENTRAL TO PERIPHERAL DATA	1W04	96	G34	5
3 W 0 3	97	134	4	2+7 CENTRAL TO PERIPHERAL DATA	1W04	97	G34	7
3W03	98	135	4	2+8 CENTRAL TO PERIPHERAL DATA	1W04	98	G34	10
3W03	99	136	4	249 CENTRAL TO PERIPHERAL DATA	1W04	99	G34	21
3W03	900	138	•	2+10 CENTRAL TO PERIPHERAL DATA	1W04	900	G34	24
3 W 0 3	901	139	1	2-11 CENTRAL TO PERIPHERAL DATA	1W04	901	G34	26
3W03	902	140	1	2+12 CENTRAL TO PERIPHERAL DATA	1W04	902	G35	-5
3W03	903	141	1	2-13 CENTRAL TO PERIPHERAL DATA	1W04	903	G35	7
3W03	904	142	1	2+14 CENTRAL TO PERIPHERAL DATA	1W04	984	G35	10
3W03	905	C14	14	MEMORY MARGIN	1W04	905	H29	20
3W03	906				1W04	906		
3W03	907				1W04	907		
3W03	908				1W04	908		

3W04	0.0			MEMORY TO READ DISTRIBUTOR	10W13	0.0		The state along the other land
3W04	90	H40	28	2+53	10W13	90	111	-5
3W04	91	H41	1	2+54	10W13	91	112	5
3W04	92	H41	8	2+55	10W13	92	114	5
3W04	93	H41	28	2+56	10W13	93	115	5
3W04	94	H42	1	2+57	10W13	94	116	. 5
3W04	95	H42	8	2+58	10W13	95	117	5
3W04	96	H42	28	2+59	10W13	96	118	3 <b>5</b>
SWO4	97	123	9	ACCEPT FROM CHAS, 10	10W13	97	G05	28
3W04	98				10W13	98		
3W04	99				10W13	99		,
3W04	900	H37	.1	2+45	10W13	900	101	5
3W04	901	H37	8	2+46	10W13	901	102	5
3W04	902	H37	28	2+47	10W13	902	103	5
3W04	903	H39	1	2*48	10W13	903	104	5
5 W O 4	904	H39	8	2+49	10W13	904	105	5
5W04	905	H39	28	2+50	10W13	905	108	5
5W04	906	H40	1	2+51	10W13	906	109	5
8W04	907	H40	8	2+52	10W13	907	110	5
3w04	908				10W13	908		-

5005	00			READ DISTRIBUTOR FROM MEMORY	4W05	00		To the time the second of the
5 W O 5	90	125	-3	2+0	4W05	90	H01	1
W05	91	126	3	· 2+1	4W05	91	H01	8
W05	92	127	3	2+2	4W05	92	H01	28
WO5	93	128	3	2+3	4W05	93	H02	1
W05	94	129	3	2+4	4W05	94	H02	8
W05	95	132	3	2+5	4405	95	H02	28
W 0 5	96	133	3	2+6	4W05	96	HO3	1
WO5	97	134	3	2 * 7	4W05	97	H03	8
W 0 5	98	135	3	2+8	4405	98	HO3	28
WO5	99	136	3	2.9	4W05	99	H04	. 1
W 0 5	900	138	3	2+10	4005	900	HD4	ä
W 0 5	901	139	3	2.11	4405	901	H04	28
W 0 5	902	140	3	2+12	4W05	902	H06	1
W 0 5	903	141	3	2.13	4405	903	H06	8
W 0 5	904	142	3	2+14	4405	904	H06	28
WOS	905	G38	21	<u> </u>	4W05	905	121	22
W 0 5	906	938	20	WRITE	4W05	906	124	26
WD5	907			and the state of t	4405	907		
W05	908				4W05	908		

3W06	00	may divin office when more graph when		MEMORY TO READ DISTRIBUTOR	4006	0.0		
3w06	90	H27	28	2+23	4 W 0 6	90	111	5
3W06	91	H29	1	2+24	4006	91	112	5
3W06	92	H29	8	2+25	4006	92	114	5
3W06	93	H29	28	2+26	4W06	93	115	5
3WD6	94	H30	1	2+27	4406	94	116	5
3W06	95	H30	8	2+28	4406	95	117	5
3W06	96	H30	28	2+29	4406	96	118	5
3W06	97	124	12	READ PERIPHERAL	4006	97	121	11
3W06	98	123	3	ACCEPT	4006	98	G 0 5	28
3W06	99				4406	99		
3W06	900	H25	1	2+15	4W06	900	101	5
3W06	901	H25	8	2*16	4006	901	102	5
3W06	902	H25	28	2+17	4406	902	103	5
3W06	903	H26	1	2*18	4006	903	104	5
3W06	904	H26	8	2*19	4006	904	105	5
3W06	905	H26	28	2*20	4W06	905	108	-5
3W06	906	H27	1	2+21	4006	906	109	5
3W06	907	H27	8	2+22	4006	907	110	5
3W06	908			***	4406	908	_	

3w07	0.0			READ DISTRIBUTOR FROM MEMORY	9W07	00		
3W07	90	135	7	2*8	9W07	90	H21	28
3W07	91	136	7	2+9	9W07	91	H22	1
3W07	92	138	7	2+10	9W07	92	H22	8
3W07	93	139	7	2+11	9W07	93	H22	28
3W07	94	140	7	2+12	9W07	94	H24	4
3W07	95	141	7	2+13	9W07	95	H24	8
3W07	96	142	7	2+14	9W07	96	H24	28
3W07	97			entropio and the second of the second of the second of the second of the second of the second of the second of	9W07	97	ini ama	. <del></del> .
3w07	98				9W07	98		
3W07	99	123	7	ACCEPT	9W07	99	G39	28
3W07	900	125	7	2+0	9W07	900	H19	4
3W07	901	126	7	2*1	9007	901	H19	â
3w07	902	127	7	2+2	907	902	H19	28
3W07	903	128	7	2+3	907	903	H20	4
3W07	904	129	7	2+4	9W07	904	H20	8
3W07	905	132	7	2+5	9W07	905	H20	28
3W07	906	133	7	2*6	9w07	906	H21	1
3W07	907	134	7	2+7	9W07	907	H21	ā
3W07	908				9W07	908	** <b>C</b> **	······································

3W08	00			READ DISTRIBUTOR FROM MEMORY	10w07	0.0		<i>}</i>
3W08	90	135	9.	'2+6	10w07	90	H03:28	
3W08	91	136	: <b>9</b> ·	12+9	10007	91	H04 1	
3W08	92	138	19	2+10	10W07	92	H04 8	
3W08	93	139	9	·2+11·	10W07	93	H04 28	
3wo8	94	140	9	2+12	10W07	94	H06 1	
3W08	95	141	9	2+13	10W07	95	H06 8	
SWOB	96	142	9	2+14	10W07	96	H06 28	
3WD8	97				10W07	97		***************************************
3WD8	98				10W07	98		
3W08	99				10W07	99		
3WO8	900	125	9	<sup>1</sup> 2 • 0	10W07	900	H01 1	
3W08	901	126	9	2+1:	10W07	901	H01 8	
SOWE	902	127	9	2#2	10W07	902	H01 28	
3W08	903	128	9	2+3	10W07	903	H02 1	
3W08	904	129	9	2.4	10W07	904	H02 8	
3W08	905	132	9	2+5	10W07	905	H02 28	
3WD8	906	133	9	:2*6	10w07	906	H03 1	
3W08	907	134	9	2+7	10W07	907	HO3 8	*** **** *** ***
3w08	908				10W07	908	-	

W 0 9	00			READ DISTRIBUTOR FROM MEMORY	13406	00		
W09	90	135	11	2*8	13406	90	H21	28
WO9	91	136	11	2 + 9	13W06	91	H22	1
W 0 9	92	138	11	2+10	13W06	92	H22	8
W09	93	139	11	2+11	13W06	93	H22	28
W09	94	140	11	2+12	13W06	94	H24	1
W 0 9	95	141	11	2+13	13W06	95	H24	8
W 0 9	96	142	11	2+14	13W06	96	H24	28
W 0 9	97			The state of the s	13W06	97		
W09	98				13006	98		
W 0 9	99				13W06	99		
W09	900	125	11	2+0	13W06	900	H19	4
W09	901	126	14	2+1	13W06	901	H19	A
W09	902	127	11	2+2	13406	902	H19	28
WD9	903	128	11	2+3	13W06	903	H20	
W09	904	129	11	2.4	13W06	904	H20	ā
WD9	905	132	11	2.5	13W06	905	H20	28
. •	906	133	11	2*6	13W06	906	H21	- 0
W09				. <del>6.7.9</del>	13w06	907	H21	1 8
W09	907 908	134	11	£ ¥ /	13W06	908	MZ T	9

3W10	00			READ DISTRIBUTOR FROM MEMORY	14006	00		
W10	90	135	19	2+8	14406	90	H03	28
W10	91	136	19	2+9	14406	91	H04	1
W10	92	138	19	2+10	14006	92	H04	8
W10	93	139	19	2+11	14006	93		28
W10	94	140	19	2+12	14W06	94	H06	1
W10	95	141	19	2+13	14W06	95	H06	8
W10	96	142	19	2+14	14006	96	H06	28
W10	97				1406	97		
3W10	98				14006	98		
3W10	99				14006	99		
3W10	900	125	19	2+0	14006	900	H01	1
W10	901	126	19	2+1	14406	901	H01	8
3W10	902	127	19	2+2	14006	902	H01	28
3W10	903	128	19	2+3	14406	903	H02	1
3W10	904	129	19	2+4	14406	904	H02	8
3W10	905	132	19	2+5	14406	905	H02	28
5W10	906	133	19	2+6	14406	906	H03	1
3W10	907	134	19	2+7	14406	907	H03	8
W10	908				14006	908		

3W11	0.0			READ	DISTRIBUTOR FROM MEMORY	15W06	00		-011 Table 100 T
3W11	90	135	21	2 • 8	,	15W06	90	H21	28
3W11	91	136	21	2+9		15W06	91	H22	1
3W11	92	138	21	2+10		15W06	92	H22	8
3W11	93	139	21	2+11		15W06	93	H22	28
3W11	94	140	21	2+12		15006	94	H24	1
3W11	95	141	21	2+13		15W06	95	H24	8
3W11	96	142	21	2+14		15W06	96	H24	28
3W11	97					15W06	97		
3W11	98					15W06	98		
3W11	99					15W06	99		
3W11	900	125	21	2+0		15W06	900	H19	1
3W11	901	126	21	2+1		15W06	901	H19	8
3W11	902	127	21	2 * 2		19W06	902	H19	28
3W11	903	128	21	2+3	Control of the second of the s	15W06	903	H20	1
3W11	904	129	21	2 + 4		15W06	904	H20	8
3W11	905	132	21	2+5		15W06	905	H20	28
3W11	906	133	21	2 • 6		15W06	906	H21	1
3W11	907	134	21	2+7		15W06	907	H21	1 8
3W11	908					15W06	908		

3W12	00		READ DISTRIBUTOR TO LOWER REGISTER	7W05	0.0	
3W12	90	135 28	2+6 MEMORY TO REGISTER	7W05	90	A41 2
3W12	91	136 28	2+9 MEMORY TO REGISTER	7W05	91	A41 26
3W12	92	138 28	2+10 MEMORY TO REGISTER	7W05	92	A42 2
3W12	93	139 28	2+11 MEMORY TO REGISTER	7W05	93	A42 26
3W12	94	140 28	2+12 MEMORY TO REGISTER	7W05	94	B37 2
3W12	95	141 28	2+13 MEMORY TO REGISTER	7W05	95	B37 26
3W12	96	142 28	2+14 MEMORY TO REGISTER	7W05	96	B38 2
3W12	97			7W05	97	
3W12	98			7W05	98	
3W12	99			7W05	99	
3W12	900	125 28	2+0 MEMORY TO REGISTER	7W05	900	A37 2
3W12	901	126 28	2+1 MEMORY TO REGISTER	7W05	901	A37 26
3W12	902	127 28	2+2 MEMORY TO REGISTER	7W05	902	A38 2
3W12	903	128 28	2+3 MEMORY TO REGISTER	7W05	903	A38 26
3W12	904	129 28	2+4 MEMORY TO REGISTER	7W05	904	A39 2
3W12	905	132 28	245 MEMORY TO REGISTER	7W05	905	A39 26
3W12	906	133 28	2+6 MEMORY TO REGISTER	7W05	906	A40 2
3W12	907	134 28	2+7 MEMORY TO REGISTER	7W05	907	A40 26
3W12	908			7W05	908	_

3W13	0.0			READ DISTRIBUTOR FROM MEMORY	16006	00		<b></b>
3W13	90	135	23	2+8	16W06	90	H03	28
3W13	91	136	23	2+9	16W06	91	H04	1
3W13	92	138	23	2+10	16006	92	H04	ā
3W13	93	139	23	2+11	16W06	93	H04	28
3W13	94	I40	23	2+12	16W06	94	H06	1
3W13	95	141	23	2+13	16W06	95	HD6	8
W13	96	142	23	2+14	16W06	96	H06	28
3W13	97				16W06	97		
3W13	98				16W06	98		
3W13	99				16W06	99		
3W13	900	125	23	2+0	16W06	900	H01	1
3W13	901	126	23	2+1	16W06	901	H01	8
3W13	902	127	23	2+2	16W06	902	H01	28
3W13	903	128	23	2+3	16W06	903	H02	1
3W13	904	129	23	2+4	16W06	904	H02	8
3W13	905	132	23	2+5	16W06	905	H02	28
3W13	906	133	23	2+6	16W06	906	HO3	1
3W13	907	134	23	2+7	16006	907	H03	8
3W13	908				16006	908	•	

3W14	0.0			READ DISTRIBUTOR TO CONTROL	5W02	0.0	rra rada alast kajas relas dere aras	
3W14	90	132	26	2+5	5w02	90	E41	7
3W14	91	133	26	2+6	5W02	91	<b>B41</b>	22
3W14	92	134	26	2+7	5w02	92	E41	25
3W14	93	135	26	2+8	5W02	93	E41	27
3W14	94	136	26	2 • 9	5w02	94	E42	4
3W14	95	138	26	2+10	5W02	95	E42	6
3W14	96	139	26	2+11	5w02	96	E42	7
3W14	97	140	26	2*12	5w02	97	E42	22
3W14	98	141	26	2+13	5w02	98	E42	25
3W14	99	142	26	2+14	5W02	99	E42	27
3W14	900	129	26	2+4	5W02	900	E41	6
3W14	901				5W02	901		
3W14	902				5W02	902		
3W14	903	***************************************			5w02	903		
3W14	904	125	26	2+0	5W02	904	D42	22
3W14	905	126	26	2+1	5W02	905	D42	25
3W14	906	127	26	2+2	5W02	906	D42	27
3W14	907	128	26	2+3	5W02	907	E41	4
3W14	908				5W02	908		•

3W15	00			MEMORY FROM WRITE DISTRIBUTOR	13W13	0.0	
3W15	90	H21	24	2+8 FAN OUT TO MEMORY	13W13	90	133 3
3W15	91	H22	2	2+9 FAN OUT TO MEMORY	13W13	91	134 18
3W15	92	H22	19	2+10 FAN OUT TO MEMORY	13W13	92	134 13
3W15	93	H22	24	2+11 FAN OUT TO MEMORY	13W13	93	135 3
3W15	94	H24	2	2+12 FAN OUT TO MEMORY	13W13	94	136 18
3W15	95	H24	19	2+13 FAN OUT TO MEMORY	13413	95	136 13
3W15	96	H24	24	2+14 FAN OUT TO MEMORY	13W13	96	137 3
3W15	97	123	19	MEMORY ACCEPT	13W13	97	G39 28
3W15	98				13W13	98	
3W15	98				13413	99	
3W15	900	H19	2	2+0 FAN OUT TO MEMORY	13W13	900	128 18
3W15	901	H19	19	2+1 FAN OUT TO MEMORY	13W13	901	128 13
3W15	902	H19	24	2#2 FAN OUT TO MEMORY	13W13	902	129 3
3W15	903	H20	2	2+3 FAN OUT TO MEMORY	13W13	903	130 18
3W15	904	H20	19	2+4 FAN OUT TO MEMORY	13W13	904	130 13
3W15	905	H20	24	2+5 FAN OUT TO MEMORY	13W13	905	131 3
3W15	906	H21	2	2+6 FAN OUT TO MEMORY	13W13	906	132 18
3W15	907	H21	19	2+7 FAN OUT TO MEMORY	13W13	907	132 13
3W15	908				13W13	908	

3W1	6 00			MEMORY FROM WRITE DISTRIBUTOR	14W13	00	an desar data man desar otah atau basa dana man dana dana dana dan dan dan dan dan da
3w1	6 90	H27	24	2423FAN OUT TO MEMORY	14W13	90	111:28
3W1	6 91	H29	2	2424 FAN OUT TO MEMORY	14W13	91	112 24
SWE				2425 FAN OUT TO MEMORY	14W13	92	112 7
JW:				2426 FAN OUT TO MEMORY	14W13	93	113 28
3 4				2427 FAN OUT TO MEMORY	14W13	94	114 24
34				2+28 FAN OUT TO MEMORY	14W13	95	114 7
3 W				2+29 FAN OUT TO MEMORY	14W13	96	115:28
3W:			21		14W13	97	005 28
3 พ					14W13	98	
3W					14W13	99	Make pang maga alike satu, agan pang maga pang pang pang maga pang-ang satu satu satu satu satu satu satu
3 W		_	2	2+15 FAN OUT TO MEMORY	14W13	900	106 24
3W:				2+16 FAN OUT TO MEMORY	14W13	901	106 7
3 W				2+17 FAN OUT TO MEMORY	14W13	902	107 28
3W				2+18 FAN OUT TO MEMORY	14W13	903	108 24
3W				2+19 FAN OUT TO MEMORY	14W13	904	108 7
3 W				2+20 FAN OUT TO MEMORY	14W13	905	109 28
3W			2	2+21 FAN OUT TO MEMORY	14W13	906	110 24
3 W	a management of the second	the state of the same water than the	19	2+22 FAN OUT TO MEMORY	14W13	907	110 7
341					14W13	908	- <del></del> ·

3W17	00			MEMORY FROM WRITE DISTRIBUTOR	15W13	00	
3W17	90	H34 2	14	2+38 FAN OUT TO MEMORY	15W13	90	133 28
3W17	91	H35	2	2+39 FAN OUT TO MEMORY	15W13	91	134 24
3W17	92	H35 1	9	2+40 FAN OUT TO MEMORY	15W13	92	134 7
3W17	93	H35 2	4	2+41 FAN OUT TO MEMORY	15W13	93	135 28
3W17	94	H36	2	2+42 FAN OUT TO MEMORY	15W13	94	136 24
3W17	95	H36 1	. 9	2+43 FAN OUT TO MEMORY	15W13	95	136 7
3W17	96	H36 2	14	2+44 FAN OUT TO MEMORY	15W13	96	137 28
3W17	97	123 2	13	ACCEPT	15W13	97	G39 28
3W17	98				15W13	98	
3W17	99				15W13	99	
3W17	900	H31	2	2+30 FAN OUT TO MEMORY	19W13	900	128 24
3W17	901	H31 1	9	2+31 FAN OUT TO MEMORY	15W13	901	128 7
3W17	902	H31 2	14	2+32 FAN OUT TO MEMORY	15W13	902	129 28
3W17	903	H32	2	2+33 FAN OUT TO MEMORY	15W13	903	130 24
3W17	904	H32 1	9	2+34 FAN OUT TO MEMORY	15W13	904	130 7
3W17	905	H32 2	4	2+35 FAN OUT TO MEMORY	15W13	905	131 28
3W17	906	H34	2	2+36 FAN OUT TO MEMORY	15W13	906	132 24
3W17	907	H34 1	9	2+37 FAN OUT TO MEMORY	15W13	907	132 7
3W17	908				15W13	908	

3W18	00			MEMORY FROM WRITE DISTRIBUTOR	16W13 0	0
3W18	90	H40	24	2+53 FAN OUT TO MEMORY	16W13 9	0 111 28
3W18	91	H41	2	2+54 FAN OUT TO MEMORY	16W13 9	1 112 24
3W18	92	H41	19	2+55 FAN OUT TO MEMORY	16W13 9	2 112 7
3W18	93	H41	24	2+56 FAN OUT TO MEMORY	16W13 9	3 113 28
3W18	94	H42	2	2+57 FAN OUT TO MEMORY	16W13 9	4 114 24
3W18	95	H42	19	2+58 FAN OUT TO MEMORY	16W13 9	5 I14 7
3W18	96	H42	24	2+59 FAN OUT TO MEMORY	16W13 9	6 115 28
3W18	97	123	25	ACCEPT	16W13 9	7 G05 28
3W18	98				16W13 9	8
3W18	99				16W13 9	9
3W18	900	H37	2	2+45 FAN OUT TO MEMORY	16W13 90	0 106 24
3W18	901	H37	19	2446 FAN OUT TO MEMORY	16W13 90	1 106 7
3W18	902	H37	24	2#47 FAN OUT TO MEMORY	16W13 90	2 107 28
3W18	903	H39	2	2+48 FAN OUT TO MEMORY	16W13 90	
3W18	904	H39	19	2+49 FAN OUT TO MEMORY	16W13 90	4 108 7
3W18	905	H39	24	2+50 FAN OUT TO MEMORY	16W13 90	
3W18	906	H40	2	2+51 FAN OUT TO MEMORY	16W13 90	
3w18	907	H40	19	2+52 FAN OUT TO MEMORY	16W13 90	7 I10 7
3W18	908				16W13 90	

3W20	0.0			MEMORY ADDRESS FROM CONTROL	5W29	0.0		
3W20	90	G40 1	1.8	2+8 ADDRESS TO MEMORY	5W29	90	Q36	20
3W20	91	G41 1	11	2+9 ADDRESS TO MEMORY	5W29	91	Q37	9
3W20	92	G41 1		2+10 ADDRESS TO MEMORY	5W29	92	<b>Q37</b>	-5
3W20	93	G41 1		2+11 ADDRESS TO MEMORY	5W29	93	Q37	20
3W20	94	G41 1	18	2+12 ADDRESS TO MEMORY	5W29	94	038	9
3W20	95		11	2+13 ADDRESS TO MEMORY	5W29	95	038	5
3W20	96		14	2+14 ADDRESS TO MEMORY	5W29	96	038	
3W20	97		7	2+15 ADDRESS TO MEMORY	5W29	97	039	9
3W20	98	G42 1	1.8	2+16 ADDRESS TO MEMORY	5W29	98	039	5
3W20	99		28	ACCEPT	5W29	99	040	17
3W20	900	G35 1		CLOCK	5W29	900	J39	23
3W20	901		18	2+0 BANK SELECT	5W29	901	034	9
3W20	902		22	2+1 BANK SELECT	5W29	902	034	Ś
3W20	903	G38	9	2.2 CHASSIS 3 SELECT (000)	5W29	903	Q34	20
3W20	904	G38	4	2+3 CHASSIS 3 SELECT (000)	5W29	904	035	9
3W20	905		14	2+4	5W29	905	035	<u> </u>
3W20	906		11	2+5 ADDRESS TO MEMORY	5W29	906	035	20
3W20	907		14	2+6 ADDRESS TO MEMORY	5W29	907	036	9
3W20	908		17	2#7 ADDRESS TO MEMORY	5W29	908	036	5

3W21	00		CONTROL ( 1 TO 5 VIA 3)	1W01	0.0		ng makin apang pangga gapa manga cerus cerus ce a sa sa sa cerus cerus ceru
3W21	90			1W01	90		
3W21	91			1W01	91		
3W21	92			1W01	92		
3W21	93			1w01	93		
3W21	94		·	1w01	94		
3W21	95			1W01	95		THE PART AND THE REAL PROPERTY AND THE PARTY
3W21	96			1001	96		
3W21	97			1401	97		
3W21	9.8			1001	98		
3W21	99			1W01	99		
3W21	900	J37 27	CLOCK	.1W01	900	H05	12
3W21	901	J39 10	READ GO	1W01	901	130	27
3W21	902	J39 9	WRITE: GO	1W01	902	130	25
3W21	903	J39 8	EXCHANGE GO	1W01	903	130	8
3W21	904			1W01	904		
3W21	905			1W01	905		
3W21	906			1W01	906		
3W21	907			1W01	907		
3W21	908			1W01	908		

3M22	0.0			R	EADF	WRITE EXCHANGE ADDRESS	1403	00		
W22	90					,	1W03	90		
W22	91	J41	10	В	17 9	pp. (Commonwhite) - professor and construction of the commonwealth	1w03	91	F41	27
W22	92	J41	9	8	IT 1	Ĭ	1403	92	F41	25
W22	93	J41	8	B	IT 15		1403	93	F41	23
W22	94	J42	23	8	ÎT 12	2	1403	94	F42	8
W22	95	J42			17 13		1403	95	F42	6
W22	96	J42			IT 14	_	1403	96	G42	4
W22	97	J42	10	8	IT 1	allegates manus sament annough in 1979 - an Manusco an principal in 1989, 1989, 1989, 1979	1W03	97	F42	27
W22	98	J42	9	8	17 10	\$	1403	98	F42	25
W22	99	J42	8	B	17 17		1403	99	F42	
W22	900	J41	21	B	IT 8		103	900	F41	4
W22	901	J40	23		ÎT 0		1W03	901	F40	A.
W22	902	J40	22		ÎT 1		1003	902	F40	6
W22	903	J40	21		IT 2	geregier uns geschen gegenne geliebnisgene freide in sog der der megliftligt med nacht vor alle bekannt der der versten der der der der der der der der der der	1003	903	F40	4
W22	904	J40	10	_	ÎT 3		1W03	904	F40	27
W22	905	J40			17 4	The second secon	1003	905	F40	
W22	906	J40	á	8	7 **		1003	906	F40	
W22	907	J41	23		T 6		1003	907	F41	Ā
W22	908	J41		_	İŤŽ		1003	908	F41	6

3w23	00			READFWRITE EXCHANGE ADDRESS	5W24 00	nga <b>agan agan agan aran s</b> ama sama anan cump dang sama sama dana da
3W23	90				5W24 90	
3W23	91	J41	28	BIT 9	5W24 91	N41 21
3W23	92	J41	26	BÎT 10	5W24 92	N41 24
3W23	93	J41	24	B!7 11	5w24 93	N41 26
3W23	94	J42	7	BIT 12	5W24 94	N42 5
3W23	95	J42	5	BIT 13	5W24 95	N42 7
3W23	96	J42	3	BIT 14	5W24 96	N42 10
3W23	97	J42	28	BIT 15	5W24 97	N42 21
3W23	98	J42	26	BIT 16	5w24 98	N42 24
3W23	99	J42	24	BIT 17	5w24 99	N42 26
3W23	900	J41	3	BIT 8	5W24 900	N41 10
3W23	901	J40	7	BITO	5W24 901	N40 5
3W23	902	J40	5	BÎŤ 1	5W24 902	N40 7
3W23	903	J40	3	817 2	5W24 903	N40 10
3W23	904	J40	28	BIT 3	5W24 904	N40 21
3W23	905	J40	26	BIT 4	5W24 905	N40 24
3W23	906	J40	24	B1T 5	5W24 906	N40 26
3W23	907	J41	7	BIT 6	5W24 907	N41 5
3W23	908	J41	5	Bit 7	5W24 908	N41 7

3W24	0.0	CONTROL (1 TO 5 VIA 3)	5w21	00	-
3W24	90	,	5W21	90	
3W24	91	THE CONTRACTOR COMMISSION CONTRACTOR OF THE CONT	5w21	91	
3W24	92		5W21	92	
3W24	93		5W21	93	
3W24	94		5W21	94	
3W24	95		5W21	95	
3W24	96		5W21	96	
3W24	97		5W21	97	
3W24	98		5W21	98	
3W24	99		5W21	99	
3w24	900	J39 28 READ GO (PERIPH)	5w21	900	041 18
3W24	901	J39 26 WRITE GO (PERIPH)	5W21	901	041 20
3W24	902	J39 24 EXCHANGE GO (PERIPH)	5W21	902	040 15
3W24	903		5W21	903	
3W24	904		5W21	904	
3W24	905		5w21	905	
3W24	906		5W21	906	
3W24	907		5W21	907	
3W24	908		5W21	908	

3W39	00			MEMORY TO READ DISTRIBUTOR	3W39	0.0		to retta sapa force attiga sapa etta attina esca, socie como e
3W39	90	H21:	28	2+8	. 9EWE	90	135	<b>5</b>
3W39	91	H22	1	2+9	PENE	91	136	5
3W39	92	H22	8	2+10	3W39	92	138	5
3W39	93	H22	28	2+11	3W39	93	139	. 5
3W39	94	H24	1	·2•12·	3w39	94	140	:5
3W39	95	H24	8	2+13	3W39	95	141	- 5
3W39	96	H24	28	2+14	3W39	96	142	. 5
3W39	97	<b>G39</b>		ACCEPT	3W39	97	123	5
3W39	98	-•			3W39	98		•
3W39	99				3w39	99		
3W39	900	H19	1	2 • 0	3439	900	125	:5.
3W39	901	H19	8	2+1	3W39	901	126	5
3W39	902		_	2+2	3W39	902	127	5
3W39	903	H20	1	2+3	3W39	903	128	5
3W39	904	H20	- 8	2+4	3w39	904	129	5
3W39	905	H20	28	2.5	3W39	905	132	5
3W39	906	H21	1	2.6	3W39	906	133	5
3W39	907	H21	8	2+7	3W39	907	134	5
3W39	908				-3W39	908		-

W39	0.0			READ DISTRIBUTOR FROM MEMORY	3W39	00	and the training of the training to the traini
W39	90	135	5	2+8	3W39	90	H21 28
W39	91	136	5	2+9	3W39	91	H22 1
W39	92	138	5	2+10	3W39	92	H22 8
W39	93	139	5	2+11	3w39	93	H22 28
W39	94	140	5	2+12	3W39	94	H24 1
W39	95	141	5	2+13	SW39	95	H24 8
W39	96	142	5	2+14	3W39	96	H24 28
W39	97	123	5	ACCEPT	3W39	97	G39 28
W39	98				3W39	98	
W39	99				3W39	99	
W39	900	125	5	2+0	3W39	900	H19 1
W39	901	126	5	2+1	3439	901	H19 8
W39	902	127	5	2+2	3W39	902	H19 28
W39	903	128	5	2.43	3W39	903	H20 4
W39	904	129	- 5	2+4	3W39	904	H20 8
W39	905	132	5	2+5	3W39	905	H20 28
W39	906	133	5	2+6	3W39	906	H21 1
W39	907	134	5	2 * 7	3W39	987	H21 8
W39	908				3w39	908	•

4W03	0.0		MEMORY FROM WRITE BISTRIBUTOR	13W12	00	, .
4W03	90	H03 24	2+6 FAN OUT TO MEMORY	13W12	90	133 5
4W03	91	H04 2	2+9 FAN OUT TO MEMORY	13W12	91	134 20
4W03	92	H04 19	2+10 FAN OUT TO MEMORY	13W12	92	134: 11
4W03	93	H04 24	2+11 FAN OUT TO MEMORY	13W12	93	135 5
4003	94	H06 2	2412 FAN OUT TO MEMORY	13812	94	136 20
4W03	95	H06 19	2+13 FAN OUT TO MEMORY	13W12	95	136 11
4W03	96	H06 24	2+14 FAN OUT TO MEMORY	13W12	96	137 5
4W03	97			13W12	97	
4W03	98			13W12	98	
4W03	99			13W12	99	The state of the s
4003	900	H01 2	2+0 FAN OUT TO MEMORY	13W12	900	128 20
4W03	901	H01 19	2+1 FAN OUT TO MEMORY	13W12	901	128 11
403	902	H01 24	2+2 FAN OUT TO MEMORY	13W12	902	129 5
4W03	903	H02 2	2.3 FAN OUT TO MEMORY	13W12	903	130 20
4003	904	H02 19	2+4 FAN OUT TO MEMORY	13W12	904	130 11
4W03	905	H02 24	245 FAN OUT TO MEMORY	13W12	905	131 5
4W03	906	H03 2	2+6 FAN OUT TO MEMORY	13W12	906	132 20
4W03	907	H03 19	247 FAN OUT TO MEMORY	13W12	907	132 11
4003	908			13W12	908	105 77

4W04	00			MEMORY FROM WRITE DISTRIBUTOR	14W12	00	er om som om one one op gan, one has som one o
4W04	90	H09	24	2+23 FAN OUT TO MEMORY	14W12	90	111 5
4W04	91	H11	2	2+24 FAN OUT TO MEMORY	14W12	91	112 20
4W04	92	H11	19	2+25 FAN OUT TO MEMORY	14W12	92	112 11
4W04	93	H11	24	2+26 FAN OUT TO MEMORY	14W12	93	113 5
4W04	94	H12	2	2+27 FAN OUT TO MEMORY	14W12	94	114 20
4W04	95	H12	19	2+28 FAN OUT TO MEMORY	14W12	95	114 11
4W04	96	H12	24	2+29 FAN OUT TO MEMORY	14W12	96	115 5
4W04	96 97	11 1117921		som times and the source of th	14W12	97	······································
4W04	98				14W12	98	
4W04	99				14W12	99	
4 W O 4	900	H <sub>0</sub> 7	2	2+15 FAN OUT TO MEMORY	14W12	900	106 20
4WD4	901	H07	19	2+16 FAN OUT TO MEMORY	14W12	901	106 11
4W04	902	HO7	24	2+17 FAN OUT TO MEMORY	14W12	902	107 5
4004	903	HO8	2	2+18 FAN OUT TO MEMORY	14W12	903	108 20
4W04	904	HO8	19	2-19 FAN OUT TO MEMORY	14W12	904	108 11
4W04	905	HOB	24	2+20 FAN OUT TO MEMORY	14W12	905	109 5
4W04	906	H09	2	2+21 FAN OUT TO MEMORY	14W12	906	110 20
4W04	907	H09	19	2+22 FAN OUT TO MEMORY	14W12	907	110 11
4W04	908				14W12	908	.10 11

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4W05	00	-p. 450 000 min told god fire min god son son en	MEMORY TO READ DISTRIBUTOR	3W05	00		10 may 100 may 100 may 100 may 100 may 100 may 100 may 100 may 100 may 100 may 100 may 100 may 100 may 100 may
4W05	90	H01 1	2+0	3405	• • 0	125	3
4405	91	H01: 8	2.1	3W05	91	126	3
4W05	92	H01 28	2+2	3W05	92	127	3
4W05	93	H02 1	·2+3·	3W05	93	128	3
AWOS.	94	H02 8	2 • 6	3W05	94	129	3
4W05	95	H02 28	2.5	3405	95	132	3
4W05	96	H03 1	2.6	3W05	96	133	3
4W05	97	H03 8	2+7	3W05	97	134	3
4W05	98	H03 28	2+8	3W05	98	135	3
4W05	99	H04 1	2+9	3W05	99	136	3
4W05	900	H04 8	2+10	3405	900	138	3
4W05	901	H04 28	2+11	3W05	901	139	3
4W05	902	H06 1	2+12	3W05	902	140	:3
4W05	903	H06 8	2+13	3405	903	141	3
4W05	904	H06 28	2+14	3W05	904	142	3
4W05	905	121 22	·G0	3W05	905	938	21
4W05	906	124 26	WRITE	3W05	906	038	20
4W05	907	ALL I ALL DE SE PE PE SE SÉ PER PUI IPI		3W05	907		W
4W05	908			3W05	908		

W 0 6	0.0			READ DISTRIBUTOR FROM MEMORY	3w06	0.0		
W 0 6	90	111	5	2+23	3W06	90	H27	28
W 0 6	91	112	5	2+24	3W06	91	H29	1
W 0 6	92	114	4	2+25	3W06	92	H29	ī
W D 6	93	115	- 5	2+26	3406	93	H29	28
N D 6	94	116	,	2*27	3W06	94	H30	- 4
106	95	117	5	2+28	3406	95	H30	A
106	96	118	5	2+29	3W06	96	H30	28
06	97	121	11	READ PERIPHERAL	3W06	97	124	12
-	98	005	28	ACCEPT	3W06	98	123	3
106	99	בעט			3W06	99	120	
_			-	2.48			HOE	
06	900	101	5	2+15	3W06	900	H25	
06	901	102	5	2+16	3406	901	H25	
06	902	103	<u> 5</u>	2+17	3406	902	H25	5
06	903	104	5	2+18	3W06	903	H26	1
106	904	105	- 5	2+19	3W06	904	H26	- (
106	905	108	5	2+20	3W06	905	H26	2 8
06	906	109	5	2+21	3406	906	H27	
106	907	110	5	2*22	3W06	907	H27	É
106	908	. 10	9	# F & C	3006	908	m & *	•

4W07	00			CENTRAL MEMORY TO PERIPHERAL	1W09 0	0
4W07	90	101	1	2+15 CENTRAL TO PERIPHERAL DATA	1W05 9	0 035 21
4W07	91	102	1	2+16 CENTRAL TO PERIPHERAL DATA	1W05 9	1 635 24
4W07	92	103	1	2+17 CENTRAL TO PERIPHERAL DATA	1W05 9	2 635 26
4W07	93	104	1	2+18 CENTRAL TO PERIPHERAL DATA	1W05 9	3 G36 5
4W07	94	105	1	2+19 CENTRAL TO PERIPHERAL DATA	1W05 9	4 G36 7
4W07	95	108	1	2+20 CENTRAL TO PERIPHERAL DATA	1W05 9	5 G36 10
4W07	96	109	- <del>-</del>	2+21 CENTRAL TO PERIPHERAL DATA	1W05 9	6 G36 21
4W07	97	110	1	2+22 CENTRAL TO PERIPHERAL DATA	1W05 9	7 G36 24
4W07	98	111	1	2+23 CENTRAL TO PERIPHERAL DATA	1W05 9	8 G36 26
4W07	99	112	1	2+24 CENTRAL TO PERIPHERAL DATA	1W05 9	9 G37 5
4W07	900	114	1	2+25 CENTRAL TO PERIPHERAL DATA	1W05 90	0 637 7
4W07	901	115	ī	2+26 CENTRAL TO PERIPHERAL DATA	1W05 90	
4W07	902	116	1	2+27 CENTRAL TO PERIPHERAL DATA	1W05 90	2 G37 21
4W07	903	117	1	2+28 CENTRAL TO PERIPHERAL DATA	1W05 90	
4W07	904	118	4	2+29 CENTRAL TO PERIPHERAL DATA	1W05 90	4 G37 26
4W07	905	124	~ <del>7</del>	RESUME CENTRAL READ	1W05 90	
4W07	906	124	9	RESUME CENTRAL READ	1W05 90	-
4W07	907	C14	14	STORAGE MARGIN	1W05 90	
4W07	908		_ '		1W05 90	

4W08	0 0			MEMORY TO READ DISTRIBUTOR	9W14	0.0		
4 W O B	90	H16	28	2+38	9W14	90	135	3
4 W Q 8	91	H17	î	2+39	9W14	91	136	3
BOW	92	H17	8	2+40	9W14	92	138	3
BOW	93	H17	28	2+41	9W14	93	139	3
W08	94	H18	1	2+42	9W14	94	140	3
4W08	95	H18	8	2+43	9W14	95	141	3
WO8	96	H18	28	2+44	9W14	96	142	3
WO8	97	121	20	GO	9W14	97	G38	21
WO8	98	124	5	WRITE	9W14	98	G38	20
80W	99	121	9	READ PERIPHERAL	9W14	99	124	12
80 W	900	H13	1	2+30	9W14	900	125	3
4W08	901	H13	8	2+31	9W14	901	126	- 3
1W08	902	H13	28	2+32	9W14	902	127	3
4 W O 8	903	H14	1	2+33	9W14	903	128	3
80 W	904	H14	8	2+34	9W14	904	129	3
8 0 W	905	H14	28	2+35	9W14	905	132	3
WO8	906	H16	1	2+36	9W14	906	133	3
WOB	907	H16	8	2+37	9W14	907	134	3
4W08	908				9W14	908		

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4W09	0.0		MEMORY FROM WRITE DISTRIBUTOR	15W12 00
4W09	90	H16 24	2+38 FAN OUT TO MEMORY	15W12 90 133 5
4W09	91	H17 2	2+39 FAN OUT TO MEMORY	15W12 91 134 20
4W09	92	H17 19	2+40 FAN OUT TO MEMORY	15W12 92 134 11
4W09	93	H17 24	2-41 FAN OUT TO MEMORY	15W12 93 I35 5
4409	94	H18 2	2+42 FAN OUT TO MEMORY	15W12 94 136 20
4409	95	H18 19	2+43 FAN OUT TO MEMORY	15W12 95 136 11
4W09	96	H18 24	2+44 FAN OUT TO MEMORY	15W12 96 137 5
4W09	97		,	15W12 97
4W09	98			15W12 98
4W09	99			15W12 99
4 W 0 9	900	H13 2	2+30 FAN OUT TO MEMORY	15W12 900 128 20
4W09	901	H13 19	2+31 FAN OUT TO MEMORY	15W12 901 128 11
4W09	902	H13 24	2+32 FAN OUT TO MEMORY	15W12 902 129 5
4W09	903	H14 2	2+33 FAN OUT TO MEMORY	15W12 903 130 20
4W09	904	H14 19	2+34 FAN OUT TO MEMORY	15W12 904 I30 11
4W09	905	H14 24	2+35 FAN OUT TO MEMORY	15W12 905 [31 5
4W09	906	H16 2	2+36 FAN OUT TO MEMORY	15W12 906 132 20
4W09	907	H16 19	2+37 FAN OUT TO MEMORY	15W12 907 132 11
4409	908		· .	15W12 908

4W10	0.0			MEMORY FROM WRITE DISTRIBUTOR	16W12	0.0	
4W10	90	H22	24	2+53 FAN OUT TO MEMORY	16W12	90	111 5
4W10	91	H23	2	2+54 FAN OUT TO MEMORY	16W12	91	112 20
W10	92	H23	19	2+55 FAN OUT TO MEMORY	16W12	92	112 11
W10	93	H23	24	2+56 FAN OUT TO MEMORY	16W12	93	113 5
W10	94	H24	2	2+57 FAN OUT TO MEMORY	16W12	94	I14 20
4W10	95	H24	19	2+58 FAN OUT TO MEMORY	16W12	95	114 11
W10	96	H24	24	2+59 FAN OUT TO MEMORY	16W12	96	115 5
W10	97				16W12	97	
W10	98	124	24	WRITE	16W12	98	G04 20
W10	99	121	3	GO	16W12	99	G04 21
W10	900	H19	2	2+45 FAN OUT TO MEMORY	16W12	900	106 20
4W10	901	H19	19	2+46 FAN OUT TO MEMORY	16W12	901	106 11
W10	902	H19	24	2+47 FAN OUT TO MEMORY	16W12	902	I 07 5
4W10	903	H21	2	2+48 FAN OUT TO MEMORY	16W12	903	108 20
4W10	904	H21	19	2+49 FAN OUT TO MEMORY	16W12	904	108 11
4W10	905	H21	24	2+50 FAN OUT TO MEMORY	16W12	905	109 5
4W10	906	H22	2	2+51 FAN OUT TO MEMORY	16W12	906	110 20
4W10	907	H22	19	2+92 FAN OUT TO MEMORY	16W12	907	I10 11
4W10	908				16W12	908	

4W11	00			MEMORY TO READ DISTRIBUTOR	10W14	00		<b></b>
4W11	90	H22	28	2+53	10W14	90	111	3
4W11	91	H23	1	2+54	10W14	91	112	3
4W11	92	H23	8	2+55	10W14	92	114	3
4W11	93	H23	28	2+56	10W14	93	115	3
4W11_	94	H24	1	2+57	10W14	94	116	3
4W11	95	H24	8	2+58	10W14	95	117	3
4W11	96	H24	28	2+59	10W14	96	118	3
4W11	97	121	18	GO	10W14	97	G 0 4	21
4W11	98	124	3	WRITE	10W14	98	Gn4	20
4W11	99	121	7	READ PERIPHERAL	10W14	99	119	12
4W11	900	H19	1	2+45	10W14	900	101	3
4W11	901	H19	8	2+46	10W14	901	102	3
4W11	902	H19	28	2+47	10W14	902	103	3
4W11	903	H21	1	2+48	10W14	903	104	- 3
4W11	904	H21	8	2+49	10W14	904	105	3
4W11	905	H21	28	2+50	10W14	905	108	3
4W11	906	H22	1	2+51	10W14	906	109	3
4W11	907	H22	8	2+52	10W14	907	110	3
4W11	908				10W14	908	_	_

4W12	0.0			READ	DISTRIBUTOR	FROM MEMORY		9W08	00		
4W12	90	111	7	2*23				9W08	90	H27	28
4W12	91	112	7	2+24				9W08	91	H29	1
4W12	92	114	7	2+25				9W08	92	H29	8
4W12	93	115	7	2+26				9W08	93	H29	28
4W12	94	116	7	2+27				9 W 0 8	94	H30	1
4W12	95	117	7	2+28				9W08	95	H30	8
4W12	96	118	7	2+29	erakan er saater en e e	one or a contract of		9W08	96	H30	28
4W12	97							9W08	97		
4W12	98							9W08	98		
4W12	99							9 W Q 8	99		
4W12	900	101	7	2+15				9 W Q B	900	H25	1
4W12	901	102	7	2+16				9W08	901	H25	8
4W12	902	103	7	2+17				9W08	902	H25	28
4W12	903	104	7	2+18	Management of the second supply of the second secon			9W08	903	H26	1
4W12	904	105	7	2+19				9 W O 8	904	H26	8
4W12	905	108	7	2+20	•		• •	9W08	905	H26	28
4W12	906	109	7	2+21				9W08	906	H27	1
4W12	907	110	7	2*22				9W08	907	H27	8
4W12	908			ing a shipping the property and				9W08	908		en standard constitution

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4W13	0.0			READ DISTRIBUTOR FROM MEMORY	10W08	0.0	
4W13	90	111	9.	2+23	10W08	90	H09 28
4W13	91	112	9	2+24	10008	91	H11 1
4W13	92	114	9	2+25	10W08	92	H11 8
4W13	93	115	9	2*26	10008	93	H11 28
4W13	94	116	9	2+27	10W08	94	H12 1
4W13	95	117	9	2+28	10008	95	H12 8
4W13	96	118	9	2+29	10008	96	H12 28
4W13	97		***************************************		10008	97	
4W13	98				10008	98	
4W13	99				10W08	99	and the last
4W13	900	101	9	2+15	10008	900	H07 1
4W13	901	102	9	2*16	10W08	901	H07 8
4W13	902	103	9	2+17	10008	902	H07 28
4W13	903	104	9	2+18	10008	903	H08 1
4W13	904	105	9	2+19	10W08	904	H08 8
4W13	905	108	9	2+20	10008	905	H08 28
4W13	906	109	9	2+21	10W08	906	H09 1
4W13	907	110	9	2+22	10W08	907	H09 8
4W13	908	_			10W08	908	

4W14	00			READ DISTRIBUTOR TO CONTROL	5W01	00			
4W14	90	108	26	2+20	5w01	90	C42	27	
W14	91	109	26	2+21	5W01	91	D41	4	
W14	92	110	26	2+22	5w01	92	D41	6	
W14	93	111	26	2+23	5W01	93	D41	7	
W14	94	112	26	2+24	5w01	94	D41	22	
W14	95	114	26	2+25	5W01	95	D41	25	
W14	96	115	26	2+26	5W01	96	D41	27	
W14	97	116	26	2+27	5W01	97	D42	4	
W14	98	117	26	2+28	5W01	98	D42	6	
W14	99	118	26	2+29	5W01	99	D42	7	
W14	900	105	26	2+19	5W01	900	C42	25	
W14	901			en en en en en en en en en en en en en e	5W01	901			
W14	902				5w01	902			
4W14	903	a remografication of alternation of		AND ASSESSMENT OF CHARLES OF CASE AND ASSESSMENT OF A STATE OF CASE CASE CASE CASE CASE CASE CASE CASE	5w01	903			
4W14	904	101	26	2*15	9w01	904	C42	4	
4W14	905	102	26	2.16	5W01	905	C42	6	
4W14	906	103	26	2+17	5W01	906	C42	7	
4W14	907	104	26	2+18	5W01	907	C42	22	
4W14	908	- •			5W01	908			

4W15	00		READ DISTRIBUTOR FROM MEMORY	1307	00		
4W15	90	I11 11	2*23	13W07	90	H27	28
4W15	91	112 11	2+24	13407	91	H29	1
W15	92	114 11	2*25	13W07	92	H29	8
W15	93	115 11	2*26	13407	93	H29	28
4W15	94	116 11	2+27	13W07	94	H30	_ 1
W15	95	117 11	2+28	13W07	95	H30	8
4W15	96	118 11	2+29	13W07	96	H30	28
W15	97	121 28	MEMORY GO	13W07	97	G38	21
4W15	98	124 18	MEMORY WRITE	13W07	98	G38	20
4W15	99			13W07	99		
W15	900	101 11	2+15	13W07	900	H25	1
W15	901	102 11	2+16	13407	901	H25	8
W15	902	103 11	2+17	13W07	902	H25	28
4W15	903	104 11	2+18	13407	903	H26	1
W15	904	I 05 11	2+19	13W07	904	H26	8
W15	905	108 11	2*20	13W07	905	H26	28
4W15	906	109 11	2+21	13W07	906	H27	1
4W15	907	110 11	2+22	13w07	907	H27	8
4W15	908		··· ••	13W07	908	_	•

4W16	0.0			READ DISTRIBUTOR FROM MEMORY	1407	00	
4W16	90	111	19	2+23	14W07	90	H09 28
		4 1 100		2+24	14W07	91	H11 1
4W16	91		19	2+25	14407	92	H11 8
4W16	92		19		14W07	93	H11 28
4W16	93		19	2*26	1407	94	H12 1
4W16	94		19	2+27		95	
4W16	95		19	2+28	14W07		H12 8
4W16	96	118	19	2+29	14W07	96	H12 28
4W16	97	121	26	GO MEMORY	14W07	97	004 21
4W16	98	124	20	WRITE MEMORY	14W07	98	G04 20
4W16	99	• • •			14W07	99	
4W16	900	101	19	2+15	14W07	900	H07 1
4W16	901		19	2+16	14W07	901	H07 8
4W16	902		19	2+17	14W07	902	H07 28
	903		19	2.18	1407	903	H08 1
4W16			19	2+19	14W07	904	H08 8
4W16	904		7.5	_ 7 1	1407	905	H08 28
4W16	905	108	19	2+20	14W07	906	H09 1
4W16	906		19	2+21			H09 8
4W16	907	I10	19	2+22	14W07	987	F1 U 7 0
4W16	908				14W07	908	The second section of the second

4W17	0.0		READ DISTRIBUTOR FROM MEMORY	19W07	0.0	******************************
4W17	90	111:25	: :2 <b>+23</b>	19W07	90	H27:28
4W17	91	112 21	2+24	15W07	91	H29 1
4W17	92	114 21	2+25	19W07	92	H29 8
4W17	93	115 21	2+26	1 W 07	93	H29 28
4W17	94	116 21	2+27	38W07	94	H30 1
4W17	95	117 21	2+28	19W07	95	H30 8
4W17	96	118 21	2+29	15W07	96	H30 28
4W17	97	121	GO	15W07	97	038 21
4W17	98	124 22		15w07	98	G38 20
4W17	99			19W07	99	
4W17	900	I 01 21	2+15	19w07	900	H25 1
4W17	901	102 21		15W07	901	H25 8
4W17	902	103 21	2+17	19W07	902	H25 28
4W17	903	104 21		15W07	903	H26 1
4W17	904	105 21	2+19	15W07	904	H26 8
4W17	905	108 21	2*20	19W07	905	H26 28
4W17	906	109 21	2*21	15W07	906	H27 1
4W17	907	110 21	2+22	15W07	907	H27 8
4W17	908			19W07	908	

W18	00			READ	DÍSTRI	UT	R TO LOWER REGISTER	7W06	0.0			
W18	90	111	28	2*23	MEMORY	ŤO	REGISTER	7W06	90	842	26	
W18	91	112	28	2+24	MEMORY	TO	REGISTER	7W06	91	C37	2	
118	92	114	28	2+25	MEMORY	TO	REGISTER	7W06	92	C37	26	
W18	93	115	28	2+26	MEMORY	TO	REGISTER	7W06	93	C38	2	
W18	94	116	28	2+27	MEMORY	ŤO	REGISTER	7W06	94	C38	26	
W18	95	117	28	2+28	MEMORY	TO	REGISTER	7W06	95	<b>C39</b>	2	
W18	96		28	2+29	MEMORY	TO	REGISTER	7W06	96	<b>C39</b>	26	
W18	97		<b></b>			a province and .	production with the second section of the section of the section	7W06	97			
W18	98							7W06	98			
W18	99						the test than the field that the field the test than the test than the test than the test that it is the field the test the test than the test	7W06	99		mg Mills and 1986 and 1986 18 5 1866 71	
W16	900	101	28	2+15	MEMORY	ŤO	REGISTER	7W06	900	B38	26	
W18	901		28	2+16	MEMORY	TO	REGISTER	7W06	901	B39	2	
W18	902		28	2+17	MEMORY	ŤΟ	REGISTER	7W06	902	B39	26	
W18	903		28	2+18	MEMORY	ŤÖ	REGISTER	7w06	903	B40	2	
W18	904		28	2+19	MEMORY	TO	REGISTER	7W06	904	B40	26	
W18	905		28	2+20	MEMORY	TO	REGISTER	7W06	905	B41	2	
W18	906		28	2+21	MEMORY	TO	REGISTER	7W06	906	841	_	
W18	907	7 75	28	2+22			the contract of the contract o	7W06	907	B42		
W18	908		_ 0			, 5		7W06	908		_	

4W19	00			READ DISTRIBUTOR TO CONTROL	5W18	00		
4W19	90				5W18	90		
4W19	91				5W18	91	***************************************	
4W19	92				5W18	92		
4W19	93				5W18	93		*** *** *** *** *** *** *** *** *** **
4W19	94				5W18	94		
4W19	95				5w18	95		
4W19	96				5W18	96		
4W19	97				5W18	97		
4W19	98				5W18	98		
4W19	99				5W18	99		
4W19	900				5W18	900		
4W19	901	120	5	MEMORY GO	5W18	901	F42	27
4W19	902	120	7	MEMORY WRITE	5W18	902	K38	6
4W19	903			The second secon	5W18	903		
4W19	904	120	24	READ TO PERIPHERAL	-5W18	904	K38	- 4
4W19	905				5W18	905		
4W19	906				5W18	906		
4W19	907				5W18	907		And the same and the first one of the same
4W19	908				5W18	908		

4W20	0.0			MEMORY ADDRESS FROM CONTROL	5W28	0.0		
4W20	90	G01	18	2+8 ADDRESS TO MEMORY	5w28	90	<b>Q</b> 36	18
4W20	91	002	11	2+9 ADDRESS TO MEMORY	5W28	91	Q37	7
4W20	92	002	14	2+10 ADDRESS TO MEMORY	5W28	92	Q37	3
4W20	93		17	2+11 ADDRESS TO MEMORY	5W28	93	Q37	18
4W20	94	G02	18	2+12 ADDRESS TO MEMORY	5W28	94	038	7
4W20	95	· · · · · · · · · · · · · · · · · ·	11	2+13 ADDRESS TO MEMORY	5W28	95	038	3
4W20	96		14	2+14 ADDRESS TO MEMORY	5W28	96	038	18
4W20	97	and the second second	17	2+15 ADDRESS TO MEMORY	5W28	97	Q39	7
4W20	98		18	2+16 ADDRESS TO MEMORY	5w28	98	039	3
4W20	99				5w28	99		💆
4W20	900	G13 :	12	CLOCK	5W28	900	J39	6
4W20	901		18	2+0 BANK SELECT	5W28	901	034	7
4W20	902		22	2+1 BANK SELECT	5W28	902	034	3
4W20	903	G04	8	A CONTRACTOR OF THE PROPERTY O	5W28	903	034	18
4W20	904	G04	4	2+3 CHASSIS 4 SELECT (001)	5W28	904	035	7
4W20	905	and the second of	14	2+4	5W28	905	035	3
4W20	906		11	2+5 ADDRESS TO MEMORY	5W28	906	Q35	18
4W20	907		14	2+6 ADDRESS TO MEMORY	5w28	907	036	7
4W20	908		17	2+7 ADDRESS TO MEMORY	5w28	908	Q36	3

4W21	00			READ DISTRIBUTOR FROM MEMORY	16407	00			
4W21	90	111	23	2+23	16W07	90	H09	28	
4W21	91	112	23	2+24	16W07	91	H11	1	
4W21	92	114	23	2.25	16W07	92	H11	8	
4W21	93	115	23	2.26	16W07	93	H11	28	
4W21	94	116	23	2+27	16W07	94	H12	1	
4W21	95	117	23	2.28	16W07	95	H12	8	
4W21	96		23	2+29	16W07	96	H12		
4W21	97				16W07	97			
4W21	98				16W07	98			
4W21	99				16W07	99			100 March 21
4W21	900	101	23	2+15	16W07	900	HO7	1	
4W21	901	102		2+16	1607	901	HO7	8	
4W21	902			2+17	16007	902	H07	28	
4W21	903	104	23	2.18	16407	903	HO8	1	
4W21	904		23	2+19	16W07	904	Hg8	ā	
4W21	905		23	2+20	1607	905	HOB	28	
4w21	906		23	2.21	16W07	906	H09	- 4	
4W21	907	110		2.2	16W07	907	H09	<del>-</del>	
4W21	908				16W07	908		•	

4W24	00			DISK	SŸNC	PASSON	(OUTPUT)
4W24	90	K02	8	2*0	OÙTR	UT DATA	
4W24	91	KD2	6	2+1	OUTP	UT DATA	The second secon
4W24	92	K02	Ā	2+2	OUTP		
4W24	93	K02	27	2+3	OUTP		
4W24	94	K02	25	2+4	OUTP		
4W24	95	KO2		2+5	DUTP	of the second second second second	
4W24	96	K03	8	2+6	OUTP		
- 21 To - 1	97	K03		2.7	OUTP		The same of the state of the st
4W24			6	Ξ .			
4W24	98	K03	4:	2+8	OUTP		
4W24	99			2+9	OUTP		
4W24	900	K03	25	2+10	OUTP	and the same of the same	
4W24	901	K03	23	2+11	OUTP	UT DATA	
4W24	902	K01	23	ACTIV	/E		
4W24	903	K01	8	INAC	TIVE		THE STATE OF THE PROPERTY OF T
4W24	904	K01	6	FULL			
4W24	905	K01	27	EMPTY	<i>ī</i>		The state of the s
4W24	906	K01	4	FUNC			
4W24	907	K01	25		R CL	EAD	
		VOT	2 9	MASIE	= R - U = 1	EAR	
4W24	908					Management on the spirit for the second	CONTROL OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF T

4W25	00			DISK	SŸNO	PASSON	(INPUT)	
4W25	90	L09	5	2+0	INPU	T DATA		
4W25	91	L09	7	2+1	INPU'	TDATA		, and the second
4W25	92	L09	10	2+2	INPU			
4W25	93	L09	21	2+3	INPU	and the same of th		
4W25	94	L09	24	2+4	INPU'			
4W25	95	L09	26	2+5	INPU'	der in the second second second second	and the second s	and before the time that the time that the time time time time time time time tim
4W25	96	L n 8	-5	2+6	INPU			
4W25	97	L08	7	2+7	INPU		ner en consultarios i agrecionary acomposamento per en estación de el 1994 consultar e transfer atomició acomposamen <sub>os</sub>	A STATE OF THE PARTY OF THE PAR
4W25	98	Ln8	1 n	2+8	INPU'	· + + + + + + + + + + + + + + + + + + +		
4W25	99	L08	21	2+9	INPU	the same of the district terms in the same of the	* 1 TO THE PROPERTY OF THE PRO	to the second section and section and the sect
4W25	900	L08		2+10	INPU'	-		
4W25	901	L08				DATA	•	AND NOTICE CONTINUES THE CONTINUES OF THE PART AND AND NOTICE CONTINUES CONTINUES OF THE CONTINUES CONTINU
4W25	902			ACTIV		, ,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,		
4W25	903	L07	5	INAC			AND THE PARTY OF T	
4W25	904	L07		FULL				
4W25	905	E 0 /		EMPT	J			THE WAY AND THE REST OF THE AND THE WAY AND THE REST OF THE REST OF THE PARTY OF TH
4W25	906	L26	4	₩rir i	,			
		L26					*** **********************************	the state of the s
4W25	907	L20	7.2					
4W25	908				21.49.2021 1.69.5.1408		mandam construct a new commencer interesting on translations. I see a second of the construction of the co	

4W26	0.0		DISK SYNC TO CHANNEL (INPUT)
4W26	90	K05 8	2+0 INPUT DATA
4W26	91	K05 6	2+1 INPUT DATA
4W26	92	K05 4	2+2 INPUT DATA
4W26	93	K05 27	2+3 INPUT DATA
4W26	94	K05 25	2+4 INPUT DATA
4W26	95	K05 23	2+5 INPUT DATA
4W26	96	K06 8	2+6 INPUT DATA
4W26	97	K06 6	2+7 INPUT DATA
4W26	98	K06 4	2+8 INPUT DATA
4W26	99	K06 27	2+9 INPUT DATA
4W26	900	K06 25	2+10 INPUT DATA
4W26	901	K06 23	2+11 INPUT DATA
4W26	902	K04 4	ACTIVE
4W26	903	K04 8	INACTIVE
4W26	904	K04 6	FULL
4W26	905	K04 27	TEMPTY
4W26	906	L26 3	
4W26	907	L26 13	
4W26	908		
"	, , ,		

2. G. HALLING SELECT A THEORETICAL SERVICES.

4W27	00			DISK SYNG TO CHANNEL (OUTPUT)
4W27	90	L01	5	2+0 OUTPUT DĂTÁ
4W27	91	L01	7	2+1 OUTPUT DATA
4W27	92	L01	10	2#2°OÙŤPUŤ ĎÁŤÁ
4W27	93	L01	21	2+3 OUTPUT DATA
4W27	94	L01	24	2+4 OUTPUT DATA
4W27	95	L01	26	2+9 OUTPUT DATA
4W27	96	L02	- 5	2+6 OUTPUT DATA
4W27	97	L02	7	2+7 OUTPUT DATA
4W27	98	L02	1.0	2+8 QUTPUT DATA
4W27	99		21	2+9 OUTPUT DATA
4W27	900	L02	24	2+10 OUTPUT DATA
4W27	901		26	2+11 OUTPUT DATA
4W27	902	L03	7	ACTIVE
4W27	903		21	INACTIVE
4W27	904	L03	10	FUEL
4W27	905		26	EMPTY
4W27	906	L03	5	FUNCTION
are an area area.				The state of the s
4W27	907	L03	24	MASTER : CLEAR
4W27	908			

4W28	00			DISK SÝNC	'O DISK	FILE	COUTPUT	T)				
4W28	90	J06	8	2+0 WRITE	JATA							
4W28	91	J06	6	2+1 WRITE	JATA		The transfer of the same of the same of the	TO THE PART OF THE PART OF THE PARTY.		THE REAL PROPERTY OF THE PARTY	market and the comments of the control of the contr	and the parameters of the para
4W28	92	J06	4	2+2 WRITE								
4W28	93	J06	27	2+3 WRITE	ATA							AND AREA COMMISSION AND AND AREA OF THE SECON
4W28	94	J06	25	2.4 WRITE	JATA							
4W28	95	J06	23	2.5 WRÎTE	ATA							
4W28	96	J05	8		ATA							
4W28	97	J05	6	2+7 WRITE			#1. # 4 to 10 to 1		Promis como o solutilizaren en en esta en esta en esta en esta en esta en esta en esta en esta en esta en esta		ern is Mills speakings (1 m y histories) — histories	
4W28	98	J05	4	2+8 WRITE								
4428	99	J05	27	2+9 WRITE								
4W28	900	J05	25	2+10 WRITE								
4W28	901	J05	23	2+11 WRITE								
4W28	902	J07	8									
4W28	903	J07	6	***************************************	married to a company one transmission and	recognitions consists there are a	******************	programme and the rest summering contact that yet	MATERIAL COLORS AND SAPERFUL LIBERTY COLORS		har a fatoric acceptant materials and desire and	y
4W28	904	J07	4									
4W28	905	J07	27	Commence of the party of the pa								
4W28	906											
4W28	907											
4W28	908											
7 7 6 9	7.3.2			er n <del>ajmeje majdelak kaje</del> rinske, <del>ajak</del> et ke i se krej ges yn ke sersendrejk se erteke	Mention of the Late of the Common of				" " o weekeek	m rate or at a tree	control of the sector of the sector of	F 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4

4W29	0.0	the prime page to the right spice store after 1888 again, ann gains.	DISK SYNG TO DISK FILE (INPUT)
4W29	90	J01 12	2+0 READ DATA ANALOG
4W29	91	J01 5	2+1 READ DATA ANALOG
4W29	92	J01 26	2+2 READ DATA ANALOG
4W29	93	J01 19	2+3 READ DATA ANALOG
4W29	94	J02 12	2+4 READ DATA ANALOG
4W29	95	J02 5	2+5 READ DATA ANALOG
	96		
4W29		J02 26	
4W29	97	J02 19	2+7 READ DATA ANALOG
4W29	98	J03 12	2+8 READ DATA ANALOG
4W29	99	J03 ≒5	2+9 READ DATA ANALOG
4W29	900	J03 26	2+10 READ DATA ANALOG
4429	901	J03 19	2+11 READ DATA ANALOG
4W29	902	J04 12	REV. MARK
4W29	903	J04 5	GLOCK
4W29	905		4200
4W29	906		
4W29	907		
4W29	908		

W39	0.0			MEMORY TO READ DISTRIBUTOR	4W39	0.0		
W39	90	H07	1	2+15	4W39	90	101	3
W39	91	HO7	8	2+16	4439	91	102	3
W39	92	H07	28	2+17	4W39	92	103	- 3
W39	93	H08	1	2+18	4W39	93	104	3
W39	94	H08	8	2+19	4W39	94	105	3
W39	95	H08	28	2+20	4439	95	108	3
W39	96	H09	1	2+21	4W39	96	109	3
W39	97	H09	8	2+22	4W39	97	110	3
W39	98	H09	28	2+23	4W39	98	111	3
W39	99	H11	1	2+24	4W39	99	112	3
W39	900	H11	8	2+25	4W39	900	114	. 3
W39	901	H11	28	2+26	4W39	901	115	3
W39	902	H12	1	2+27	4W39	902	116	3
W39	903	H12	8	2+28	4439	903	117	3
W39	904	H12	28	2+29	4W39	904	118	3
W39	905	G 0 4	21	GO	4W39	905	121	24
W39	906	G 0 4	50	WRITE	4W39	906	124	28
W39	907	121	13	READ PERIPHERAL	4W39	907	119	12
W39	908				4W39	908		

and the service of th

4439	00			READ DISTRIBUTOR FROM MEMORY	4W39	00		en our sous roo des ren c
4839	90	101	3	·2+15	-4W39	90	H07 1	
4839	91	102	3	2+16	-4W39	91	H07 8	-
4W39	92	103	3	2+17	-4W39	92	H07 28	
4839	93	104	3	2+18	4439	93	H08 1	
4W39	94	105	3	2+19	4W39	94	H08 8	
4W39	95	108	3	2+20	4439	95	H08 28	
4W39	96	109	3	2 × 2 1	4W39	96	H09 1	
4439	97	110	3	2+22	-4W39	97	H09 8	
4439	98	111	3	2 23	4W39	98	H09 28	
4W39	99	112	3	2+24	4W39	99	H11 1	
4W39	900	114	3	2 • 25	4W39	900	H11 8	
4W39	901	115	3	2+26	4W39	901	H11 28	
4439	902	116	3	2+27	4W39	902	H12 1	
4W39	903	117	3	2+28	4W39	903	H12 8	
4839	904	118	3	2+29	4W39	904	H12 28	
4W39	905	121	24	GO	4W39	905	004 21	
4839	906	124	28	WRITE	4W39	906	G04 20	
4W39	907	119	12	READ PERIPHERAL	4W39	907	121 13	
4839	908				4W39	908		

5W01	00			CONTROL	FROM READ DIS	TRIBUTOR	4W14	00		
5w01	90	C42	27	2*20			4W14	90	108	26
5W01	91	D41	4	2+21			4W14	91	109	26
5W01	92	D41	- 6	2+22			4W14	92	110	26
5W01	93	D41	7	2+23	and the same of th	A man and developes their state and state at the control of the control of the	4W14	93	111	26
5W01	94	D41	22	2*24			4W14	94	112	26
SW01	95	D41	25	2+25		The said with the day of the Ann and the said th	4W14	95	114	26
5W01	96	D41	27	2+26			4W14	96	115	26
5W01	97	D42	4	2+27			4W14	97		26
SW01	98	D42	6	2+28			4W14	98	117	26
BW01	99	D42	7	2+29	the rate work with their time had to take the time of the rate with the rate of the time to		4W14	99	118	26
5W01	900	C42	25	2+19			4W14	900	105	26
5W01	901		10000				4W14	901		
5W01	902						4W14	902		
SW01	903			and the state of t	ne differit indynamiet is en disclinifique montratificacy montreparture meter y antoniole a provintiu etc.		4W14	903		
5W01	904	C42	- 4	2+15			4W14	904	101	26
5W01	905	C42	6	2+16	an dan 1855 also April v ne volv han the than bell the dear total comment of the same total comment of the site of the same total comment of the sam	and the second s	4W14	905	102	26
5W01	906	C42	7	2+17			4W14	906	103	26
5W01	907	C42	22	2*18	W (1. *		4W14	907	104	man you want out out of
5w01	908			<del>-</del> -			4W14	908	• • •	

5W02	0.0			CONTROL	FROM	READ	DISTRIBUTOR		3W14	00		
5W02	90	E41	7	2+5		,			3W14	90	132	26
5W02	91	E41	22	2+6	• • • • • • • • • • • • • • • • • • • •		The state of the s		3W14	91	133	26
5W02	92	E41	25	2+7					3W14	92	134	26
5W02	93	E41	27	2+8					3W14	93	135	
5W02	94	E42	4	2+9					3W14	94		26
5W02	95	E42	6	2+10					3W14	95	138	26
5W02	96	E42	7	2+11					3W14	96	139	26
5W02	97	E42	22	2+12		••			3W14	97	140	26
5W02	98	E42	25	2+13					3W14	98	141	
5W02	99	E42	27	2+14					3W14	99	142	26
5W02	900	E41	6	2+4					3W14	900	129	
5W02	901		_				* *	**	3W14	901		
5W02	902								3W14	902		
5W02	903			epina andriferi i super ranga erdifikenserandikanse sover na en r			A CANADA TO THE CANADA SPECIAL	e on the same of the same of the same	3W14	903		e estrum rumatum en e
5w02	904	D42	22	2+0					3W14	904	125	26
5W02	905	D42	25	2+1			***	· • •	3W14	905	126	26
5W02	906		27	2+2					3W14	906	127	26
5W02	907	E41	4	2+3					3W14	907	128	26
5w02	908	- 7 <u>-</u>	7						3W14	908		W

5W03	00		CONTROL FROM READ DISTRIBUTOR	10W10 00
5w03	90	A42: '7	2+53	10W10 90 I11 26
5WO3	91	A42:22	2+54	10W10 91 112 26
5W03	92	A42 25	2+55	10W10 92 114 26
5w03	93	A42:27	2+56	10W10 93 115 26
5W03	94	841\ 4	2.57	10W10 94 116 26
5W03	95	B41 6	¹2.58	10W10 95 I17 26
SW03	96	841 7	2+59	10W10 96 118 26
5W03	97			10W10 97
5w03	98			10W10 98
5w03	99			10W10 99
5w03	900	A41 4	2+45	10W10 900 101 26
5w03	901	A41 6	2.46	10W10 901 102 26
5w03	902	A41 7	2+47	10W10 902 103 26
5w03	903	A41 22	2.48	10W10 903 104 26
5w03	904	A41 25	2.49	10W10 904 105 26
5 W 0 3	905	A41 27	2.50	10W10 905 108 26
5w03	906	A42 4	2.51	10W10 906 109 26
5 w 0 3	907	A42 6	2,52	10W10 907 I10 26
5w03	908		~ n = 6	10W10 908

5W04	00		CONTROL FROM READ DISTRIBUTOR	9W10	00		The transfer of the second sec
5W04	90	B42 27	2+38 BUFFER TO CONTROL	9W10	90	135	26
5W04	91	C41 4	2+39 BUFFER TO CONTROL	9W10	91	136	26
5W04	92	C41 6	2.40 BUFFER TO CONTROL	9W10	92	138	26
5W04	93	C41 7	2+41 BUFFER TO CONTROL	9W10	93	139	26
5W04	94	C41 22	2+42 BUFFER TO CONTROL	9W10	94	140	26
5W04	95	C41 25	2+43 BUFFER TO CONTROL	9W10	95	141	26
5W04	96	C41 27	2+44 BUFFER TO CONTROL	9W10	96	142	26
5W04	97		energia di cale pila mati di di dan dan 186 dajanan da di sama di dajanan da dajanan da da dajanan da da dajanan da da dajanan da da dajanan da dajanan da dajanan da dajanan da dajanan da dajanan da dajanan daj	9W10	97		The second secon
5W04	98		•	9W10	98		
5W04	99		we can see your day you. The can be see that the see that the seed of the see that	9W10	99		
5W04	900	B41 22	2+30 BUFFER TO CONTROL	9W10	900	125	26
5W04	901	B41 25	2+31 BUFFER TO CONTROL	9W10	901		26
5W04	902	B41 27	2+32 BUFFER TO CONTROL	9W10	902		26
5W04	903	B42 4	2+33 BUFFER TO CONTROL	9W10	903		26
5W04	904	B42 6	2+34 BUFFER TO CONTROL	9W10	904		26
5W04	905	B42 7	2+35 BUFFER TO CONTROL	9W10	905		26
5w04	906	B42 22	2+36 BUFFER TO CONTROL	9W10	906		26
5W04	907	B42 25	2+37 BUFFER TO CONTROL	9W10	907	134	APPL TO 1
5W04	908	U-1	ato, por the tolername	9W10	908		W U

5W08	00			INCREMENT RESULT	7W21	00	
5W08	90	F41	27	⊹BÍŤ∷9	<sup>.</sup> 7W21	90	F42 26
SW08	91	F42	4	BIT 10	7W21	91	<b>G38</b> 2
5W08	92	F42	6	BÍT 11	7W21	92	G38 26
5W08	93	F42	8	BIT 12	7W21	93	G39 2
5 W 0 8	94	F42	10	BIT 13	7w21	94	G39 26
5W08	95	F42		B!T 14	7W21	95	G40 2
5W08	96	F42		BÎT 15	7W21	96	G40 26
5W08	97	F42		Bi7 16	7W21	97	G41 2
5 W Q 8	98	F42	23	BIT 17	7W21	98	G41 26
5W08	99	i			7W21	99	and the same and t
5W08	900	F41	4	BITO	7W21	900	F38 2
5W08	901	F41	6	BIT 1	7W21	901	F38 26
5W08	902	F41	8	BIT 2	7W21	902	F39 2
5W08	903	F41	10	BIT 3	7W21	903	F39 26
5W08	904	F41	12	BIT 4	7W21	904	F40 2
5W08	905	F41	19	817 5	7W21	905	F40 26
SW08	906	F41	21	BIT 6	7W21	906	F41 2
5W08	907	F41	own year disk of the	Bit	7W21	907	F41 26
5W08	908	F41		BÍŤ 8	7W21	908	F42 2

5W09	00			CENTRAL PROGRAM ADDRESS TO PERIPHERAL	1002	00		
5W09	90	G41	4	2+0 CENTRAL PROGRAM ADDRESS	1402	90	F37	5
5W09	91	G41	6	2+1 CENTRAL PROGRAM ADDRESS	1402	91	F37	7
5W09	92	G41	8	2+2 CENTRAL PROGRAM ADDRESS	1W02	92	F37	10
5W09	93	G41	10	2+3 CENTRAL PROGRAM ADDRESS	1W02	93	F37	21
5W09	94	G41	12	2+4 CENTRAL PROGRAM ADDRESS	1402	94	F37	
5W09	95	G41	19	2+5 CENTRAL PROGRAM ADDRESS	1W02	95	F37	26
5W09	96	G41	21	2+6 CENTRAL PROGRAM ADDRESS	102	96	F38	5
SWO9	97	G41	23	2+7 CENTRAL PROGRAM ADDRESS	1002	97	F38	7
5W09	98	G41	25	2+8 CENTRAL PROGRAM ADDRESS	1W02	98	F38	10
5W09	99	G41	27	2+9 CENTRAL PROGRAM ADDRESS	1W02	99	F38	21
5W09	900	G42	4	2+10 CENTRAL PROGRAM ADDRESS	1402	900	F38	24
5W09	901	G42	6	2+11 CENTRAL PROGRAM ADDRESS	1W02	901	F38	26
5W09	902	G42	8	2+12 CENTRAL PROGRAM ADDRESS	1402	902	F39	- 5
5W09	903	G42	10	2+13 CENTRAL PROGRAM ADDRESS	1402	903	F39	7
5W09	904	G42	12	2+14 CENTRAL PROGRAM ADDRESS	1W02	904	F39	10
5W09	905	G42	19	2+15 CENTRAL PROGRAM ADDRESS	1W02	905	F39	21
5W09	906	G42	21	2+16 CENTRAL PROGRAM ADDRESS	1W02	906	F39	24
5W09	907	G42	23	2+17 CENTRAL PROGRAM ADDRESS	1W02	907	F39	26
5W09	908			<del>-</del>	1002	908		

And the second s

5W10	00		CENTRAL CONTROL TO MEMORY DISTRIBUTION	2W13	00	
5W10	90	139 4	STORE CONTROL BIT 2+44	2W13	90	A03 10
5W10	91	139 27	STORE CONTROL BIT 2+45	2W13	91	A03 21
5W10	92	139 25	STORE CONTROL BIT 2446	2W13	92	A03 24
W10	93	139 23	STORE CONTROL BIT 2+47	2W13	93	A03 26
W10	94	139 21	STORE CONTROL BIT 2+48	2W13	94	A04 5
W10	95	139 19	STORE CONTROL BIT 2+49	2W13	95	A04 7
W10	96	140 4	STORE CONTROL BIT 2+50	2W13	96	A04 10
W10	97	140 27	STORE CONTROL BIT 2+51	2W13	97	A04 21
W10	98	140 23	STORE CONTROL BIT 2+52	2W13	98	A04-24
W10	99	140 23	STORE CONTROL BIT 2+53	2W13	99	A04 26
W10	900	139 6		2W13	900	A03 7
W10	901	138 25	STORE CONTROL BIT 2+36	2W13	901	A02 5
W10	902	138 23	STORE CONTROL BIT 2+37	2W13	902	A02 7
W10	903	138 21	STORE CONTROL BIT 2+38	2W13	903	A02 10
W10	904	138 19	STORE CONTROL BIT 2+39	2W13	904	A02 21
W10	905	139 12	STORE CONTROL BIT 2+40	2W13	905	A02 24
W10	906	139 10	STORE CONTROL BIT 2+41	2W13	906	A02 26
W10	907	139 8	STORE CONTROL BIT 2+42	2W13	907	A03 5
W10	908			2W13	908	

1	0.0			CONT	ROL TO UPPER REGISTER	8W13	00	na 1000 2000 1000 2000 2000 2000	
1	90	F42	25	INCR	SIGN	8W13	90	E 0 6	26
1	91			THE ROPE WATER	Commence of the Control of the Contr	8W13	91		
L	92	E39	23	ADD	PLUS	8W13	92	G01	26
L	93					8W13	93		
	94					8W13	94		
	95					8W13	95		
	96			Transport Comment of the		8W13	96		
	97					8W13	97		
	98					8W13	98		
	99					8W13	99		
	900	042	24	LONG	ADD INDEFINITE	8W13	900	HQ6	12
,	901	042	21	LONG	ADD INFINITE	8W13	901	H06	10
	902				and the second s	8W13	902		ryamo grankogo og krist
	903					8W13	903		
	904					8W13	904		
	905					8W13	905		
	906					8W13	906		
,	907					8W13	907		
	908					8W13	908		

5W12	0.0			CONTROL TO LONG ADD, ADD AND SHIFT	8W12	00		
9W12	90	E40	27	LONG ADD IN	8W12	90	H01	5
5W12	91	E40	25	ADD'IN	8W12	91	H01	10
5W12	92	E39	12	MASK	8W12	92	119	9
5W12	93	E39	21	ROUND ADD	8W12	93	HO1	21
5W12	94	E39	19	ADD	8W12	94	HO1	24
5W12	95	E39	8	PACK	8W12	95	016	5
5W12	96	E39	10	UN-PACK	8W12	96	016	10
5W12	97	E38	19	SHIFT (JK)	8W12	97	G16	21
5W12	98	E38	23	SHIFT NOMINAL	8W12	98	G16	26
5W12	99	E38	21	SHIFT LEFT	8W12	99	G16	24
5W12	900	E39	6	ROUND SHIFT	8W12	900	H01	7
5W12	901	J40	21	TRANSMIT LONG ADD	8W12	901	F01	24
5W12	902	J40	12	TRANSMIT SHIFT	8W12	902	001	24
5W12	903	J40	19	TRANSMIT ADD	8W12	903	002	24
5W12	904	J22	19	REQUEST RELEASE SHIFT	8W12	904	H06	4
5W12	905	E39	27	LONG ADD - PLUS	8W12	905	119	5
5W12	906	K22	19	REQUEST RELEASE LONG ADD	8W12	906	H06	8
5W12	907	E40	23	SHIFT IN	8W12	907	H01	26
9W12	908	E39	4	NORM. (SHIFT)	8W12	908	G16	7

5W13	0.0			CONTROL TO DIVIDE	2W21	0.0		
5W13	90				2W21	90		
5W13	91	E40	12	ROUND	2W21	91	J02	7
5W13	92			BOCLEAN	2W21	92		
5W13	93	E38	8	SUM	2W21	93	J01	5
5W13	94	E38	10	DIFFERENCE	2W21	94	J01	7
W13	95	E38	12	PRODUCT	2W21	95	J01	10
W13	96	E40	19	POP. COUNT	2W21	96	J02	10
W13	97	E38	6	COMPLEMENT	2W21	97	J01	24
W13	98	E38	- 4	TRANSFER	2W21	98	J01	21
W13	99	J40	10	TRANSMIT BCOLEAN	2W21	99	L11	18
W13	900	J40	27	TRANSMIT DIVIDE	2W21	900	L11	14
W13	901	<b>-</b>	7.	i i i i i i i i i i i i i i i i i i i	2W21	901		
W13	902	E40	21	GO BOOLEAN	2W21	902	L11	16
W13	903	J40	8	GO DIVIDE	2W21	903	L11	-
W13	904			CLEAR	2W21	904		
W13	905	J24	19	DIVIDE REQUEST RELEASE	2W21	905	E01	8
W13	986	J23	19	BOOLEAN REQUEST RELEASE	2W21	906	E01	6
W13	907	035	10	TRANSMIT MUIT 1	2W21	907	009	Ť
W13	908	035	12	TRANSMIT MUIT 2	2W21	908	009	21

5W14	00		CONTROL TO MULTIPLY	6W16 00
W14	90	J40 4	GO UNIT NO. 1.	6W16 90 I09 23
W14	91	E40 4	ROUND NO. 1	6W16 91 109 3
W14	92	E40 8	DOUBLE NO. 1	6W16 92 IO8 23
3W14	93	J40 23	XMIT RESULT NO. 1	6W16 93 L08 26
W14	94	J40 25	XMÍT RESULT NO. 2	6W16 94 L08 2
5W14	95	J25 19	REQUEST RELEASE	6W16 95 H09 1
W14	96	J39 27	CLOCK	6W16 96 122 12
W14	97		CLEAR	6W16 97
W14	98			6W16 98
W14	99			6W16 99
W14	900	J40 6	GO UNIT NO. 2	6W16 900 J09 23
W14	901	E40 6	ROUND	6W16 901 J09 3
W14	902	E40 10	POÚBLE	6W16 902 J08 23
W14	903		XMIT RESULT	6W16 903
W14	904		XMIT RESULT	6W16 904
W14	905	K21 19	REQUEST RELEASE	6W16 905 K09 1
W14	906			6W16 906
W14	907			6W16 907
W14	908			6W16 908

W15	00		CONTROL TO UPPER REGISTER	8w20 00	
W15	90	142 7	MEMORY TO D GO	8W20 90 G05 17	
W15	91	142 3	MEMORY TO D 0	8W20 91 G05 5	
W15	92	142 28	MEMORY TO D 1	8W20 92 G05 4	
W15	93	142 24	MEMORY TO D 2	8W20 93 G05 16	
W15	94	J42 7	K TO MULTIPLY GO	8W20 94 F02 21	
W15	95	J42 3	K TO MULTIPLY O	8W20 95 F02 5	
W15	96	J42 28	K TO MULTIPLY 1	8W20 96 F02 7	
W15	97	J42 24	K TO MULTIPLY 2	8W20 97 F02 10	
W15	98	H39 24	ADD TO X 2	8W20 98 G07 16	
W15	99			8W20 99	
W15	900	J39 25	GLOCK	8W20 900 121 12	
W15	901	H38 7	MULTIPLY TO X GO	8W20 901 G06 17	•
W15	902	H38 3	MULTIPLY TO X 0	8W20 902 G06 5	
W15	903	H38 28	MULTIPLY TO X 1	8W20 903 G06 4	W., C.,
W15	904	H38 24	MULTIPLY TO X 2	8W20 904 G06 16	
W15	905	H39 7	ADD TO X GO	8W20 905 G07 17	
W15	906	H39 3	ADD TO X 0	8W20 906 G07 5	
W15	907	H39 28	ADD TO X 1	8W20 907 G07 4	
W15	908			8W20 908	

5W16	00		CONTROL TO LOWER REGISTER	<u> 7W17 :00</u>
5W16	90	142: 1	MEMORY TO D 0	7W17 90 G28 4
5W16	91	142 26	MEMORY TO D 1	7W17 91 028 5
5W16	92	142 22	MEMORY TO D 2	7W17 92 028 16
5W16	93	J42 5	K TO MULTIPLY (1) GO	7W17 93 126 21
5W16	94	J42 1	K TO MULTIPLÝ (1) O	7W17 94 126 24
5W16	95	J42 26	K TO MULTIPLY (1) 1	7W17 95 127 5
5W16	96	J42 22	K TO MULTIPLY (1) 2	7W17 96 127 10
5W16	97	J41 1	J TO MULTIPLY (2) 0	7W17 97 I27 26
5W16	98	J41 26	J TO MULTIPLY (2) 1	7W17 98 [27 21
5W16	99	J41 22	J'TO'MÜLTIPLŸ (2) 2	7W17 99 127 24
5W16	900	H42 5	INCREMENT TO A GO	7W17 900 H27 17
5W16	901	H42 1	INCREMENT TO A 0	7W17 901 H27 16
5W16	902	H42 26	INCREMENT TO A 1	7W17 902 H27 4
5W16	903	H42 22	INCREMENT TO A 2	7W17 903 H27 5
5W16	904	141 5	SHIFT TO B GO	7W17 904 K34 17
5W16	905	141 1	SHIFT TO B	7W17 905 K34 16
5W16	906	141 26	SHIFT TO B 1	7W17 906 K34 4
5W16	907	141 22	SHIFT TO B 2	7W17 907 K34 5
5W16	908			7W17 908

5W17	0.0			CONT	ROL TO UP	PER REGISTER	8w21	00	
5W17	90	K42	3	X TO	ADD	0	8W21	90	F01 5
5W17	91	K42	28	X TO	ADD	1	8W21	91	F01 7
5W17	92	K42	24	X TO	ADD	2	8W21	92	F01 10
5W17	93	K42	7	X TO	ADD	GO	8W21	93	F01 21
5W17	94	H40	7	INCR	TO X GO		8W21	94	G08 17
5W17	95	H40	3	INCR	TO X O		8W21	95	G08 5
5W17	96	H40	28	INCR	TO X 1		8W21	96	G08 4
5W17	97	H40	24	INCR	TO X 2	y to the Minimum transfer when the second terms to the second term	8W21	97	G08 16
5W17	98	042	10	LONG	ADD ZERO	CH 5	8W21	98	H06 23
5W17	99	042	7	LONG	ADD SIGN	CH 5	8W21	99	H06 25
5W17	900	J41	7	J To	MULTIPLY	·GO	8W21	900	001 21
5W17	901	J41	3	J To	MULTIPLY	0	8W21	901	G01 5
5W17	902	J41	28	J TO	MULTIPLY	1	8W21	902	601 7
5W17	903	J41		JTO	MULTIPLY	2	8W21	903	001 10
5W17	904	K41	7	X TO	ADD	GO	8W21	904	E01 21
5W17	905	K41	3	X TO	ADD	0.	8W21	905	E01 5
5W17	906	K41	-	X TO	ADD	1	8W21	906	E01 7
9W17	907	K41	24	X TO	ADD	2	8w21	907	E01 10
5W17	908			, 0		-	8W21	908	

5W18	00	de Print della Print sons form data dissa Print della lace	CONTROL FROM READ DISTRIBUTOR	4W19	0.0		. <b> </b>
5W18	90			4W19	90		
5W18	91	:	·	4819	91		
5W18	92			4W19	92		
5W18	93			4W19	93		
5W18	94			4W19	94		
5W18	95			4W19	95		and the same time time and the same time time time to the same time time time time time time time ti
5W18	96			4W19	96		
5W18	97			4W19	97		
5W18	98			4 1 9	98		
5W18	99			4W19	99		and the same way when you was not been done on the same of the sam
5W18	900			4W19	900		
5W18	901	F42 27	MEMORY GO	4W19	901	120	5
5W18	902	K38 6	MEMORY WRITE	4W19	902	120	7
5W18	903			4W19	903		
5W18	904	K38 4	READ TO PERIPHERAL	4W19	904	120	24
BW18	905			4W19	905		A STATE OF THE PARTY AND ADDRESS OF THE PARTY ADDRESS OF THE PARTY ADDRESS OF THE PARTY AND ADDRESS OF THE PARTY AND ADDRESS OF THE PARTY ADDRESS OF THE PARTY ADDRESS OF THE PARTY ADDRESS OF THE PARTY ADDRESS OF THE PARTY ADDRESS OF THE PARTY ADDRESS OF THE PARTY ADDRESS OF THE PARTY ADDRESS OF THE PARTY ADDRESS OF THE PARTY ADDRESS OF THE PART
5W18	906			4W19	906		
5W18	907			4W19	907		
5W18	908		•	4W19	908		

5W19	00	air Aria, maga riako raina dirita titur raina dirita titur saina	CONTROL TO LOWER REGIST	ER 7W18 00
W19	90	K40 1	X TO INCREMENT 0	7w18 90 J27 24
W19	91	K40 26	X TO INCREMENT 1	7W18 91 J28 5
W19	92	K40 22	X TO INCREMENT 2	7W18 92 J28 10
W19	93	M41 5	B TO ADD (1) GO	7W18 93 M34 7
W19	94	M41 1	B TO ADD (1)	7W18 94 M34 26
W19	95	M41 26	8 TO ADD (1) 1	7W18 95 M34 21
W19	96	M41 22	B to ADD (1) 2	7W18 96 M34 24
W19	97			7W18 97
W19	98			7W18 98
W19	99			7W18 99
W19	900	K41 5	XJ TO ADD (1) GO	7W18 900 J28 7
W19	901	K41 1	XJ TO ADD (1) 0	7W18 901 J28 26
W19	902	K41 26	XJ TO ADD (1) 1	7W18 902 J28 21
W19	903	K41 22	XJ TO ADD (1) 2	7W18 903 J28 24
W19	904	K42 5	XL TO ADD (2) GO	7W18 904 126 5
W19	905	K42 1	XL TO ADD (2) 0	7W18 905 126 10
W19	906	K42 26	XL TO ADD (2) 1	7W18 906 126 7
W19	907	K42 22	XL TO ADD (2) 2	7w18 907 126 26
W19	908	N-1- 66	uP   n upm 151	7W18 908

5W20	0.0		CONTROL TO LOWER	REGISTER	7W19 00	
5W20	90	M42 1	A TO INCREMENT	0	7W19 90 G2	6 10
5W20	91	M42 26	A TO INCREMENT	.1	7W19 91 G2	6 7
5W20	92	M42 22	A TO INCREMENT	2	7W19 92 G2	6 26
5W20	93	H39 26	ADD TO X	1	7W19 93 12	
5W20	94	142 5	MEM. TO D	:GO	. =	8 17
5W20	95	K40 5	X TO INCR.	GO		7 21
5W20	96	M42 5	A TO INCR.	g o		6 5
5W20	97				7W19 97	
5W20	98				7W19 98	
5W20	99			- CONT. AND AND AND AND AND AND AND AND AND AND	7W19 99	part and the part
5W20	900	L41 5	BJ TO INGREMENT	GO	7W19 900 L3	4 5
5W20	901	L41 1	BJ TO INCREMENT	0	7W19 901 L3	
5W20	902	L41 26	BJ TO INGREMENT	1	7W19 902 L3	
5W20	903	L41 22	BJ TO INCREMENT	2	7W19 903 L3	
5W20	904	L42 5	BK TO INCREMENT	GO	7W19 904 L3	
5W20	905	L42 1	BK TO INCREMENT	0	7W19 905 L3	
5W20	906	L42 26	BK TO INCREMENT	1	7W19 906 M3	45
5W20	907	L42 22	BK TO INCREMENT	· · · · · · · · · · · · · · · · · · ·		4 10
5W20	908	- ·- <b></b>			7W19 908	

1	0.0			CONTROL (1 TO 5 VIA 3)	3W24	0.0		
1	90				3W24	90		
1	91			Constitution (Managements) and only that Matter a constitution of the constitution of	3W24	91		
1	92				3W24	92		
1	93				3W24	93		
1	94				3W24	94		
1	95				3W24	95		
1	96			Control of the Contro	3W24	96		
1	97				3W24	97		
1	98			Experience of the control of the section of the sec	3W24	98		
1	99				3W24	99		
1	900	041	18	READ/GO (PERIPH)	3W24	900	738	28
1	901	041	20	WRITE GO (PERIPH)	3W24	901	J39	
1	902	040	15	EXCHANGE GO (PERIPH	3W24	902	J39	24
1	903				3W24	903		
1	904				3W24	904		
1	905				3W24	905		
1	906				3W24	906		
1	907				3W24	907		
1	908				3W24	908		

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and the second of the second of the second of the second of the second of the second of the second of the second

5w22	0.0			CONTROL TO LOWER RE	GISTER	7W16	00	
5W22	90	H39	22	ADD TO X	2	7W16	90	128 5
5W22	91	H40	5	INCREMENT TO X	GO	7W16	91	H28 17
5W22	92	H40	1	INCREMENT TO X	0	7W16	92	H28 16
5W22	93	H40	26	INCREMENT TO X	1	7W16	93	H28 4
5W22	94	H40	22	INCREMENT TO X	2	7W16	94	H28 5
5W22	95	H41	:5	INCREMENT TO B	GO	7W16	95	K33 17
5W22	96	H41	1:	INCREMENT TO B	0	7W16	96	K33 16
5W22	97	H41	26	INCREMENT TO B	1	7W16	97	K33 4
5W22	98	H41	22	INCREMENT TO B	2	7W16	98	K33 5
5W22	99	J41	5	XJ TO MULTIPLY (2)	GO	7W16	99	127 7
5W22	900			•		7W16	900	
5W22	901	J39	4	CLOCK		7W16	901	122 12
5W22	902	H38	5		GO	7W16	902	G27 17
5W22	903	H38		MULTIPLY TO X	0	7W16	903	G27 16
5W22	904	H38		MULTIPLY TO X	1	7W16	904	027 4
5W22	905	H38	22		2	7W16	905	G27 5
5W22	906	H39	:5	ADD TO X	GO	7W16	906	128 17
5W22	907	H39	1	ADD TO X	0	7W16	907	128 16
5W22	908					7W16	908	The same of

W23	00	CONTROL TO PERIPHERAL	1W12	00			
W23	90		1W12	90			
W23	91	1 - Company of the Co	1W12	91		THE RESERVE AND ADDRESS OF THE PARTY OF THE	
W23	92		1W12	92			
W23	93		1W12	93			
W23	94		1W12	94			
W23	95		1W12	95			
W23	96		1W12	96			
123	97		1W12	97			.,.,
W23	98		1W12	98			
W23	99		1W12	99			
123	900	040 2 EXCHANGE RESUME OUT	1W12	900	129	5	
123	901	042 26 MC	1W12	901	130	23	
W23	902	122 12 CLOCK	1W12	902	130	4	
W23	903	- A To Pin Manage - Mai Conferences and consequences in the consequences of the cons	1W12	903		The comments of the contract of	
W23	904		1W12	904			
W23	905	And the second of the second o	1412	905			
123	906		1W12	906			
123	907		1W12	907			
W23	908		1W12	908			

5W24	0.0	READPWRITE EXCHANGE ADDRESS	3W23 00
5w24	90		3W23 90
5W24	91	N41 21 BIT 9	3W23 91 J41 28
5W24	92	N41 24 BIT 10	3W23 92 J41 26
5W24	93	N41 26 BIT 11	3W23 93 J41 24
5W24	94	N42 5 BIT 12	3W23 94 J42 7
5W24	95	N42 7 BIT 13	3W23 95 J42 5
5W24	96	N42 10 BIT 14	3W23 96 J42 3
5w24	97	N42 21 BIT 15	3W23 97 J42 28
5W24	98	N42 24 BIT 16	3W23 98 J42 26
5W24	99	N42 26 BIT 17	3W23 99 J42 24
5W24	900	N41 10 BIT 8	3W23 900 J41 3
5W24	901	N40 5 BIT 0	3W23 901 J40 7
5W24	902	N40 7 BIT 1	3W23 902 J40 5
5W24	903	N40 10 BIT 2	3W23 903 J40 3
5W24	904	N40 21 817 3	3W23 904 J40 28
5W24	905	N40 24 BIT 4	3W23 905 J40 26
5W24	906	N40 26 BIT 5	3W23 906 J40 24
5W24	907	N41 5 BIT 6	3W23 907 J41 7
5W24	908	N41 7 Bit 7	3W23 908 J41 9

5W25	00	CONTROL TO LOWER REGISTER	7W20 00
5W25	90		7w20 90
5W25	91	A STATE OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF T	7W20 91
5W25	92		7W20 92
5W25	93	033 6	7W20 93 G37 5
5W25	94		7W20 94
5W25	95		7W20 95
5W25	96		7W20 96
5W25	97		7W20 97
5W25	98	034 4	7W20 98 J26 24
5W25	99		7W20 99
5W25	900	034 12	7W20 900 J26 5
5W25	901	034 10	7W20 901 J26 7
5W25	902	034 8	7W20 902 J26 10
5W25	903	034 6	7W20 903 J26 21
5W25	904	031 6	7w20 904 H26 4
5W25	905	031 5	7W20 905 H26 6
5W25	906	031 26 X TO MEMORY 1	7W20 906 H26 7
5W25	907	031 23	7W20 907 H26 27
5W25	908		7W20 908

5W26	00		CONTROL TO UPPER REGISTER	8W22	0.0		
5W26	91	033 8		8W22	91	E01	24
5W26	92	035 8	ENTER (JK) 2+0	8W22	92	H03	. 4
5W26	93	035 6	ENTER (JK) 2+1	8W22	93	H02	-6
5W26	94	035 4	ENTER (JK) 2+2	AW22	77	HO2	7
5W26	95	035 27	ENTER (JK) 2+3	8W22	95	H02	22
5W26	96	035 25	ENTER (JK) 2+4	8W22	96	HO2	
5W26	97	035 23	ENTER (JK) 2+5	8W22	97	H02	
5W26	98	K23 19	REQUEST RELEASE ADD	W22	98	HOS	- 6
5W26	99	042 5	LONG ADD SER CONTROL	8H22	99	H06	27
5W26	904	031 8		8W22	904	002	21
5W26	905	031 3	X TO MEMORY O	8W22	905	002	= 2 5
5W26	906	031 28	X TO MEMORY 1	8W22	906	002	· 5
5W26	907	031 25	W. I.O. HELLADI. T	8W22	907	602	1 6
5W26	908				988		** V

W27	0.0			CENTRAL CONTROL TO MEMORY DISTRIBUTION	2W14	00	
W27	90	138	12	2+30	2W14	90	A01: 5
W27	91	138	10	2+31	2W14	91	A01 7
W27	92	138	8	2+32	2W14	92	A01 10
W27	93	138	6	2+33	2W14	93	A01 21
W27	94	138	4	2+34	2W14	94	A01 24
127	95	138	27	2+35	2W14	95	A01 26
127	96	140	19	2+56	2W14	96	A05 10
127	97	= 11.7	ana na	- And And And And And And And And And And	2W14	97	
27	98				2W14	98	
127	99				2W14	99	
127	900				2W14	900	
127	901	<b>J</b> 39	8	CLOCK	2W14	901	122 12
127	902	K38	23	STORE CONTROL	2W14	902	A07 7
127	903	K38	25	STORE REGISTERS	2W14	903	A07 10
127	904	K38	27	STORE PERIPHERAL	2W14	904	A07 5
127	905	N23	10	EXP # 1777 ERROR	2W14	905	E01 25
127	906	N23	7	EXP = 3777 ERROR	2W14	906	E01 23
127	907	_			2W14	907	
127	908				2W14	908	

5W28	00		CONTROL TO MEMORY ADDRESS	4W20	00	
5W28	90	036 18	2+6 ADDRESS TO MEMORY	4W20	• 0	001 18
5W28	91	037 7	2+9 ADDRESS TO MEMORY	4W20	91	G02 11
5W28	92	Q37 3	2+10 ADDRESS TO MEMORY	4W20	92	002 14
5W28	93	037 18	2+11 ADDRESS TO MEMORY	4W20	93	G02 17
5W28	94	038	2+12 ADDRESS TO MEMORY	4W20	94	G02 18
5W28	95	038 3	2-13 ADDRESS TO MEMORY	4W20	95	G03 11
5W28	96	038 16	2+14 ADDRESS TO MEMORY	4W20	96	G03 14
5W28	97	039 7	2+15 ADDRESS TO MEMORY	4W20	97	003 17
3W28	98	039 3	2+16 ADDRESS TO MEMORY	4W20	98	G03 18
5W28	99			4W20	99	
5W28	900	J39 6	CLOCK	4W20	900	G13 12
5W28	901	Q34 7	2+0 BANK SELECT	4W20	901	605 18
5W28	902	Q34 3	2+1 BANK SELECT	4W20	902	005 22
5W28	903	034 18	2+2 CHASSIS 4 SELECT (001)	4W20	903	G04 8
5W28	904	035 7	2+3 CHASSIS 4 SELECT (001)	4W20	904	G 0 4 4
5W28	905	Q35 3	2+4 CHASSIS 4 SELECT (001)	4W20	905	G04 14
5W28	906	035 18	2+5 ADDRESS TO MEMORY	4W20	906	G01 11
5w28	907	Q36 7	2+6 ADDRESS TO MEMORY	4W20	907	G01 14
5W28	908	036 3	2+7 ADDRESS TO MEMORY	4W20	908	G01 17

5W29	00			CONTROL TO MEMORY ADDRESS	3W20	00	
5W29	90	036	20	2+8 ADDRESS TO MEMORY	3W20	90	G40 18
5W29	91	037	9	2+9 ADDRESS TO MEMORY	3W20	91	G41 11
5W29	92	Q37	5	2+10 ADDRESS TO MEMORY	3W20	92	G41 14
5W29	93	Q37	20	2+11 ADDRESS TO MEMORY	3W20	93	G41 17
5W29	94	038	9	2+12 ADDRESS TO MEMORY	3W20	94	G41 18
5W29	95	038	5	2+13 ADDRESS TO MEMORY	3W20	95	G42 11
5W29	96	Q38	20	2+14 ADDRESS TO MEMORY	3W20	96	G42 14
5W29	97	039	9	2+15 ADDRESS TO MEMORY	3W20	97	G42 17
5W29	98	039	5	2+16	3W20	98	G42 18
5W29	99	040	17	ACCEPT	3W20	99	123 28
5W29	900	J39	23	CLOCK	3W20	900	G35 12
5W29	901	034	9	2+0 BANK SELECT	3W20	901	G39 18
5W29	902	Q34	5	2+1 BANK SELECT	3W20	902	G39 22
5W29	903	Q34	20	2.2 CHASSIS 3 SELECT(000)	3W20	903	G38 9
5W29	904	035	9	2+3 CHASSIS 3 SELECT (000)	3W20	904	G38 4
5W29	905	035	5	2+4 CHASSIS 3 SELECT (000)	3W20	905	G38 14
5W29	906	035	20	2+5 ADDRESS TO MEMORY	3W20	906	G40 11
5W29	907	036	9	2.6 ADDRESS TO MEMORY	3W20	907	040 14
5W29	908	036	5	2+7 ADDRESS TO MEMORY	3W20	908	640 17

5W30	0.0	ni- Ang Mga Ang Agai agai agai agai an an an an an an an	CONTROL TO MEMORY ADDRESS	9W20	00	ere dette dette dette dette dette dette dette dette dette dette i Princip over vers vers vers vers vers vers vers
5W30	90	036-22	2+6 ADDRESS TO MEMORY	9W20	90	G40 18
5W30	91	037 11	2+9 ADDRESS TO MEMORY	9W20	91	041:11
5W30	92	037 26	2+10 ADDRESS TO MEMORY	9W20	92	041 14
5W30	93	037 22	2+11 ADDRESS TO MEMORY	9W20	93	G41 17
5W30	94	038 11	2+12 ADDRESS TO MEMORY	9W20	94	041 18
5W30	95	938 26	2+13 ADDRESS TO MEMORY	9W20	95	G42 11
5W30	96	038 22	2+14 ADDRESS TO MEMORY	9W20	96	042 14
5W30	97	039 11	2+15 ADDRESS TO MEMORY	9W20	97	G42 17
5W30	98	039 26	2+16 ADDRESS TO MEMORY	9W20	98	G42 18
5W30	99			9W20	99	
5W30	900	M32 8	CLOCK	9W20	900	G35 12
5W30	901	Q34 11	2+0 BANK SELECT	9W20	901	G39 18
5W30	902	Q34 26	2+1 BANK SELECT	9420	902	G39 22
5W30	903	034 22	2+2 CHASSIS 9 SELECT (010)	9W20	903	Q38 9
5W30	904	035 11	2+3 CHASSIS 9 SELECT (010)	9W20	904	038 5
5W30	905	035 26	2+4 CHASSIS 9 SELECT (010)	9W20	905	G38 14
5W30	906	035 22	2+5 ADDRESS TO MEMORY	9W20	906	G40 11
5W30	907	036 11	2+6 ADDRESS TO MEMORY	9W20	907	G40 14
5W30	908	036 26	2+7 ADDRESS TO MEMORY	9W20	908	G40 17

5W31	00		CONTROL TO MEMORY ADDRESS	10020	00	which were the control of the contro	
5W31	90	Q36 24	2+8 ADDRESS TO MEMORY	10W20	90	G01 18	
5W31	91	037 13	2+9 ADDRESS TO MEMORY	10W20	91	002 11	
5W31	92	937 28	2+10 ADDRESS TO MEMORY	10W20	92	002 14	,
5W31	93	037 24	2+11 ADDRESS TO MEMORY	10W20	93	G02 17	
5W31	94	038 13	2+12 ADDRESS TO MEMORY	10W20	94	002 18	
5W31	95	038 28	2+13 ADDRESS TO MEMORY	10W20	95	G03 11	(
5W31	96	038 24	2+14 ADDRESS TO MEMORY	10W20	96	G03 14	1
5W31	97	039 13	2+15 ADDRESS TO MEMORY	10W20	97	G03 17	
5W31	98	039 28	2+16 ADDRESS TO MEMORY	10020	98	G03 18	
5W31	99			10W20	99		(
5W31	900	M32 6	CLOCK	10W20	900	G13 12	
5W31	901	Q34 13	2+0 BANK SELECT	10W20	901	G05 18	
5W31	902	034 28	2+1 BANK SELECT	10020	902	G05 22	
5w31	903	Q34 24	2+2 CHASSIS 10 SELECT (011)	10W20	903	G04 8	
5W31	904	035 13	2+3 CHASSIS 10 SELECT (011)	10020	904	G04 5	
5W31	905	Q35 28	2+4 CHASSIS 10 SELECT (011)	10W20	905	004 14	
5W31	906	Q35 24	2+5 ADDRESS TO MEMORY	10W20	906	001 11	4
5w31	907	036 13	2.6 ADDRESS TO MEMORY	10W20	907	001 14	. (
5W31	908	036 28	2+7 ADDRESS TO MEMORY	10W20	908	G01 17	

3W32	0.0		CONTROL TO MEMORY ADDRESS	13W18 00
W32	90	042 18	2+8 ADDRESS TO MEMORY	13w18 90 G40 18
3W32	91	R40 7	2+9 ADDRESS TO MEMORY	13W18 91 G41 11
W32	92	R40 3	2+10 ADDRESS TO MEMORY	13W18 92 G41 14
W32	93	R40 18	2+11 ADDRESS TO MEMORY	13W18 93 G41 17
W32	94	R41 7	2+12 ADDRESS TO MEMORY	13W18 94 G41 18
W32	95	R41 3	2+13 ADDRESS TO MEMORY	13W18 95 G42 11
W32	96	R41 18	2+14 ADDRESS TO MEMORY	13W18 96 G42 14
W32	97	R42 7	2+15 ADDRESS TO MEMORY	13W18 97 G42 17
W32	98	R42 3	2+16 ADDRESS TO MEMORY	13W18 98 G42 18
W32	99			13W18 99
W32	900	M32 4	CLOCK	13W18 900 G35 12
W32	901	Q40 7	2+0 BANK SELECT	13W18 901 G39 18
W32	902	Q40 3	2+1 BANK SELECT	13W18 902 039 22
W32	903	Q40 18	2+2 CHASSIS 13 SELECT (100)	13W18 903 G38 9
W32	904	041 7	2+3 CHASSIS 13 SELECT (100)	13W18 904 G38 4
W32	905	Q41 3	244 CHASSIS 13 SELECT (100)	13W18 905 G38 13
W32	906	041 18	2+5 ADDRESS TO MEMORY	13W18 906 G40 11
W32	907	042 7	2+6 ADDRESS TO MEMORY	13W18 907 G40 14
W32	908	042 3	2+7 ADDRESS TO MEMORY	13W18 908 G40 17

5W33	0.0		CONTROL TO MEMORY ADDRESS	14W18	0.0
5w33	90	042 20	2+8 ADDRESS TO MEMORY	14W18	90 G01 18
5w33	91	R40 9	2+9 ADDRESS TO MEMORY	14W18	91 G02 11
5W33	92	R40 5	2+10 ADDRESS TO MEMORY		92 602 14
5W33	93	R40 20	2+11 ADDRESS TO MEMORY		93 G02 17
5W33	94	R41 9	2+12 ADDRESS TO MEMORY		94 002 18
5W33	95	R41 5	2+13 ADDRESS TO MEMORY		95 603 11
5W33	96	R41 20	2+14 ADDRESS TO MEMORY		96 003 14
5W33	97	R42 9	2+15 ADDRESS TO MEMORY		97 G03 17
5W33	98	R42 5	2+16 ADDRESS TO MEMORY		98 603 18
5W33	99				99
5w33	900	M32 27	CLOCK		00 G13 12
5W33	901	Q40 9	2+0 BANK SELECT		01 G05 18
5W33	902	040 5	2+1 BANK SELECT		02 005 22
5W33	903	Q40 20	2+2 CHASSIS 14 SELECT (101)	The second secon	03 004 8
5w33	904	041 9	2+3 CHASSIS 14 SELECT (101)	14W18 9	04 004 4
5W33	905	Q41 5	2+4 CHASSIS 14 SELECT (101)		05 G04 13
5W33	906	041 20	2+5 ADDRESS TO MEMORY		06 G01 11
5W33	907	042 5	2+6 ADDRESS TO MEMORY	*	07 G01 17
5w33	908	042 9			08 G01 14

5W34	00		CONTROL TO MEMORY ADDRESS	15W18	00	
5w34	90	042 22	2+8 ADDRESS TO MEMORY	15W18	90	G40 18
5W34	91	R40 11	2+9 ADDRESS TO MEMORY	15W18	91	G41 11
5W34	92	R40 26	2+10 ADDRESS TO MEMORY	15W18	92	G41 14
5W34	93	R40 22	2+11 ADDRESS TO MEMORY	15W18	93	G41 17
14. WE	94	R41 11	2+12 ADDRESS TO MEMORY	15W18	94	041 18
5W34	95	R41 26	2+13 ADDRESS TO MEMORY	15W18	95	G42 11
5W34	96	R41 22	2+14 ADDRESS TO MEMORY	15W18	96	G42 14
5W34	97	R42 11	2+15 ADDRESS TO MEMORY	15W18	97	G42 17
5W34	98	R42 26	2+16 ADDRESS TO MEMORY	15W18	98	G42 18
5W34	99			15W18	99	and the second s
5W34	900	M32 25	GLOCK	15W18	900	G35 12
5W34	901	040 11	2+0 BANK SELECT	15W18	901	G39 18
5W34	902	Q40 26	2+1 BANK SELECT	15W18	902	G39 22
W34	903	Q40 22	2+2 CHASSIS 15 SELECT (110)	15W18	903	G38 9
5W34	904	041 11	2+3 CHASSIS 15 SELECT (110)	15W18	904	G38 5
W34	905	Q41 26	2+4 CHASSIS 15 SELECT (110)	15W18	905	G38 13
5W34	906	041 22	2+5 ADDRESS TO MEMORY	15W18	906	G40 11
5W34	907	042 11	2+6 ADDRESS TO MEMORY	15W18	907	G40 14
5W34	908	Q42 26	2+7 ADDRESS TO MEMORY	15W18	908	G40 17

5W35	0.0		CONTROL TO MEMORY ADDRESS	16W18	00	
5w35	90	042 24	2+8 ADDRESS TO MEMORY	16W18	90	G01 18
5W35	91	R40 13	2+9 ADDRESS TO MEMORY	16W18	91	G02 11
5W35	92	R40 28	2+10 ADDRESS TO MEMORY	16W18	92	G02 14
5W35	93	R40 24	2+11 ADDRESS TO MEMORY	16W18	93	G02 17
5W35	94	R41 13	2+12 ADDRESS TO MEMORY	16W18	94	G02 18
5W35	95	R41 28	2+13 ADDRESS TO MEMORY	16W18	95	G03 11
5W35	96	R41 24	2+14 ADDRESS TO MEMORY	16W18	96	G03 14
5W35	97	R42 13	2+15 ADDRESS TO MEMORY	16W18	97	G03 17
5W35	98	R42 28	2+16 ADDRESS TO MEMORY	16W18	98	Gn3 18
5W35	99			16W18	99	
5W35	900	M32 23	CLOCK	16W18	900	G13 12
5W35	901	Q40 13	2+0 BANK SELECT	16W18	901	G05 18
5W35	902	040 28	2+1 BANK SELECT	16W18	902	005 22
5W35	903	Q40 24	2+2 CHASSIS 16 SELECT (111)	16W18	903	G04 8
5W35	904	Q41 13	2+3 CHASSIS 16 SELECT (111)	16W18	904	G04 5
5W35	905	Q41 28	2+4 CHASSIS 16 SELECT (111)	16W18	905	G04 13
5W35	906	Q41 24	2+5 ADDRESS TO MEMORY	16W18	906	G01 11
5W35	907	042 13	2+6 ADDRESS TO MEMORY	16W18	907	G01 14
5W35	908	Q42 28	2+7 ADDRESS TO MEMORY	16W18	908	G01 17

5W36	0 0			INCREMENT	OPERAND	Ţ	) RE	GISTER	7W22	00		
5W36	90	026	7	INCREMENT	OPERAND	J	BIT	2+9	7W22	90	K30	3
5W36	91	027	23	INCREMENT	OPERAND	ij	BIT	2+10	7W22	91	K30	25
9W36	92	027	7	INCREMENT	OPERAND	J	BIT	2*11	7W22	92	K30	21
5W36	93	028	23	INCREMENT	OPERAND	J	BIT	2+12	7W22	93	K31	3
5W36	94	028	7	INCREMENT	OPERAND	J	BIT	2*13	7W22	94	K31	25
5W36	95	029	23	INCREMENT	OPERAND	J	BIT	2+14	7W22	95		21
5W36	96	029	7	INCREMENT	OPERAND	j	BIT	2*15	7W22	96	K32	3
5W36	97	030	7	11	• •				7W22	97	K32	25
5W36	98	030	24						7W22	98	K32	21
5w36	99								7W22	99		
5W36	900	022	23	INCREMENT	OPERAND	J	BIT	2 * 0	7W22	900	K27	3
5W36	901	022	7	INCREMENT	OPERAND	J	BIT	2*1	7W22	901	K27	25
5W36	902	023	23	INCREMENT	OPERAND	J	BIT	2*2	7W22	902	K27	21
5w36	903	023	7	INCREMENT	OPERAND	J	BIT	2+3	7W22	903	K28	3
5W36	904	024	23	INCREMENT	OPERAND	J	BIT	2+4	7W22	904	K28	25
5W36	905	024	7	INCREMENT	OPERAND	J	BIT	2+5	7W22	905	K28	21
5W36	906	025	23	INCREMENT	OPERAND	J	BIT	2*6	7W22	906	K29	3
5w36	907	025	7	INCREMENT	OPERAND	J	BIT	2*7	7W22	907	K29	25
5W36	908	026	23	INCREMENT	OPERAND	J	BIT	2 * 8	7W22	908	K29	21

5w37	00			INCREMENT	OPERAND	T	RE	GISTER	7w23	0.0		
5w37	90	026	3	INCREMENT	OPERAND	K	BIT	2*9	7W23	90	L30	3
5w37	91	027	27	INCREMENT	OPERAND	K	BIT	2*10	7w23	91	L30	25
5W37	92	027	3	INCREMENT	OPERAND	K	BIT	2*11	7W23	92	L30	21
5W37	93	028	27	INCREMENT	OPERAND	K	BIT	2*12	7W23	93	L31	3
5W37	94	028	3	INCREMENT	OPERAND	K	BIT	2*13	7w23	94	L31	25
5w37	95	029	27	INCREMENT	OPERAND	K	BIT	2+14	7W23	95	L31	21
5W37	96	029	3	INCREMENT	OPERAND	K	BIT	2*15	7W23	96	L32	3
5W37	97	030	3						7W23	97	L32	25
5W37	98	030	25						7w23	98	<b>L32</b>	21
5w37	99								7W23	99		
5W37	900	022	27	INCREMENT	OPERAND	K	BIT	2 * 0	7W23	900	L27	3
5W37	901	022	3	INCREMENT	OPERAND	K	BIT	2*1	7W23	901	L27	25
5W37	902	023	27	INCREMENT	OPERAND	K	BIT	2*2	7W23	902	L27	21
5W37	903	023	3	INCREMENT	OPERAND	K	BIT	2+3	7W23	903	L28	3
5W37	904	024	27	INCREMENT	OPERAND	K	BIT	2 * 4	7W23	904	L28	25
5W37	905	024	3	INCREMENT	OPERAND	K	BIT	2*5	7W23	905	L28	21
5W37	906	025	27	INCREMENT	OPERAND	K	BIT	2*6	7W23	906	L29	3
5w37	907	025	3	INCREMENT	OPERAND	K	BIT	2 * 7	7W23	907	L29	25
5W37	908	026	27	INCREMENT	OPERAND	K	BIT	2*8	7W23	908	L29	21

Consider the state of the state

6W07	0.0			MULT	PLY	FROM	RE	GISTER				8W07	0.0			
6W07	90	H04	12									8w07	90	C09	3	
6W07	91	H04	14							The second control of the second control of		8W07	91	C09	21	
6W07	92	H04	16									8W07	92	C09	25	
6W07	93	H04	18									8W07	93	C10	3	
6W07	94	H05	12	2+40	REGI	STER	TO	MULTI		OPERAND	1(XK)	8W07	94	C10	21	
6W07	95	H05	14	2+41	REGI	STER	TO	MULTI	PLY.	OPERAND	1(XK)	8W07	95	C10	25	
6W07	96	H05	16	2+42	REG!	STER	TO	MULTI		OPERAND	1(XK)	8W07	96	C11	3	
6W07	97	H05	18	2+43	REGI	STER	TO	MULTI		OPERAND	1(XK)	8W07	97	C11	21	-
6W07	98	HD6	12	2+44	REGI	STER	TO	MULTI	PLY,	OPERAND	1(XK)	8W07	98	C11	25	,
6W07	99	H06	14	2+45	REGI	STER	TO	MULTI		OPERAND	1(XK)	8W07	99	C12	3	
6W07	900	HQ6	16	2+46	REGI	STER	TO	MULTI		OPERAND	1(XK)	8W07	900	C12	21	
6W07	901	H06	18	2+47	REGI	STER	TO	MÚLTI	PLY,	OPERAND	1(XK)	8W07	901	C12	25	
6W07	902	F07	15	2*59	REGI	STER	TO	MULTI	PLY,	OPERAND	1(XK)	8W07	902	D12	25	
6W07	903											8W07	903			
6W07	904											8W07	904			
6W07	905											8W07	905			
6W07	906											8W07	906			
6W07	907										·	8W07	907			
6W07	908			-	ent company to the control of the con-				44.000.00.00.00.00		1870 Av. Clark 1984 1984	8W07	908			

W 0 8	0.0			MULTIPLY TO REGISTER	8008	00		
W 0 8	90	F03	1	2+36 MULTIPLY TO REGISTER	8 W O 8	90	805	2
W 0 8	91	F03	5	2+37 MULTIPLY TO REGISTER	8 W D 8	91	805	26
WOB	92	F03	25	2+38 MULTIPLY TO REGISTER	8 W 0 8	92	B06	2
W D 8	93	F03	27	2+39 MULTIPLY TO REGISTER	8008	93	B06	26
W 0 8	94	FD4	1	2+40 MULTIPLY TO REGISTER	8008	94	807	2
W D B	95	F04	5	2+41 MULTIPLY TO REGISTER	8 W 0 8	95	B 0 7	26
W 0 8	96	F04	25	2+42 MULTIPLY TO REGISTER	8 W D 8	96	808	2
8 0 W	97	F 0 4	27	2+43 MULTIPLY TO REGISTER	8 W O 8	97	808	26
804	98	F05	1	2+44 MULTIPLY TO REGISTER	8 W Q 8	98	C01	2
8 O W	99	F05	5	2+45 MULTIPLY TO REGISTER	8W08	99	C01	26
8 0 k	900	F05	25	2+46 MULTIPLY TO REGISTER	8 W D 8	900	002	2
108	901	F05	27	2.47 MULTIPLY TO REGISTER	8 W D 8	901	002	26
108	902	. •			8008	902		
108	903			The state of the companion of the state of t	8 W 0 8	903	- mathematica per a proper m	
108	904				8 W 0 8	904		
108	905			• • • • • • • • • • • • • • • • • • • •	8008	905		
108	906				8008	906		
804	907			•	8 W O 8	907		
408	908				8 W 0 8	908		

6 W D 9	0.0			MULT	PLY FROM	REGISTER			8W09	0.0		
6 W 0 9	90	J04	12	2+36	REGISTER	TO MULTIPLY.	OPERAND	(LX)	8W09	90	E09	:3
6W09	91	J04	14	2+37	REGISTER	TO MULTIPLY.	OPERAND	(XJ)	8W09	91	E09	21
6W09	92	J04	16	2+38	REGISTER	TO MULTIPLY.	OPERAND	(XJ)	8W09	92	ED9	25
6W09	93	J04	18	2+39	REGISTER	TO MULTIPLY.	OPERAND	(XJ)	8W09	93	E10	3
6 W D 9	94	J05	12	2+40	REGISTER	TO MULTIPLY,	OPERAND	(XJ)	8409	94	E10	21
6 W D 9	95	J05	14	2+41	REGISTER	TO MULTIPLY.	OPERAND	(XJ)	8W09	95	E10	25
6W09	96	J05	16	2+42	REGISTER	TO MULTIPLY.	OPERAND	(XJ)	8009	96	E11	3
5 W D.9	97	J05	18	2-43	REGISTER	TO MULTIPLY.	OPERAND	(XJ)	8W09	97	E11	21
SWO9	98	J06	12	2+44	REGISTER	TO MULTIPLY,	OPERAND	(XJ)	8409	98	E11	25
5 W O 9	99	J06	14	2+45	REGISTER	TO MULTIPLY.	OPERAND	(XJ)	8W09	99	E12	3
5W09	900	J06	16	2+46	REGISTER	TO MULTIPLY,	OPERAND	(LX)	8 W D 9	900	E12	21
6W09	901	J06	18	2+47	REGISTER	TO MULTIPLY,	OPERAND	(XY)	8W09	901	E12	25
6W09	902	F06	15	2+59	REGISTER	TO MULTIPLY,	OPERAND	(XJ)	8W09	902	F12	25
6W09	903								8W09	903		
6W09	904								8W09	904		
6 W 0 9	905								8W09	905		
6 W O P	906								8W09	906		
6 W O 9	907								8W09	907		
5W09	908								8 W D 9	908		

6W10	0.0			MULTIPLY TO REGISTER	7W10	00	
6W10	90	E01	1	2+0 MULTIPLY TO REGISTER	7W10	90	D34 2
6W10	91	E01	5	2+1 MULTIPLY TO REGISTER	7W10	91	D34 26
6W10	92	E01	25	2+2 MULTIPLY TO REGISTER	7W10	92	D35 2
6W10	93	E01	27	2+3 MULTIPLY TO REGISTER	7W10	93	D35 26
6W10	94	E 0 2	1	2+4 MULTIPLY TO REGISTER	7W10	94	D36 2
6W10	95	E02	- 5	2+5 MULTIPLY TO REGISTER	7W10	95	D36 26
6W10	96	E02	25	2+6 MULTIPLY TO REGISTER	7W10	96	D37 2
6W10	97	<b>€</b> 02	27	247 MULTIPLY TO REGISTER	7W10	97	D37 26
6W10	98	€03	1	2+8 MULTIPLY TO REGISTER	7W10	98	D38 2
6W10	99	E03	5	2+9 MULTIPLY TO REGISTER	7W10	99	D38 26
6W10	900	E03	25	2+18 MULTIPLY TO REGISTER		900	D39 2
6W10	901	E03	27	2+11 MULTIPLY TO REGISTER	,	901	D39 26
6W10	902	E 0 4	Í	2+12 MULTIPLY TO REGISTER		902	D40 2
6W10	903	E04	5	2+13 MULTIPLY TO REGISTER	the state of the s	903	D40 26
6W10	904		25	2+14 MULTIPLY TO REGISTER		904	D41 2
6W10	905		27	2-15 MULTIPLY TO REGISTER	and the second of the second	905	D41 26
6W10	906	E05	1	2+16 MULTIPLY TO REGISTER		906	D42 2
6W10	987	E05	. 6	2+17 MULTIPLY TO REGISTER		907	D42 26
6W10	908	_ 0 -				908	

W11	0.0			MULTIPLY TO REGISTER	7W11	0.0		
W11	90	E 0 5	25	2+18 MULTIPLY TO REGISTER	7W11	90	E34	2
W11	91	E05	27	2+19 MULTIPLY TO REGISTER	7W11	91	E34	26
W11	92	E06	1	2+20 MULTIPLY TO REGISTER	7W11	92	E35	2
W11	93	E06	5	2+21 MULTIPLY TO REGISTER	7W11	93	E35	26
W11	94	E06	25	2+22 MULTIPLY TO REGISTER	7W11	94	E36	2
W11	95	E06	27	2+23 MULTIPLY TO REGISTER	7W11	95	E36	26
W11	96	E07	1	2+24 MULTIPLY TO REGISTER	7W11	96	E37	2
W11	97	E07	5	2+25 MULTIPLY TO REGISTER	7W11	97	E37	26
W11	98	E07	25	2+26 MULTIPLY TO REGISTER	7W11	98	E38	2
W11	99	E07	27	2+27 MULTIPLY TO REGISTER	7W11	99	E38	26
W11	900	F01	1	2+28 MULTIPLY TO REGISTER	7W11	900	E39	2
w11	901	F01	5	2+29 MULTIPLY TO REGISTER	7W11	901	E39	26
W11	902	F01	25	2+30 MULTIPLY TO REGISTER	7W11	902	E40	2
W11	903	F01	27	2+31 MULTIPLY TO REGISTER	7W11	903	E40	26
W11	904	F02	1	2+32 MULTIPLÝ TO REGISTER	7W11	904	E41	2
W11	905	F 0 2	5	2+33 MULTIPLY TO REGISTER	7W11	905	E41	26
W11	906	_	25	2+34 MULTIPLY TO REGISTER	7W11	906	E42	2
W11	907	F02		2+35 MULTIPLY TO REGISTER	7W11	907	E42	26
W11	908	•-			7W11	908	. ,	

W12	0.0		MULTIPLY FROM REGISTER	7W12	0.0	
W12	90	G01 12	2+0 REGISTER TO MULTIPLY, OPERAND 1(XK)	7W12	90	G29
W12	91	G01 14	2+1 REGISTER TO MULTIPLY, OPERAND 1(XK)	7W12	91	G29 2
112	92	G01 16	2+2 REGISTER TO MULTIPLY, OPERAND 1(XK)	7W12	92	G29 2
112	93	G01 18	2+3 REGISTER TO MULTIPLY, OPERAND 1(XK)	7W12	93	G30
W12	94	G02 12	2+4 REGISTER TO MULTIPLY, OPERAND 1(XK)	7W12	94	G30 2
W12	95	G02 14	2+5 REGISTER TO MULTIPLY, OPERAND 1(XK)	7W12	95	G30 2
W12	96	G02 16	2+6 REGISTER TO MULTIPLY, OPERAND 1(XK)	7W12	96	G31
W12	97	G02 18	2+7 REGISTER TO MULTIPLY, OPERAND 1(XK)	7W12	97	G31 2
W12	98	Gn3 12	2+8 REGISTER TO MULTIPLY, OPERAND 1(XK)	7W12	98	G31 2
W12	99	G03 14	2+9 REGISTER TO MULTIPLY, OPERAND 1(XK)	7W12	99	G32
W12	900	G03 16	2+10 REGISTER TO MULTIPLY, OPERAND 1(XK	7W12	900	G32 2
W12	901	G03 18	2+11 REGISTER TO MULTIPLY, OPERAND 1(XK	7W12	901	G32 2
W12	902	G04 12	2+12 REGISTER TO MULTIPLY, OPERAND 1(XK	7W12	902	G33
W12	903	G04 14	2413 REGISTER TO MULTIPLY, OPERAND 1(XK	7W12	903	G33 2
W12	904	G04 16	2-14 REGISTER TO MULTIPLY, OPERAND 1(XK	7W12	904	G33 2
W12	905	G04 18	2+15 REGISTER TO MULTIPLY, OPERAND 1(XK	7W12	905	G34
W12	906	G05 12	2+16 REGISTER TO MULTIPLY, OPERAND 1(XK	7W12	906	G34 2
W12	907	G05 14	2+17 REGISTER TO MULTIPLY, OPERAND 1(XK	7W12	907	G34 2
W12	908			7W12	908	

6W13	0.0			MULT!	PLY FR	DM I	REGISTER	accessing to a second	•	7W13	00		- make place (0.00) dates a
6W13	90	105	16	2*18	REGIST	ER	TO MULTIPLY,	OPERAND	2(XJ	7W13	90	J29	3
6W13	91	105	18	2+19	REGIST	ER '	TO MULTIPLY.	OPERAND	2(XJ	7W13	91	J29	25
6W13	92	106	12		REGIST		TO MULTIPLY,	OPERAND	2(XJ	7W13	92	J29	21
6W13	93	106	14	2+21	REGIST		TO MULTIPLY,	OPERAND	2(XJ	7W13	93	J30	3
6W13	94	106	16	2+22	REGIST	ER '	TO MULTIPLY.	OPERAND	2(XJ	7W13	94	J30	25
6W13	95	106	18	2+23	REGIST	ER '	TO MULTIPLY.	OPERAND	2(XJ	7W13	95	J30	21
6W13	96	J01	12	2+24	REGIST	ER '	TO MULTIPLY.	OPERAND	2(XJ	7W13	96	J31	- 3
6W13	97	J01	14	2+25	REGIST	ER '	TO MULTIPLY,	OPERAND	2(XJ	7W13	97	J31	25
6W13	98	J01	16	2+26	REGIST	ER '	TO MULTIPLY,	OPERAND	2(XJ	7W13	98	J31	21
6W13	99	J01	18	2+27	REGIST	ER '	TO MULTIPLY,	OPERAND	2(X)	7W13	99	J32	3
6W13	900	J02	12	2+28	REGIST	ER	TO MULTIPLY.		2(XJ	7W13	900	J32	25
6W13	901	J02	14	2+29	REGIST	ER	TO MULTIPLY.	OPERAND	2(XJ	7W13	901	J32	21
6W13	902	J02	16	2+30	REGIST	ER "	TO MULTIPLY,	OPERAND	2(XJ)	7W13	902	<b>J</b> 33	3
6W13	903	J02	18	2+31	REGIST	ER '	TO MULTIPLY,			7W13	903	J33	25
6W13	904	J03	12		REGIST		TO MULTIPLY.	OPERAND	2(XJ:	7W13	904	<b>J</b> 33	21
6W13	905	J03	14	2+33	REGIST		TO MULTIPLY,	OPERAND	2(XJ:	7W13	905	J34	3
6W13	906	J03	16	2+34	REGIST	ER '	TO MULTIPLY.	OPERAND	2(XJ.	7W13	906	J34	25
6W13	907	J03	18	2+35	REGIST	ER '	TO MULTIPLY.	OPERAND	2(XJ)	7W13	907	J34	21
6W13	908									7W13	908		_

6W14	0.0			MULTIPLY FROM REGISTER	7W14	0.0	
6W14	90	101	12	2+0 REGISTER TO MULTIPLY, OPERAND 2(XJ)	7W14	90	129 3
6W14	91	101	14	2+1 REGISTER TO MULTIPLY, OPERAND 2(XJ)	7W14	91	129 25
6W14	92	101	16	2+2 REGISTER TO MULTIPLY, OPERAND 2(XJ)	7W14	92	129 21
6W14	93	101	18	2+3 REGISTER TO MULTIPLY, OPERAND 2(XJ)	7W14	93	130 3
6W14	94	102	12	2+4 REGISTER TO MULTIPLY, OPERAND 2(XJ)	7W14	94	130 25
6W14	95	102	14	2+5 REGISTER TO MULTIPLY, OPERAND 2(XJ)	7W14	95	I30 21
6W14	96	102	16	2+6 REGISTER TO MULTIPLY, OPERAND 2(XJ)	7W14	96	131 3
6W14	97	102	18	2+7 REGISTER TO MULTIPLY, OPERAND 2(XJ)	7W14	97	131 25
6W14	98	103	12	2+8 REGISTER TO MULTIPLY, OPERAND 2(XJ)	7W14	98	131 21
6W14	99	103	14	2+9 REGISTER TO MULTIPLY, OPERAND 2(XJ)	7W14	99	132 3
6W14	900	103	16	2+10 REGISTER TO MULTIPLY, OPERAND 2(XJ)	7W14	900	132 25
6W14	901	103	18	2+11 REGISTER TO MULTIPLY, OPERAND 2(XJ)	7W14	901	132 21
6W14	902	104	12	2+12 REGISTER TO MULTIPLY, OPERAND 2(XJ)	7W14	902	133 3
6W14	903	104	14	2+13 REGISTER TO MULTIPLY, OPERAND 2(XJ)	7W14	903	133 25
6W14	904	104	16	2+14 REGISTER TO MULTIPLY, OPERAND 2(XJ)	7W14	904	133 21
6W14	905	104	18	2+15 REGISTER TO MULTIPLY, OFERAND 2(XJ)	7W14	905	134 3
6W14	906	105	12	2+16 REGISTER TO MULTIPLY, OPERAND 2(XJ)	7W14	906	134 25
6W14	907	105	14	2+17 REGISTER TO MULTIPLY, OPERAND 2(XJ)	7W14	907	134 21
6W14	908	- 0 -		The state of the s	7W14	908	

6W15	0.0		MULTIPLY FROM	REGISTER	7W15	00	
6W15	90	G05 16	2+18 REGISTER	TO MULTIPLY, OPERAND 1(XK)	7W15	90	H29 3
6W15	91	G05 18	2+19 REGISTER	TO MULTIPLY, OPERAND 1(XK)	7W15	91	H29 25
6W15	92	006 12	2+20 REGISTER	TO MULTIPLY, OPERAND 1(XK)	7W15	92	H29 21
6W15	93	G06 14	2+21 REGISTER	TO MULTIPLY, OPERAND 1(XK)	7W15	93	H30 3
6W15	94	G06 16	2+22 REGISTER	TO MULTIPLY, OPERAND 1(XK)	7W15	94	H30 25
6W15	95	G06 18	2+23 REGISTER	TO MULTIPLY, OPERAND 1(XK)	7W15	95	H30 21
6W15	96	H01 12	2+24 REGISTER	TO MULTIPLY, OPERAND 1(XK)	7W15	96	H31 3
6W15	97	H01 14	2+25 REGISTER	TO MULTIPLY, OPERAND 1(XK)	7W15	97	H31 25
6W15	98	H01 16	2+26 REGISTER	TO MULTIPLY, OPERAND 1(XK)	7W15	98	H31 21
6W15	99	H01 18	2+27 REGISTER	TO MULTIPLY, OPERAND 1(XK)	7W15	99	H32 3
6W15	900	H02 12	2+28 REGISTER	TO MULTIPLY, OPERAND 1(XK)	7W15	900	H32 25
6W15	901	H02 14	2+29 REGISTER	TO MULTIPLY, OPERAND 1(XK)	7W15	901	H32 21
6W15	902	H02 16	2+30 REGISTER	TO MULTIPLY, OPERAND 1(XK)	7W15	902	H33 3
6W15	903	H02 18	2+31 REGISTER	TO MULTIPLY, OFERAND 1(XK)	7W15	903	H33 25
6W15	904	H03 12	2+32 REGISTER	TO MULTIPLY, OPERAND 1(XK)	7W15	904	H33 21
6W15	905	H03 14	2+33 REGISTER	TO MULTIPLY, OPERAND 1(XK)	7W15	905	H34 3
6W15	906	H03 16	2+34 REGISTER	TO MULTIPLY, OPERAND 1(XK)	7W15	906	H34 25
6W15	907	H03 18	2+35 REGISTER	TO MULTIPLY, OPERAND 1(XK)	7W15	907	H34 21
6W15	908				7W15	908	

6W16	0.0			MULTIPLY FROM CONTROL	***	5W14	00	* *	新書書: 
W16	90	109	23	GO UNIT NO. I		5W14	90	J40	4
W16	91	109	3	ROUND NO. 1	** . ** .	5W14	91	E40	4
W16	92	108	23	DOUBLE NO. 1		5W14	92	E40	8
5W16	93	L08		XMIT RESULT NO. 1		5W14	93	J40	23
5W16	94	LOB	2	XMIT RESULT NO. 2		5W14	94	J40	25
5W16	95	H09	1	REQUEST RELEASE		5W14	95	J25	19
5W16	96	122	_	CLOCK		5W14	96	J39	
W16	97	. <del>.</del>		CLEAR		5W14	97		
W16	98					5W14	98		
W16	99					5W14	99		
W16	900	J09	23	GO UNIT NO. 2		5W14	900	J40	6
W16	901	Jo9	3	ROUND		5W14	901	E40	6
5W16	902	J08	_	POUBLE		5W14	902	E40	10
W16	903			XMIT RESULT		5W14	903		
W16	904			XMIT RESULT		5W14	904		
W16	905	K09	1	REQUEST RELEASE	**	5W14	905	K21	19
W16	906		_			5W14	906		
W16	907					5W14	907		
W16	908					5W14	908		

6W20	0.0		MULTIPLY FROM DIVIDE	2W24	0.0	
6W20	90	K01: 11	2+0 DIVIDE TO MULTIPLY RESULT	2W24	90	K01 1
6W20	91	K01 10	2+1 DIVIDE TO MULTIPLY RESULT	2W24	91	K01 6
6W20	92	K02 11	2+2 DIVIDE TO MULTIPLY RESULT	2W24	92	K01 23
6W20	93	K02 10	2+3 DIVIDE TO MULTIPLY RESULT	2W24	93	K01 28
6W20	94	K03 11	2+4 DIVIDE TO MULTIPLY RESULT	2W24	94	K02 1
6W20	95	K03 10	2.5 DIVIDE TO MULTIPLY RESULT	2W24	95	K02 6
6W20	96	K04 11	2+6 DIVIDE TO MULTIPLY RESULT	2W24	96	K02 23
6W20	97	K04 10	2+7 DIVIDE TO MULTIPLY RESULT	2W24	97	K02 28
6W20	98	K05 11	2+8 DIVIDE TO MULTIPLY RESULT	2W24	9.8	K03 1
6W20	99	K05 10	2.9 DIVIDE TO MULTIPLY RESULT	2W24	99	K03 6
6W20	900	K06 11	2+10 DIVIDE TO MULTIPLY RESULT	2W24	900	K03 23
6W20	901	K06 10	2+11 DIVIDE TO MULTIPLY RESULT	2W24	901	K03 28
6W20	902	K07 11	2+12 DIVIDE TO MULTIPLY RESULT	2W24	902	K04 1
6W20	903	K07 10	2+13 DIVIDE TO MULTIPLY RESULT	2W24	903	K04 6
6W20	904	L01 11	2+14 DIVIDE TO MULTIPLY RESULT	2W24	904	K04 23
6W20	905	L01 10	2+15 DIVIDE TO MULTIPLY RESULT	2W24	905	K04 28
6W20	906	L02 11	2+16 DIVIDE TO MULTIPLY RESULT	2W24	906	K05 1
6W20	907	L02 10	2+17 DIVIDE TO MULTIPLY RESULT	2W24	907	K05 6
6W20	908			2W24	908	

5W21	00		MULTIPLY FROM DIVIDE	2W25	00		
5W21	90	L03 11	2+18 DÍVIDE TO MULTIPLY RESULT	2W25	90	K05	23
6W21	91	L03 10	2+19 DIVIDE TO MULTIPLY RESULT	2W25	91	K05	28
W21	92	L04 11	2+20 DIVIDE TO MULTIPLY RESULT	2W25	92	K06	1
W21	93	L04 10	2+21 DIVIDE TO MULTIPLY RESULT	2W25	93	K06	6
W21	94	L05 11	2+22 DIVIDE TO MULTIPLY RESULT	2W25	94	K06	23
W21	95	L05 10	2+23 DIVIDE TO MULTIPLY RESULT	2W25	95	K06	28
W21	96	L06 11	2+24 DIVIDE TO MULTIPLY RESULT	2W25	96	KO7	1
W21	97	L06 10	2+25 DIVIDE TO MULTIPLY RESULT	2W25	97	K07	6
W21	98	L07 11	2+26 DIVIDE TO MULTIPLY RESULT	2W25	98	K07	23
W21	99	L07 10	2+27 DIVIDE TO MULTIPLY RESULT	2W25	99	KO7	28
W21	900	M01 11	2#28 DIVIDE TO MULTIPLY RESULT	2W25	900	K08	1
W21	901	M01 10	2+29 DIVIDE TO MULTIPLY RESULT	2W25	901	K08	6
W21	902	M02 11	2+30 DIVIDE TO MULTIPLY RESULT	2W25	902	K08	23
W21	903	M02 10	2+31 DIVIDE TO MULTIPLY RESULT	2W25	903	K08	28
W21	904	M03 11	2+32 DIVIDE TO MULTIPLY RESULT	2W25	904	K09	1
W21	905	M03 10	2433 DIVIDE TO MULTIPLY RESULT	2W25	905	K09	6
W21	906	M04 11	2+34 DIVIDE TO MULTIPLY RESULT	2W25	906	Kn9	23
W21	907	M04 10	2+35 DIVIDE TO MULTIPLY RESULT	2W25	907		28
W21	908	F07 1	REDUCE MULT. 1	2W25	908		7

6W22	00		MULTIPLY FROM DIVIDE	2W26 00
6W22	90	M05:11	2+36 DIVIDE TO MULTIPLY RESULT	2W26 90 K10 1
6W22	91	M05 10	2+37 DIVIDE TO MULTIPLY RESULT	2W26 91 K10 6
6W22	92	M06 11	2+38 DIVIDE TO MULTIPLY RESULT	2W26 92 K10 23
6W22	93	M06 10	2+39 DIVIDE TO MULTIPLY RESULT	2W26 93 K10 28
6W22	94	M07 11	2.40 DIVIDE TO MULTIPLY RESULT	2W26 94 K11 1
6W22	95	M07 10	2+41 DIVIDE TO MULTIPLY RESULT	2W26 95 K11 6
6W22	96	N01:11	2+42 DIVIDE TO MULTIPLY RESULT	2W26 96 K11 23
6W22	97	N01 10	2+43 DIVIDE TO MULTIPLY RESULT	2W26 97 K11 28
6W22	98	N02 11	2+44 DIVIDE TO MULTIPLY RESULT	2W26 98 K12 1
6W22	99	N02 10	2+45 DIVIDE TO MULTIPLY RESULT	2W26 99 K12 6
6W22	900	N03 11	2+46 DIVIDE TO MULTIPLY RESULT	2W26 900 K12 23
6W22	901	NO3 10	2+47 DIVIDE TO MULTIPLY RESULT	2W26 901 K12 28
6W22	902	F06 1	REDUCE MULT. 2	2W26 902 Q12 7
6W22	903			2W26 903
6W22	904			2W26 904
6W22	905	H10 1	GO MULÝ 1	2W26 905 M11 14
6W22	906	K10 1		2W26 906 M11 16
6W22	907	NO7 12	D P MULT 1	2W26 907 Q11 5
6W22	908	NO7 19	D P MULT 2	2W26 908 Q12 5

6W23	0.0			MULT	IPL	Y TO	DIV	IDE	Management of the contract of		2W27	0.0		
6W23	90	K01	1	2+0	MUL	TIPL	Y TO	DIVIDE	(OPERAND)	X**K	2W27	90	R06	12
6W23	91	K01	3	2+1	MUL	PIPL	Y TO	DIVIDE	(OPERAND)	X++K	2W27	91	R06	14
5W23	92	KO1	2	2+2	MUL	IPL	Ÿ TO	DIVIDE	(OPERAND)	X++K	2W27	92	R06	16
W23	93	K02	1	2+3	MUL'	IPL	Y TO	DIVIDE	(OPERAND)	X**K	2W27	93	R06	18
W23	9.4	KO2	3	2+4	MUL	IPL	Y TO	DIVIDE	(OPERAND)	X**K	2W27	94	R07	12
W23	95	KO2	2	2+5	MUL'	IPL	Y TO	DIVIDE	(OPERAND)	X++K	2W27	95	R07	14
W23	96	KO3	1	2+6	MUL	IPL	Y TO	DIVIDE	(OPERAND)	X++K	2W27	96	R07	16
W23	97	K03	3	2 + 7	MUL	TPL	Y TO	DIVIDE		X++K	2W27	97	R07	18
W23	98	K03	2	2+8	MUL!	IPL	Y TO	DIVIDE		X**K	2W27	98	R08	12
W23	99	K04	1	2 + 9	MUL'	TIPL	Y TO	DIVIDE		X**K	2W27	99	R 0 8	14
W23	900	K 0 4	3	2+10	MŲI	TIP					2W27	900	R 08	16
W23	901	K 0 4	2	2+11		TIP					2W27	901	R 0 8	18
W23	902	K05	1	2+12		TIP		CONTRACTOR OF THE PROPERTY OF	Marie Control of the	THE ENGLACE DESCRIPTION TO A 1	2W27	902	R09	12
W23	903	K05	3	2+13	MÜL	TIP	LYTO			77	2W27	903	R09	14
W23	904	K05	2	2+14	MÚI	TIP					2W27	904	R09	16
W23	905	K06	1	2+15	MUL	TIP	LY TO				2W27	905	R09	1.8
W23	906	K06	3	2+16	MUL	TIP	LÝ TO				2W27	906	R10	12
W23	907	K06	2	2+17	MŮI	TIP	LŸ T	DIVID	E (OPERAND	) X**K	2W27	907	R10	14
W23	908										2W27	908	and the second second second second	

6W24	00			MULT	PLY TO D	<u> 1 Ÿ 1 !</u>	DE	والمناور والمار والمار المنام المارة والمار والمار والمار والمار والمار المارة		2W28	0.0		
6W24	90	K07	1	2+18	MULTIPLY	ŤO	DIVIDE	(OPERAND)	X++K	2W28	90	R10	16
6W24	91	K07	3	2+19	MULTIPLY	TO	DIVIDE	(OPERAND)	X++K	2W28	91	R10	18
6W24	92	K07	2	2+20	MULTIPLŸ	TO		(OPERAND)	X++K	2W28	92	P06	12
6W24	93	L01	1		MULTIPLY	TO	DIVIDE	(OPERAND)	X**K	2W28	93	P06	
6W24	94	L01:	. 3	2+22	MULTIPLŸ	TO	DIVIDE	(OPERAND)	X++K	2W28	94	P06	16
6W24	95	L01	2	2+23	MULTIPLY	TO	DIVIDE	(OPERAND)	X**K	2W28	95	P06	18
6W24	96	L02	1		MULTIPLŸ		DIVIDE	(OPERAND)	X**K	2W28	96	P07	12
6W24	97	L02	3	2+25	MULTIPLY	TO	DIVIDE	(OPERAND)	X++K	2W28	97	P07	14
6W24	98	LO2	2	2 + 26	MULTIPLŸ	TO	DIVIDE	(OPERAND)	X++K	2W28	98	P07	
6W24	99	LO3	1	2+27	MULTIPLY	ŤO	DIVIDE	(OPERAND)	X**K	2W28	99	P07	18
6W24	900	L03	3	2+28	MULTIPLŸ	ŤO	DIVIDE	(OPERAND)	X++K	2W28	900	P08	
6W24	901	L03	2	2+29	MULTIPLY	TO	DIVIDE	(OPERAND)	X++K	2W28	901	P08	
6W24	902	L04	1		MULTIPLÝ		DIVIDE	(OPERAND)	X++K	2W28	902	P08	
6W24	903	L04	3		MULTIPLY			(OPERAND)	X++K	2W28	903	P08	
6W24	904	L04	2	2+32	MULTIPLY	TO	DIVIDE	(OPERAND)	X++K	2W28	904	P09	
6W24	905	L05	1		MULTIPLY	ŤO		(OPERAND)	X++K	2W28	905	P09	
6W24	906	L05	3		MULTIPLY	TO	DIVIDE	(OPERAND)	X**K	2W28	906	P09	<del></del> '
6W24	907	L05	2	and the second of the second	MULTIPLY	TO		(OPERAND)	X++K	2W28	907	P09	
6W24	908		-							2428	908		

W25	0.0			MULTIPLY TO BIVIDE	2W29	00	
W25	90	L06	1	2+36 MULTIPLY TO DIVIDE (OPERAND) X++K	2W29	90	P10 12
W25	91	L06	3	2+37 MULTIPLY TO DIVIDE (OPERAND) X++K	2W29	91	P10 14
W25	92	L06	2	2+38 MULTIPLY TO DIVIDE (OPERAND) X++K	2W29	92	P10 16
W25	93	L07	1	2+39 MULTIPLY TO DIVIDE (OPERAND) X++K	2W29	93	P10 18
W25	94	LO7	3	2+40 MULTIPLY TO DIVIDE (OPERAND) X++K	2W29	94	M06 12
W25	95	L07	2	2+41 MULTIPLY TO DIVIDE (OPERAND) X++K	2W29	95	M06 14
W25	96	MO1	1	2+42 MULTIPLY TO DIVIDE (OPERAND) X++K	2W29	96	M06 16
W25	97	MO1	3	2+43 MULTIPLY TO DIVIDE (OPERAND) X++K	2W29	97	M06 18
W25	98	M01	2	2+44 MULTIPLY TO DIVIDE (OPERAND) X++K	2W29	98	M07 12
W25	99	M02	1	2+45 MULTIPLY TO DIVIDE (OPERAND) X++K	2W29	99	M07 14
W25	900	M 0 2	3	2+46 MULTIPLY TO DIVIDE (OPERAND) X++K	2W29	900	M07 16
W25	901	M02	2	2+47 MULTIPLY TO DIVIDE (OPERAND) X++K	2W29	901	M07 18
W25	902		_	2+48 SIGN	2W29	902	
W25	903	MO3	1	2+0 MULTIPLY TO DIVIDE (OPERAND) X++J	2W29	903	006 12
W25	904	M 0 3	3	2+1 MULTIPLY TO DIVIDE (OPERAND) X++J	2W29	904	006 14
W25	905	MO3	2	2+2 MULTIPLY TO DIVIDE (OPERAND) X++J	2W29	905	006 16
W25	906	M 0 4	1	2+3 MULTIPLY TO DIVIDE (OPERAND) X++J	2W29	906	006 18
W25	907	M 0 4	3	2+4 MULTIPLY TO DIVIDE (OPERAND) X++J	2W29	907	007 12
W25	908		-		2W29	908	

6W26	00		·	MÚLŤÍPĽÝ TO BIVIDE	2W30 00	
6W26	90	M04	2	2+5 MULTIPLY TO DIVIDE OPERAND X++J	2W30 90 007 14	
6W26	91	M05	1		2W30 91 007 16	
6W26	92	M05	3		2W30 92 007 18	
6W26	93	M05	2		2W30 93 Q08 12	
6W26	94	M06	1	2+9 MULTIPLY TO DIVIDE OPERAND X++J	2W30 94 008 14	
6W26	95	M06	3	2+10 MULTIPLY TO DIVIDE OPERAND X++J	2W30 95 008 16	
6W26	96	M06	2	2+11 MULTIPLY TO DIVIDE OPERAND X++J	2W30 96 008 18	
6W26	97	M07	1	2+12 MULTIPLY TO DIVIDE OPERAND X++J	2W30 97 Q09 12	
6W26	98	M07	3	2+13 MULTIPLY TO DIVIDE OPERAND X++J	2W30 98 Q09 14	
6W26	99	M07	2	2+14 MULTIPLY TO DIVIDE OPERAND X++J	2W30 99 Q09 16	
6W26	900	N01	1	2+15 MULTIPLY TO DIVIDE OPERAND X++J	2W30 900 Q09 18	
6W26	901	NO1	3	2+16 MULTIPLY TO DIVIDE OPERAND X++J	2W30 901 Q10 12	
6W26	902	NO1	2	2+17 MULTIPLY TO DIVIDE OPERAND X++J	2W30 902 Q10 14	
6W26	903	N02	1	2+18 MULTIPLY TO DIVIDE OPERAND X++J	2W30 903 Q10 16	
6W26	904	N02	3	2+19 MULTIPLY TO DIVIDE OPERAND X++J	2W30 904 Q10 18	
6W26	905	N02	2	2+20 MULTIPLY TO DIVIDE OPERAND X++J	2W30 905 NO6 12	
6W26	906	NO3	1	2+21 MULTIPLY TO DIVIDE OPERAND X++J	2W30 906 N06 14	
6W26	907	NO3	3	2+22 MULTIPLY TO DIVIDE OPERAND X++J	2W30 907 NO6 16	
6W26	908		_		2W30 908	

6W27	0.0			MULT	IPLY TO D	l V I	DE			2W31	00		
6W27	90	N03	2	2+23	MULTIPLY	то	DIVIDE	OPERAND	X**J	2W31	90	N 0 6	18
6W27	91	NO4	8	2+24	MULTIPLY	ŤO	DIVIDE	OPERAND	X++J	2W31	91	No7	12
5W27	92	N04	6	2+25	MULTIPLY	TO	DIVIDE	OPERAND	X**J	2W31	92	N07	14
W27	93	NO4	4	2+26	MULTIPLY	ŤO	DIVIDE	OPERAND	X++J	2W31	93	NO7	
W27	94	N 0 4	27	2+27	MULTIPLY	TO	DIVIDE	OPERAND	X++J	2W31	9.4	No7	18
5W27	95	N 0 4	25	2+28	MULTIPLY	TO	DIVIDE	OPERAND	X++J	2W31	95	NO8	
W27	96	N04	23	2+29	MULTIPLY	TO	DIVIDE	OPERAND	X**J	2W31	96	NOB	14
W27	97	N05	8	2+30	MULTIPLY	ŤO	DIVIDE	OPERAND	X++J	2W31	97	NOS	16
W27	98	NO5	6	2+31	MULTIPLY	TO	DIVIDE	OPERAND	XwwJ	2W31	98	NO8	18
W27	99	N05	4	2+32	MULTIPLY	TO	DIVIDE	OPERAND	X**J	2W31	99	N 0 9	12
W27	900	N05	27	2+33	MULTIPLŸ	TO	DIVIDE	OPERAND	V**J	2W31	900	N 0 9	14
W27	901	N05	25	2+34	MULTIPLY	ŤO	DIVIDE	OPERAND	X**J	2W31	901	N09	16
W27	902	N05	23	2+35	MULTIPLY	ŤO	DIVIDE	OPERAND	K**J	2W31	902	N 0 9	
W27	903	N06	8	2+36	MULTIPLY	ŤO	DIVIDE	OPERAND	K**J	2W31	903	N10	12
5W27	904	N 0 6	6	2+37	MULTIPLY	TO	DIVIDE	OPERAND	K**J	2W31	904	N10	14
5W27	905	N06	4	2+38	MULTIPLY	ŤO	DIVIDE	OPERAND	X**J	2W31	905	N10	16
5W27	906	N06	27	2+39	MULTIPLY	TO	DIVIDE	OPERAND	X**J	2W31	906	N10	18
5W27	907	N06	25	2+40	MULTIPLY	ŤO	DIVIDE	OPERAND	K**J	2W31	907	L06	12
W27	908									2W31	908		

6W28 00 MULTIPLY TO DIVIDE	: <b>2W32</b> 00
6W28 90 NO6 23 2+41 MULTIPLY TO DIVIDE X++J 6W28 91 NO7 8 2+42 MULTIPLY TO DIVIDE X++J	2W32: 90 L06 14
6W28 92 NO7 6 2+43 MULTIPLY TO DIVIDE X++J	2W32
6W28 93 NO7 4 2*44 MULTIPLY TO DIVIDE X**J	2W32 93 L07 12
6W28 94 NO7 27 2+45 MULTIPLY TO DIVIDE X++J	2W32 94 L07 14
6W28 95 NO7 25 2+46 MULTIPLY TO DIVIDE X++J	2W32 95 L07 16
6W28 96 NO7 23 2+47 MULTIPLY TO DIVIDE X++J 6W28 97 2+48 MULTIPLY TO DIVIDE X++J	2W32 96 L07 18 2W32 97
6W28 98 107 23 ERROR MULT, I	2W32 97 2W32 98 Jo3 1
6W28 99 J07 23 ERROR MULT. 2	2W32 99 J03 23
6W28 900 108 27 ERROR MULT. 1	2W32 900 J03 6
6W28 901 J08 27 ERROR MULT. 2	2W32 901 J03 28
6W28 902 6W28 903	2W32 902 2W32 903
6W28 904	2W32 904
6W28 905	2W32 905
6W28 906	2W32 906
6W28 907 6W28 908	2W32 907 2W32 908
	SM3E 906
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7W05	00		LOWER REGISTER FROM READ DISTRIBUTOR	3W12	0.0		
7W05	90	A41 2	2+8 MEMORY TO REGISTER	3W12	90	135 28	
7W05	91	A41 26	2+9 MEMORY TO REGISTER	3W12	91	136 28	
7W05	92	A42 2	2+10 MEMORY TO REGISTER	3W12	92	138 28	
7W05	93	A42 26	2+11 MEMORY TO REGISTER	3W12	93	139 28	
7W05	94	B37 2	2+12 MEMORY TO REGISTER	3W12	94	140 28	
7W05	95	B37 26	2+13 MEMORY TO REGISTER	3W12	95	141 28	
7W05	96	838 2	2+14 MEMORY TO REGISTER	3W12	96	142 28	
7W05	97			3W12	97		
7W05	98			3W12	98		
7W05	99			3W12	99		
7W05	900	A37 2	2+0 MEMORY TO REGISTER	3W12	900	125 28	
7W05	901	A37 26	2+1 MEMORY TO REGISTER	3W12	901	126 28	
7W05	902	A38 2	2+2 MEMORY TO REGISTER	3W12	902	127 28	
7W05	903	A38 26	2+3 MEMORY TO REGISTER	3W12	903	128 28	**************************************
7W05	904	A39 2	2+4 MEMORY TO REGISTER	3W12	904	129 28	
7W05	905	A39 26	2+5 MEMORY TO REGISTER	3W12	905	132 28	
7W05	906	A40 2	2+6 MEMORY TO REGISTER	3W12	906	133 28	
7W05	907	A40 26	2.7 MEMORY TO REGISTER	3W12	907	134 28	
7W05	908		The second secon	3W12	908		

7W06	0.0			LOWER	REGIS	ER	FROM READ	DISTRIBUTOR	4W18	00		
7W06	90	B42	26	2+23	MEMORY	ŤΟ	REGISTER		4W18	90	111	28
7W06	91	C37	2	2+24	MEMORY	ŤO	REGISTER		4W18	91	112	28
7WD6	92	037	26		MEMORY	TO	REGISTER		4W18	92	114	28
7W06	93	C38	2		MEMORY	ŤŌ	REGISTER		4W18	93	115	***
7W06	94	C38	26	2+27	MEMORY	ŤO	REGISTER		4W18	94	116	28
7W06	95	C39	2		MEMORY	TO	REGISTER		4W18	95	117	28
7W06	96	C39	26		MEMORY	70	REGISTER		4W18	96	118	28
WD6	97	* (Tu = 1)	. 100,350	Tringen (MR 45 )	- Control of the control of the control			***	4W18	97		
W06	98								4W18	98		
7W06	99					***			4W18	99		
7W06	900	838	26	2+15	MEMORY	ŤO	REGISTER		4W18	900	101	28
7W06	901	B39	2	2-16	MEMORY	ŤO	REGISTER		4W18	901	102	28
7W06	902	B39	26	2+17	MEMORY	TO	REGISTER		4W18	902	103	
7W06	903	B40	2	2+18	MEMORY	TO	REGISTER		4W18	903	104	28
7W06	904	B40	26		MEMORY	TO	REGISTER		4W18	904	105	28
7W06	905	B41	2		MEMORY	TO	REGISTER		4W18	905	108	28
7W06	906	B41	26	E.	MEMORY	TO	REGISTER		4W18	906	109	
7W06	907	842	2	- 1	MEMORY	ŤO	REGISTER		4W18	907	110	28
7W06	908		-			,			4W18	908		

7W07	00	na, tina, mar man, anga, man mga panga tinan anna sana s	LOWER	REGIS	TER	FROM	READ	DISTRIBUTOR	9W12	00			W HOLAND W.
7W07	90								9W12	90			
W07	91	Marie Control of the	. WARRY COLUMN TO THE PARTY OF	n i no i i arappera ni mati a repropegazione dell'erio ne a			ar annual statement of the Assessment of the	ngangan merimpangan ganggaran menunu salah menungkan pengangan menungkan pengangan pengangan pengangan pengang	9W12	91			A A CONTRACT OF THE ASSESSMENT
WOZ	92								9W12	92			
W07	93								9W12	93			
WO7	94								9W12	94			
W07	95								9W12	95			
W07	96								9W12	96			
W 0 7	97		A MARTINETT SCHOOLSENGEN STATE OF THE STATE OF THE		napri sanculmina			of the security and pasted, against absence a destruction and the pasted and the advantage of the behaviory	9W12	97		processing and the second	alian to the same problems of
W07	98								9W12	98			
W07	99								9W12	99			
W07	900	C40 2	2+30	MEMORY	ŤΟ	REGIS	STER		9W12	900	125	28	
W 0 7	901	C40 26		MEMORY	TO	REGIS			9W12	901	126	28	
W07	902	C41 2		MEMORY	TO	REGIS			9W12	902	127	28	
W 0 7	903	C41 26		MEMORY	TO	REGIS	Marriage for the property of the con-	tink meningang an garagam ying ten papa berapa dintahan baramanya antar panya. Sanar sa yang saga	9W12	903		28	
W07	904	C42 2		MEMORY	, -				9W12	904	129	28	
W07	905	C42 26		MEMORY					9W12	905	132	28	
W 0 7	906								9W12	906			
W 0 7	907								9W12	907			
W07	908								9W12	908			

7W 08	00		LOWER REGISTER TO MEMORY DISTRIBUTION	2W09	00		an over the same property and the same state of the same of
W 0 8	90	F28 <b>21</b>	DATA 2+8 REG. TO MEM.	2W09	90	A09	1.0
WDB	91	F29 3	DATA 2+9 REG. TO MEM.	2W09	91	A09	21
W 0 8	92	F29 25	DATA 2+10 REG. TO MEM.	2W09	92	A09 2	24
W 0 8	93	F29 21	DATA 2+11 REG. TO MEM.	2W09	93	A09	26
WOS	94	F30 3	DATA 2+12 REG. TO MEM.	2W09	94	A10	5
W 0 8	95	F30 25	DATA 2+13 REG. TO MEM.	2W09	95	A10	7
WOB	96	F30 21	DATA 2+14 REG. TO MEM.	2W09	96	A10 1	LO
W 0 8	97	F31 3	DATA 2+15 REG. TO MEM.	2W09	97	A10	21
W D 8	98	F31 25	DATA 2+16 REG. TO MEM.	2W09	98	A10 2	24
WD8	99	F31 21	DATA 2+17 REG. TO MEM.	2W09	99	A10 2	26
WOB	900	F26 3	DATA 2+0 REG. TO MEM.	2409	900	A 0 8	-5
W 0 8	901	F26 25	DATA 2+1 REG. TO MEM.	2009	901	A 0 8	7
WD8	902	F26 21	DATA 2+2 REG. TO MEM.	2W09	902	A08	LO
WD8	903	F27 3	DATA 2+3 REG. TO MEM.	2W09	903	A08	21
W 0 8	904	F27 25	DATA 2+4 REG. TO MEM.	2W09	904		24
W 0 8	905	F27 21	DATA 2+5 REG. TO MEM.	2009	905	80A	26
WO8	906	F28 3	DATA 2+6 REG. TO MEM.	2009	906	A89	5
WOB	907	F28 25	DATA 2+7 REG. TO MEM.	2W09	907	A09	7
WOB	908		· · · · · · · · · · · · · · · · · · ·	2W09	908		

7W09	00		LOWER	REGISTER	TO	MEMORY DISTRIBUTION	2W10	0.0		and the same of th
7W09	90	F34 21	DATA 2	#26 REG.	TO	MEM,	2W10	90	A12	10
7W09	91	F35 3	DATA 2	#27 REG.	TO	MEM.	2W10	91	A12	21
7W09	92	F35 25	DATA 2	#28 REG.	TO	MEM.	2W10	92	A12	24
7W09	93	F35 21	DATA 2	#29 REG.	TO	MEM.	2W10	93	A12	26
7WD9	94	E26 3	DATA 2	+30 REG.	TQ.	MEM.	2W10	94	A13	5
7W09	95	E26 25	DATA 2	+31 REG.	TO	MEM .	2W10	95	A13	7
7W09	96	E26 21	DATA 2	+32 REG.	TO	MEM.	2W10	96	A13	10
7W09	97	E27 3	DATA 2	*33 REG.	TO	MEM.	2W10	97	A13	21
7W09	98	E27 25	DATA 2	+34 REG.	TO	MEM.	2W10	98	A13	24
7W09	99	E27 21	DATA 2	#35 REG.	TO	MEM.	2W10	99	A13	26
7W09	900	F32 3	DATA 2	+18 REG.	TO	MEM.	2W10	900	A11	5
7W09	901	F32 25	DATA 2	+19 REG.	TO	MEM.	2W10	901	A11	7
7W09	902	F32 21	DATA 2	#20 REG.	TO	MEM.	2W10	902	A11	10
7W09	903	F33 3	DATA 2	+21 REG.	TO	MEM.	2W10	903	A11	21
7W09	904	F33 25	DATA 2	#22 REG.	TO	MEM.	2W10	904	A11	24
7W09	905	F33 21	DATA 2	+23 REG.	TO	MEM.	2W10	905	A11	26
7W09	906	F34 3	DATA 2	#24 REG.	TO	MEM.	2W10	906	A12	5
7W09	907	F34 25	DATA 2	+25 REG.	TO	MEM.	2W10	907	A12	7
7W09	908						2W10	908	-	-

7W10	0.0			REGISTER FROM MULTIPLY	6W10	00	
7W10	90	D34	2	2+0 MULTIPLY TO REGISTER	6W10	90	E01 1
7W10	91	D34	26	2+1 MULTIPLY TO REGISTER	6W10	91	E01 5
7W10	92	D35	2	2+2 MULTIPLY TO REGISTER	6W10	92	E01 25
7W10	93	D35	26	2+3 MULTIPLY TO REGISTER	6W10	93	E01 27
7W10	94	D36	- 2	2+4 MULTIPLY TO REGISTER	6W10	94	E02 1
7W10	95	D36	26	2+5 MULTIPLY TO REGISTER	6W10	95	E02 5
7W10	96	D37	- 2	2+6 MULTIPLY TO REGISTER	6W10	96	E02 25
7W10	97	D37	26	2+7 MULTIPLY TO REGISTER	6W10	97	E02 27
7W10	98	D38	2	2+8 MULTIPLY TO REGISTER	6W10	98	E03 1
7W10	99	D38	26	2+9 MULTIPLY TO REGISTER	6W10	99	E03 5
7W10	900	D39	2	2+10 MULTIPLY TO REGISTER	6W10	900	E03 25
7W10	901	D39	26	2+11 MULTIPLY TO REGISTER	6W10	901	E03 27
7W10	902	D40	2	2+12 MULTIPLY TO REGISTER	6W10	902	E04 1
7W10	903	D40	26	2+13 MULTIPLY TO REGISTER	6W10	903	E04 5
7W10	904	D41	2	2+14 MULTIPLY TO REGISTER	6W10	904	E04 25
7W10	905	D41	26	2+15 MULTIPLY TO REGISTER	6W10	905	E04 27
7W10	906	D42	2	2+16 MULTIPLY TO REGISTER	6W10	906	E05 1
7W10	907	D42	26	2+17 MULTIPLY TO REGISTER	6W10	907	E05 5
7W10	908				6W10	908	

7W11	0.0		REGISTER FROM MULTIPLY	6W11 00
7W11	90	E34	2 418 MULTIPLY TO REGISTER	6W11 90 E05 25
7W11	91	E34 2	2+19 MULTIPLY TO REGISTER	6W11 91 E05 27
7W11	92	E35	2 2+20 MULTIPLY TO REGISTER	6W11 92 E06 1
7W11	93	E35 2	2+21 MULTIPLY TO REGISTER	6W11 93 E06 5
7W11	94	E36	2 2+22 MULTIPLY TO REGISTER	6W11 94 E06 25
7W11	95	E36 2	5 2+23 MULTIPLY TO REGISTER	6W11 95 E06 27
7W11	96	E37		6W11 96 E07 1
7W11	97	E37 2		6W11 97 E07 5
ZW11	98	E38	2 2+26 MULTIPLY TO REGISTER	6W11 98 E07 25
7W11	99	E38 2		6W11 99 E07 27
7W11	900	E39	2 2+28 MULTIPLÝ TO REGISTER	6W11 900 F01 1
7W11	901	E39 2		6W11 901 F01 5
7W11	902		2 +30 MULTIPLY TO REGISTER	6W11 902 F01 25
7W11	903	E40 2	1 - 의미지를 인용무실적으로과 무슨 이것으로 교육실	6W11 903 F01 27
7W11	904	E41		6W11 904 F02 1
7W11	905	E41 2		6W11 905 F02 5
7W11	906	E42	the second control of the second control of	6W11 906 F02 25
7W11	907	E42 2	5 2+35 MÚLTIPLŸ TO REGISTER	6W11 907 F02 27
7W11	908	A TO A STATE A A TOTAL STREET, THE STREET,		6W11 908

112	0.0		REGISTER TO MULTIPLY	6W12	0.0	NO PROPERTY AND AND AND PROCESSAGE AND AND AND AND AND AND AND AND AND AND	
112	90	G29 3		6W12	90	G01 12	
112	91	029 25		6W12	91	G01 14	
112	92	G29 21	2+2 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	92	G01 16	
112	93	G30 3	2+3 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	93	G01 18	
12	94	G30 25	2+4 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	94	G02 12	
12	95	G30 21	2+5 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	95	G02 14	
12	96	G31 3	2+6 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	96	G02 16	
12	97	G31 25	2.7 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	97	G02 18	
12	98	G31 21	2+8 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	98	G03 12	
12	99	G32 3	2+9 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	99	G03 14	
12	900	G32 25	2+10 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	900	G03 16	
12	901	G32 21	2+11 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	901	G03 18	
12	902	G33 3	a management of the second of	6W12	902	G04 12	
12	903	G33 25		6W12	903	G04 14	***************************************
12	904	G33 21	2+14 REGISTER TO MULTIPLY, OPERAND 1(XK)	6W12	904	G04 16	
12	905	G34 3		6W12	905	G04 18	
12	906	G34 25		6W12	906	G05 12	
12	907	G34 21	그 프랑스투하다 단점적 실찍실 통하는 사용이 한국인 소설실한 한 사이 살으로 등인 것입니다. 그 있습니다.		907	G05 14	
12	908		make Homographic in incoming mile of milestan military	6W12	908		

7W13	0.0		REGISTER TO MULTIPLY	6W13	0.0	nama and and arth and disk and airs and airs and are are an are are an are	
7W13	90	J29 3	2+18 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	90	105 16	
7W13	91	J29 25	2-19 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	91	105 18	
7W13	92	J29 21	2+20 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	92	106 12	
7W13	93	J30 3	2+21 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	93	106 14	
7.W13	94	J30 25	2+22 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	94	106 16	
7W13	95	J30 21	2+23 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	95	106 18	
7W13	96	J31 3	2+24 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	96	J01 12	
7W13	97	J31 25	2+25 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	97	J01 14	
7W13	98	J31 21	2+26 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	98	J01 16	
7W13	99	J32 3	2+27 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	99	J01 18	
7W13	900	J32 25	2+28 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	900	J02 12	
7W13	901	J32 21	2429 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	901	J02 14	
7W13	902	J33 3	2+30 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	902	J02 16	
7W13	903	J33 25	2+31 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	903	J02 18	
7W13	904	J33 21	2+32 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	904	J03 12	
7W13	905	J34 3	2.33 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	905	J03 14	
7W13	906	J34 25	2+34 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	906	J03 16	
7W13	907	J34 21	2+35 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W13	907	J03 18	
7W13	908		· · ·	6W13	908		

7W14	0.0			REGISTER TO MULTIPLY	6W14	00	
7W14	90	129	3	2+0 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	90	101 12
7W14	91	129	25	2+1 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	91	101 14
7W14	92	129	21	2+2 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	92	101 16
7W14	93	130	3	2+3 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	93	101 18
7W14	94	130	25	2+4 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	94	102 12
7W14	95	130	21	2+5 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	95	102 14
7W14	96	131	3	2+6 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	96	I02 16
7W14	97	131	25	2+7 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	97	102 18
7H14	98	131	21	2+8 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	98	103 12
7W14	99	132	3	2+9 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	99	103 14
7W14	900	132	25	2+10 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	900	103 16
7W14	901	132	21	2+11 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	901	103 18
7W14	902	133	3	2+12 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	902	104 12
7W14	903	133	25	2+13 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	903	104 14
7W14	904	133	21	2+14 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	904	104 16
7W14	905	134	3	2+15 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	905	104 18
7W14	906	134	25	2*16 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	906	105 12
7W14	907	134	21	2+17 REGISTER TO MULTIPLY, OPERAND 2(XJ)	6W14	907	105 14
7W14	908				6W14	908	

7W15	0.0			REGISTER TO MULTIPLY	<u> 6 w</u>	15	0.0	
7W15	90	H29	3	2+18 REGISTER TO MULTIPLY,	OPERAND 1(XK) 6W	15	90	005 16
7W15	91	H29	25	2+19 REGISTER TO MULTIPLY,	OPERAND 1(XK) 6W	15	91	005 18
7W15	92	H29	21	2+20 REGISTER TO MULTIPLY	OPERAND 1(XK) 6W	15	92	006 12
7W15	93	H30	3	2+21 REGISTER TO MULTIPLY,	OPERAND 1(XK) 6W	15	93	G06 14
7W15	94	H30	25	2+22 REGISTER TO MULTIPLY	OPERAND 1(XK) 6W	15	94	G06 16
7W15	95	H30	21	2+23 REGISTER TO MULTIPLY,	OPERAND 1(XK) 6W	15	95	G06 18
7W15	96	H31	3	2+24 REGISTER TO MULTIPLY.	OPERAND 1(XK) 6W	15	96	H01 12
7W15	97	H31	25	2+25 REGISTER TO MULTIPLY,	OPERAND 1(XK) 6W	15	97	H01 14
7W15	98	H31	21	2+26 REGISTER TO MULTIPLY.	DPERAND 1(XK) 6W	15	98	H01 16
7W15	99	H32	3	2+27 REGISTER TO MULTIPLY.	OPERAND 1(XK) 6W	15	99	H01 18
7W15	900	H32	25	2+28 REGISTER TO MULTIPLY.	OPERAND 1(XK) 6W	15	900	H02 12
7W15	901	H32	21	2+29 REGISTER TO MULTIPLY.	DPERAND 1(XK) 6W	15	901	H02 14
7W15	902	H33	3	2+30 REGISTER TO MULTIPLY.	OPERAND 1(XK) 6W	15	902	H02 16
7W15	903	H33	25	2+31 REGISTER TO MULTIPLY.	OPERAND 1(XK) 6W		903	H02 18
7W15	904	H33	21	2+32 REGISTER TO MULTIPLY,	OPERAND 1(XK) 6W		904	H03 12
7W15	905	H34	3	2-33 REGISTER TO MULTIPLY.	OPERAND 1(XK) 6W	15	905	H03 14
7W15	906	H34	25	2+34 REGISTER TO MULTIPLY.	OPERAND 1(XK) 6W	15	906	H03 16
7W15	907	H34	21	2+35 REGISTER TO MULTIPLY.	OPERAND 1 (XK) 6W		907	H03 18
7W15	908		_		6W		908	

W16	00		LOWER REGISTER FROM CONT	ROL 5W22	00	tti agan gara sittin sinin sasa, sitin sinin sittin	
W16	90	128 5	ADD TO X 2	5W22	90	H39 22	
W16	91	H28 17	INCREMENT TO X GO	5w22	91	H40 5	-
W16	92	H28 16	INCREMENT TO X 0	5w22	92	H40 1	
W16	93	H28 4	INCREMENT TO X 1	5w22	93	H40 26	
W16	94	H28 5	INCREMENT TO X 2	5w22	94	H40 22	
W16	95	K33 17	INCREMENT TO B GO	5W22	95	H41 5	
W16	96	K33 16	INCREMENT TO B 0	5W22	96	H41 1	
W16	97	K33 4	INCREMENT TO B 1	5w22	97	H41 26	
W16	98	K33 5	INCREMENT TO B 2	5W22	98	H41 22	
W16	99	127 7	XJ TO MULTIPLY (2) GO	5w22	99	J41 5	
W16	900		- · · · · · · · · · · · · · · · · · · ·	5w22	900		
W16	901	122 12	CLOCK	5W22	901	J39 4	
W16	902	027 17	MULTIPLY TO X GO	5W22	902	H38 5	
W16	903	G27 16	MULTIPLY TO X 0	5W22	903	H38 1	
W16	904	027 4	MULTIPLY TO X 1	5W22	904	H38 26	
W16	905	G27 5	MULTIPLY TO X 2	5W22	905	H38 22	
W16	906	128 17	ADD TO X GO	5W22	906	H39 5	
W16	907	128 16	ADD TO X	5W22	907	H39 1	
W16	908			5W22	908		

7W17	0.0			LOWER REGISTER FROM CONTROL	-5W16 00
7W17	90	G28	.4	MEMORY TO D 0	5W16 90 I42 1
7W17	91	G28	i <b>5</b>	MEMORY TO D 1	5W16 91 142 26
7W17	92	G28	16	MEMORY TO D 2	5W16 92 I42 22
7W17	93	126	21	K TO MULTIPLY (1) GO	5W16 93 J42 5
7W17	94	126	24	K TO MULTIPLY (1) 0	5W16 94 J42 1
7W17	95	127	-5	K TO MULTIPLY (1) 1	5W16 95 J42 26
7W17	96	127	10	K TO MULTIPLY (1) 2	5W16 96 J42 22
7W17	97	127	26	J TO MULTIPLY (2) 0	5W16 97 J41 1
7W17	98		21	J TO MULTIPLY (2) 1	5W16 98 J41 26
7W17	99	127	24	J TO MULTIPLY (2) 2	5W16 99 J41 22
7W17	900		17	INCREMENT TO A GO	5W16 900 H42 5
7W17	901	H27	16	INCREMENT TO A 0	5W16 901 H42 1
7W17	902	H27	4	INCREMENT TO A 1	5W16 902 H42 26
7W17	903	H27	5	INCREMENT TO A 2	5W16 903 H42 22
7W17	904	-	17	SHIFT TO B GO	5W16 904 141 5
7W17	905		16	SHIFT TO B 0	5W16 905 141 1
7W17	906	K34	- 4	SHIFT TO B 1	5W16 906 I41 26
7W17	907	K34	- 5	SHIFT TO B 2	5W16 907 I41 22
7W17	908	,			5W16 908

7W18	00			LOWER	REG	STER	FROM CONTROL	5W19	00		-
7W18	90	J27	24	X TO	INCRE	MENT	· <b>0</b> ·	5W19	90	K40	1
7W18	91	J28	5	X TO	INCRE	MENT	1	5W19	91	K40	26
7W18	92	J28	10	X TO	INCRE	MENT	2	5W19	92	K40	22
7W18	93	M34	7	BTO	ADD (	(1)	GO	5W19	93	M41	5
W18	94	M34	26	B T0	ADD (	(1)	0	5W19	94	M41	1
W18	95	M34	21	BTO	ADD	(1)	1	5W19	95	M41	26
W18	96	M34	24	BTO	ADD (	(1)	2	5W19	96	M41	22
W18	97							5W19	97		
W18	98							5W19	98		
W18	99							5W19	99		
W18	900	J28	7	OT LX	ADD	(1)	GO	5W19	900	K41	5
W18	901	J28	26	XJ TO	ADD	(1)	0	5W19	901	K41	1
W18	902	J28	21	XJ TO	ADD	(1)	1	5W19	902	K41	26
W18	903	J28	24	OT LX	ADD	(1)	2	5W19	903	K41	22
W18	904	126	-5	XL TO	ADD	(5)	GO	5W19	904	K42	5
W18	905	126	10	XL TO	ADD	(2)	0	5W19	905	K42	1
W18	906	126	7	XL TO	ADD	(2)	<b>1</b>	5W19	906	A	26
W18	907	126	26	XL TO	ADD	(2)	2	5W19	907	K42	22
W18	908							5W19	908		

7W19	0.0			LOWER REGISTER FRO	OM CONTROL	5W20	00		and the tip in the second to the second
7W19	90	G26 :	LO	A TO INCREMENT	0	5W20	90	M42	1
7W19	91	026	7	A TO INCREMENT	1	5W20	91	M42	126
7W19	92		26	A TO INCREMENT	2	5W20	92	M42	22
7W19	93	128	4	ADD TO X	1	5w20	93	H39	26
7W19	94		ĿŹ	MEM. TO D	GO	5W20	94	142	5
7W19	95		21	X TO INCR.	GO	5w20	95	K40	5
7W19	96	026	5	A TO INCR.	:GO	5W20	96	M42	· §
7W19	97					5W20	97		
7W19	98					5W20	98		
7W19	99			Fixed one dark have made made ones share yets state and made made ones that made state made when these cases made ones and		5W20	99		
7W19	900	L34	5	BJ TO INCREMENT	:GO	5W20	900	L41	5
7W19	901		LO	BJ TO INCREMENT	0	5W20	901	L41	1
7W19	902	L34	7	BJ TO INCREMENT	i	5W20	902	L41	26
7W19	903	L34 2	26	BJ TO INCREMENT	2	5w20	903	L41	
7W19	904	L34 2	21	BK TO INCREMENT	90	5W20	904	L42	5
7W19	905	L34	24	BK TO INCREMENT	70	5W20	905	L42	1
7W19	906	M34	5	BK TO INCREMENT	1	5W20	906	L42	26
7W19	907	M34 1	LO	BK TO INCREMENT	2	5W20	907	L42	
7W19	908					5W20	908		

	00			LOWER REGISTER FROM CONTROL	5W25	0.0			
7W20	90				5W25	90			
7 W 2 0	91		****	MONTH CONTRACTOR CONTR	5W25	91			per acces to something company
7W20	92				5W25	92			
7W20	93	G37	5	EXCHANGE	5W25	93	033	6	
7W20	94	-	-		5W25	94		•	
W20	95	**** * * *		AND MARKET PER CONTROL OF THE PE	5W25	95			
W20	96				5W25	96			
W20	97			The parties of the property of the state of the parties of the state o	5w25	97			
7W20	98	J26 2	4	D++5 TO X++5	5w25	98	034	4	
7W20	99				5W25	99			
	900	J26	5	D++1 TO X++1	5W25	900	034	12	
	901	J26	7	D++2 TO X++2	5W25	901	034	10	
	902	J26 1	ń	D++3 TO X++3	5w25	902	034	Ā	
grade describer and the	903	J26 2		D++4 TO X++4	5W25	903	034	<u> </u>	
	904		Ž	X TO MEMORY GO	5W25	904	031	6	
	905	H26	7	X TO MEMORY 0	5W25	905	031	<del></del>	
	906	H26	7	X TO MEMORY 1	5W25	906		26	
	907	H26 2	. <u>.</u>	X TO MEMORY 2	5W25	907	031		
	908	n20 2	,	A IU MEMART 2	5W25	908	031	<b>E</b> 3	

7W21	0.0		REGISTER FROM INCREMENT RESULT	5w08	0.0	en er sen er sen en en en en en en en en en en en en e
7W21	90	F42 26	INCREMENT RESULT BIT 2+9	5w08	90	F41 27
7W21	91	638 2	INCREMENT RESULT BIT 2+10	5W08	91	F42 4
7W21	92	038 26	INCREMENT RESULT BIT 2+11	5w08	92	F42 6
7W21	93	G39 2	INCREMENT RESULT BIT 2*12	5w08	93	F42 8
7W21	94	G39 26	INCREMENT RESULT BIT 2+13	5W08	94	F42 10
7W21	95	G40 2	INCREMENT RESULT BIT 2+14	5W08	95	F42 12
7W21	96	G40 26	INCREMENT RESULT BIT 2+15	5W08	96	F42 19
7W21	97	G41 2	INCREMENT RESULT BIT 2+16	5w08	97	F42 21
7W21	98	341 26	INCREMENT RESULT BIT 2+17	5W08	98	F42 23
7W21	99			5 W O 8	99	
7W21	900	F38 2	INCREMENT RESULT BIT 2+0	5W08	900	F41 4
7W21	901	F38 26	INCREMENT RESULT BIT 2+1	5W08	901	F41 6
7W21	902	F39 2	INCREMENT RESULT BIT 2#2	5W08	902	F41 8
7W21	903	F39 26	INCREMENT RESULT BIT 2+3	5W08	903	F41 10
7W21	904	F40 2	INCREMENT RESULT BIT 2+4	5 W 0 B	904	F41 12
7W21	905	F40 26	INCREMENT RESULT BIT 2+5	5W08	905	F41 19
7W21	906	F41 2	INCREMENT RESULT BIT 2+6	5W08	906	F41 21
7W21	907	F41 26	INCREMENT RESULT BIT 2+7	5w08	907	F41 23
7W21	908	F42 2	INCREMENT RESULT BIT 2+8	5W08	908	F41 25

7422	0.0		<del></del>	REGISTER F	ROM INC	RE	MENT	OPERAND	5W36	00		
7w22	90	K30	3	INCREMENT	OPERAND	1	BIT	2+9	5W36	90	026	7
7W22	91	K30	25	INCREMENT	OPERAND	1	BIT	2*10	5W36	91	027	23
7W22	92	K30	21	INCREMENT	OPERAND	1	BIT	2*11	5W36	92	027	7
7W22	93	K31	3	INCREMENT	OPERAND	1	BIT	2*12	5W36	93	028	23
7W22	94	K31	25	INCREMENT	OPERAND	1	BIT	2*13	5W36	94	028	7
7W22	95	K31	21	INCREMENT	OPERAND	1	BIT	2+14	5W36	95	029	23
7W22	96	K32	3	INCREMENT	OPERAND	1	BIT	2+15	5W36	96	029	7
7W22	97	K32	25	INCREMENT	OPERAND	1	BIT	2*16	5W36	97	030	7
7W22	98	K32	21	INCREMENT	OPERAND	1		2*17	5W36	98	030	24
W22	99			e name yn a Than i Bearl an affiliae i Bearl an ar Const. All Thaile and an ar alle an				The first term was the cold to the second of the cold	5W36	99		
W22	900	K27	:3	INCREMENT	OPERAND	1	BIT	2+0	5W36	900	022	23
7W22	901	K27	25	INCREMENT	OPERAND	1	BIT	2*1	5W36	901	022	7
W22	902	K27	21	INCREMENT	OPERAND	1	BIT	2*2	5W36	902	023	23
7W22	903	K28	3	INCREMENT	OPERAND	1	BIT	2*3	5W36	903	023	7
7W22	904	K28	25	INCREMENT	OPERAND	1	BIT	2 * 4	5W36	904	024	23
7W22	905	K28	21	INCREMENT	OPERAND	1	BIT	2*5	5W36	905	024	7
7W22	906	K29	3	INCREMENT	OPERAND	1	BIT	2*6	5W36	906	025	23
7W22	907	K29	25	INCREMENT	OPERAND	1	BIT	2*7	5w36	907	025	7
7W22	908	K29	21	INCREMENT	OPERAND	1	BIT	2 * 8	5W36	908	026	23

7W23	0.0	The Artist Sales states allow states states and a contract and a con-	REGISTER FROM INCREMENT OPERAND	5w37	0.0	
7w23	90	L30 3	INCREMENT OPERAND K BIT 2*9	-5W37	90	026 3
7W23	91	L30 25	INCREMENT OPERAND K BIT 2+10	5W37	91	027 27
7W23	92	L30 21	INCREMENT OPERAND K BIT 2+11	5W37	92	027 3
7W23	93	L31 3	INCREMENT OPERAND K BIT 2+12	5W37	93	028 27
7W23	94	L31 25	INCREMENT OPERAND K BIT 2+13	5W37	94	028 3
7W23	95	L31 21	INCREMENT OPERAND K BIT 2+14	5W37	95	029 27
7W23	96	L32 3	INCREMENT OPERAND K BIT 2*15	5W37	96	029 3
7W23	97	L32 25	INCREMENT OPERAND K BIT 2+16	5W37	97	030 3
7W23	98	L32 21	INCREMENT OPERAND K BIT 2+17	5W37	98	030 25
7W23	99			5w37	99	<b>THE STATE AND DESCRIPTION OF THE PERSON OF </b>
7W23	900	L27 3	INCREMENT OPERAND K BIT 2+0	5w37	900	022 27
7W23	901	L27 25	INCREMENT OPERAND K BIT 2+1	5W37	901	022 3
7W23	902	L27 21	INCREMENT OPERAND K BIT 2+2	5W37	902	023 27
7W23	903	L28 3	INCREMENT OPERAND K BIT 2+3	5W37	903	023 3
7W23	904	L28 25	INCREMENT OPERAND K BIT 2*4	5W37	904	024 27
7W23	905	L28 21	INCREMENT OPERAND K BIT 2+5	5W37	905	024 3
7W23	906	L29 3	INCREMENT OPERAND K BIT 2*6	5W37	906	025 27
7W23	907	L29 25	INCREMENT OPERAND K BIT 2+7	5w37	907	025 3
7W23	908	L29 21	INCREMENT OPERAND K BIT 2+6	5W37	908	026 27

7W24	0.0		REGISTER TO ADD	8W26 00	
7W24	90	M39 3	2+0 REGISTER TO ADD, OPERAND 1(XK)	8W26 90 J01 11	
7W24	91	M39 25	2+1 REGISTER TO ADD, OPERAND 1(XK)	8W26 91 J01 13	
7W24	92	M39 21	2+2 REGISTER TO ADD, OPERAND 1(XK)	8W26 92 J02 11	3
7W24	93	M40 3	2+3 REGISTER TO ADD, OPERAND 1(XK)	8W26 93 J02 13	
7W24	94	M40 25	2+4 REGISTER TO ADD, OPERAND 1(XK)	8W26 94 J03 11	
7W24	95	M40 21	2+5 REGISTER TO ADD, OPERAND 1(XK)	8W26 95 J03 13	
7W24	96	M41 3	2+6 REGISTER TO ADD, OPERAND 1(XK)	8W26 96 J04 8	,
7W24	97	M41 25	2+7 REGISTER TO ADD, OPERAND 1(XK)	8W26 97 J04 24	ere andre constitute and a second constitute of the second
7W24	98	M41 21	2+8 REGISTER TO ADD, OPERAND 1(XK)	8W26 98 J05 8	
7W24	99	M42 3	2+9 REGISTER TO ADD, OPERAND 1(XK)	8W26 99 J05 24	
7W24	900	M42 25	2+10 REGISTER TO ADD, OPERAND 1(XK)	8W26 900 J06 8	1
7W24	901	M42 21	2+11 REGISTER TO ADD, OPERAND 1(XK)	8W26 901 J06 24	
7W24	902	N35 3	2+12 REGISTER TO ADD, OPERAND 1(XK)	8W26 902 J07 6	
7W24	903	N35 25	2+13 REGISTER TO ADD, OPERAND 1(XK)	8W26 903 J07 24	
7W24	904	N35 21	2+14 REGISTER TO ADD, OPERAND 1(XK)	8W26 904 J08 6	
7W24	905	N36 3	2+15 REGISTER TO ADD, OPERAND 1(XK)	8W26 905 J08 24	
7W24	906	N36 25	2+16 REGISTER TO ADD, OPERAND 1(XK)	8W26 906 J09 6	
7W24	907	N36 21	2+17 REGISTER TO ADD, OPERAND 1(XK)	8W26 907 J09 24	
7W24	908			8W26 908	

7W25	00		REGISTER TO ADD	8427 00
7W25	90	N37 3	2+18 REGISTER TO ADD, OPERAND 1(XK)	8W27 90 J10 6
7W25	91	N37 25	2+19 REGISTER TO ADD, OPERAND 1(XK)	8W27 91 J10 24
7W25	92	N37 21	2*20 REGISTER TO ADD, OPERAND 1(XK)	8W27 92 J11 6
7W25	93	N38 3	2+21 REGISTER TO ADD, OPERAND 1(XK)	8W27 93 J11 24
7W25	94	N38 25	2+22 REGISTER TO ADD, OPERAND 1(XK)	8W27 94 K01 6
7W25	95	N38 21	2+23 REGISTER TO ADD, OPERAND 1(XK)	8W27 95 K01 24
7W25	96	N39 3	2+24 REGISTER TO ADD, OPERAND 1(XK)	8W27 96 K02 6
7W25	97	N39 25	2+25 REGISTER TO ADD, OPERAND 1(XK)	8W27 97 K02 24
7W25	98	N39 21	2-26 REGISTER TO ADD, OPERAND 1(XK)	8W27 98 K03 6
7W25	99	N40 3	2+27 REGISTER TO ADD, OPERAND 1(XK)	8W27 99 K03 24
7W25	900	N40 25	2+28 REGISTER TO ADD, OPERAND 1(XK)	8W27 900 K04 6
7W25	901	N40 21	2+29 REGISTER TO ADD, OPERAND 1(XK)	8w27 901 K04 24
7W25	902	N41 3	2+30 REGISTER TO ADD, OPERAND 1(XK)	8W27 902 K05 6
7W25	903	N41 25	2+31 REGISTER TO ADD, OPERAND 1(XK)	8W27 903 K05 24
7W25	904	N41 21	2+32 REGISTER TO ADD, OPERAND 1(XK)	8W27 904 K06 6
7W25	905	N42 3	2+33 REGISTER TO ADD, OPERAND 1(XK)	8W27 905 KO6 24
7W25	906	N42 25	2+34 REGISTER TO ADD, OPERAND 1(XK)	8W27 906 K07 6
7W25	907	N42 21	2+35 REGISTER TO ADD, OPERAND 1(XK)	8W27 907 K07 24
7W25	908	11 1- 1-		8W27 908

7W26	00				8W24	0.0		**************
7W26	90	H37	2	2+0 ADD TO REGISTER	8W24	90	M01	1
7W26	91	H37	26	2+1 ADD TO REGISTER	8W24	91	M01	6
7W26	92	H38	2	2+2 ADD TO REGISTER	8W24	92	MO1	23
7W26	93	H38	26	2+3 ADD TO REGISTER	8W24	93	M01	28
7W26	94	H39	2	2+4 ADD TO REGISTER	8W24	94	M02	. 1
7W26	95	H39	26	2+5 ADD TO REGISTER	8W24	95	M02	6
7W26	96	H40	2	2+6 ADD TO REGISTER	8W24	96	M02	23
7W26	97	H40	26	2+7 ADD TO REGISTER	8W24	97	M02	28
7W26	98	H41	2	2+8 ADD TO REGISTER	8W24	98	M03	1
7W26	99	H41	26	2+9 ADD TO REGISTER	8W24	99	MO3	6
7W26	900	H42	2	2+10 ADD TO REGISTER	8W24	900	M03	23
7W26	901	H42	26	2+11 ADD TO REGISTER	8W24	901	M03	28
7W26	902	J37	2	2+12 ADD TO REGISTER	8W24	902	M04	1
7W26	903	J37	26	2+13 ADD TO REGISTER	8W24	903	M04	6
7W26	904	J38	2	2+14 ADD TO REGISTER	8W24	904	M 0 4	23
7W26	905	J38	26	2+15 ADD TO REGISTER	8W24	905	M04	28
7W26	906	J39	2	2+16 ADD TO REGISTER	8W24	906	M05	1
7W26	907	J39	26	2+17 ADD TO REGISTER	8W24	907	M05	6
7W26	908	'		- · · · · · · · · · · · · · · · · · · ·	8W24	908		

7W27	00		REGISTER FROM ADD	8W25 00
7W27	90	J40 2	2+18 ADD TO REGISTER	8W25 90 M05 23
7W27	91	J40 26	2+19 ADD TO REGISTER	8W25 91 M05 28
7W27	92	J41 2	2+20 ADD TO REGISTER	8W25 92 N01 1
7W27	93	J41 26	2+21 ADD TO REGISTER	8W25 93 NO1 6
7W27	94	J42 2	2+22 ADD TO REGISTER	8W25 94 NO1 23
7W27	95	J42 26	2+23 ADD TO REGISTER	8W25 95 NO1 28
7W27	96	K37 2	2+24 ADD TO REGISTER	8W25 96 NO2 1
7W27	97	K37 26	2+25 ADD TO REGISTER	8W25 97 NO2 6
7W27	98	K38 2	2+26 ADD TO REGISTER	8W25 98 NO2 23
7W27	99	K38 26	2+27 ADD TO REGISTER	8W25 99 NO2 28
7W27	900	K39 2	2#28 ADD TO REGISTER	8W25 900 N03 1
7W27	901	K39 26	2+29 ADD TO REGISTER	8W25 901 NO3 6
7W27	902	K40 2	2+30 ADD TO REGISTER	8W25 902 N03 23
7W27	903	K40 26	2+31 ADD TO REGISTER	8W25 903 N03 28
7W27	904	K41 2	2+32 ADD TO REGISTER	8W25 904 N04 1
7W27	905	K41 26	2+33 ADD TO REGISTER	8W25 905 NO4 6
7W27	906	K42 2	2+34 ADD TO REGISTER	8W25 906 N04 23
7W27	907	K42 26	2+35 ADD TO REGISTER	8W25 907 N04 28
7W27	908		months remaining the second of the second	8W25 908

W28	0.0		REGISTER TO ADD	8W28	00	MINE STEE JOHN MINE SHEE SHEE SHEE SHEE SHEE SHEE SHEE SH	
W28	90	L35 3	2+0 REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	90	L09 14	
W28	91	L35 25	2+1 REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	91	L09 18	******
W28	92	L35 21	2+2 REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	92	L10 14	
W28	93	L36 3	2+3 REGISTER TO ADD, OPERAND 2(BJ+XJ)	8428	93	L10 18	
W28	94	L36 25	2+4 REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	94	L11 14	
W28	95	L36 21	2+5 REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	95	L11 18	
W28	96	L37 3	2+6 REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	96	J04 7	
W28	97	L37 25	2+7 REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	97	J04 21	
W28	98	L37 21	2+8 REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	98	J05 7	
W28	99	L38 3	2+9 REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	99	J05 21	
W28	900	L38 25	2+10 REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	900	J06 7	
W28	901	L38 21	2+11 REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	901	J06 21	
W28	902	L39 3	2+12 REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	902	J07 7	
W28	903	L39 25	2+13 REGISTER TO ADD, OPERAND 2(BJ+XJ)	8428	903	J07 21	and the second
W28	904	L39 21	2+14 REGISTER TO ADD, OPERAND 2(BJ+XJ)	8428	904	J08 7	
W28	905	L40 3	2+15 REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	905	J08 21	
W28	906	L40 25	2+16 REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	906	J09 7	
W28	907	L40 21	2+17 REGISTER TO ADD, OPERAND 2(BJ+XJ)	8W28	907	117 23	
W28	908	_		8W28	908		

W29	00			REGI	STER TO A	מס			وروم والمراور والمراور والمراور والمراور والمراور والمراور والمراور والمراور والمراور والمراور والمراور	8W29	0.0			gage and party that they are a long to a state out of the st
W29	90	L41	3	2+18	REGISTER	ŤO	ADD,	OPERAND	2(XJ+BJ)	8W29	90	J10	7	
W29	91	L41	25	2+19	REGISTER	70	ADD,	OPERAND	2(XJ+BJ)	8W29	91	J10	21	
W29	92	L41	21	2+20	REGISTER	TO	ADD.	OPERAND	2(XJ+BJ)	8W29	92	J11	7	
W29	93	L42	3	2+21	REGISTER	TO		OPERAND	2(XJ+BJ)	8W29	93	J11	21	
W29	94	L42	25	2+22	REGISTER		ADD,	OPERAND	2(XJ+BJ)	8W29	94	K01	<b>7</b>	
W29	95	L42	21	2+23	REGISTER	TO	ADD,	OPERAND	2(XJ+BJ)	8W29	95	K01	21	
W29	96	M35	3	2+24	REGISTER	TO		OPERAND	2(XJ+BJ)	8W29	96	K02	7	
W29	97	M35	25	2+25	REGISTER	TO	ADD.	OPERAND		8W29	97	K02	21	
W29	98	M35	21	2+26	REGISTER			OPERAND	2(XJ+BJ)	8W29	98	K03	7	
129	99	M36	3	2+27	REGISTER	TO	ADD.	OPERAND	2(XJ+BJ)	8W29	99	KO3	21	
129	900	M36	25	2 * 28	REGISTER	TO	ADD.	OPERAND		8W29	900	K04	7	
W29	901	M36	21	2+29	REGISTER	TO	ADD.	OPERAND		8W29	901	K04	21	
W29	902	M37	3	2+30	REGISTER	TO	ADD,			8W29	902	K05	7	
W29	903	M37	25	2+31	REGISTER	TO	ADD,		5(X]+B])	8W29	903	K05	21	
W29	904	M37	21				ADD.	OPERAND	Š(XJ+BJ)	8W29	904	K06	7	
129	905	M38	3	2+33	REGISTER	TO	ADD,	OPERAND	5(X]+B])	8429	905	K06	21	
129	906	M38	25	2+34	REGISTER	ŤO		OPERAND	5(X]+B])	8W29	906	KO7	7	
W29	907	M38	21	2+35	REGISTER	TO	ADD.	OPERAND	2(XJ+BJ)	8W29	907	K07	21	
W29	908		_							8W29	908	•		

7W30	0.0			DT	0 X			8W23	0.0		
7W30	90	L26	8	Dei			UPPER	8W23	90	G11	5
W30	91	L26	6	D+2	TO		UPPER	8w23	91	G11	7
W30	92	L26	4	D+3	10	X * 3	UPPER	8W23	92	G11	10
W30	93	L26	27	D+4	TO	X * 4	UPPER	8W23	93	G11	21
W30	94	L26	25	D+5	TO	X*5	UPPER	8W23	94	G11	24
W30	95							8W23	95		
W30	96							8W23	96		
W30	97							8W23	97		
W30	98							8W23	98		
W30	99							8w23	99		
W30	900							8W23	900		
W30	901							8W23	901		
W30	902							8W23	902		
W30	903	garagere scannor e e e	***************************************		-		CONTRACTOR OF THE PROPERTY OF	8W23	903		
W30	904							8W23	904		
W30	905							8W23	905		4 pr - 100 and 100 and
W30	906							8W23	906		
W30	907							8W23	907		
W30	908							8W23	908		

and the second of the second o

7W31	00		REGISTER FROM SHIFT	-8W30	0.0	als and 655 650 FGC Spd also have area you had also 450 fGG Adv area you had are
7W31	90	038 2	2+0 SHIFT TO REGISTER	8W30	90	H03: 1
7W31	91	038 26	2+1 SHIFT TO REGISTER	-8W30	91	H03 5
7W31	92	039 2	2+2 SHIFT TO REGISTER	-8W30	92	H03-25
7W31	93	039 26	2+3 SHIFT TO REGISTER	8W30	93	H03 27
7W31	94	040 2	2+4 SHIFT TO REGISTER	8W30	94	H04 1
7W31	95	040 26	2+5 SHIFT TO REGISTER	8W30	95	H04 5
7W31	96	041 2	2+6 SHIFT TO REGISTER	8W30	96	H04 25
7W31	97	041 26	2+7 SHIFT TO REGISTER	8W30	97	H04 27
7W31	98	042 2	2+8 SHIFT TO REGISTER	8W30	98	H05 1
7W31	99	042 26	2+9 SHIFT TO REGISTER	8W30	99	H05 5
7W31	900	P38 2	2+10 SHIFT TO REGISTER	8W30	900	H17 1
7W31	901	P38 26	2+11 SHIFT TO REGISTER	8W30	901	H17 5
7W31	902	P39 2	2+12 SHIFT TO REGISTER	8W30	902	H17 25
7W31	903	P39 26	2+13 SHIFT TO REGISTER	8W30	903	H17 27
7W31	904	P40 2	2+14 SHIFT TO REGISTER	8W30	904	H18 1
7W31	905	P40 26	2+15 SHIFT TO REGISTER	8W30	905	H18 5
7W31	906	P41 2	2+16 SHIFT TO REGISTER	8W30	906	H18 25
7W31	907	P41 26	2+17 SHIFT TO REGISTER	8W30	907	H18 27
7W31	908	F 7 - 60	MATA OUT 1:10 UMMIGIN	-8W30	908	HIO E/

BW05	00		UPPER REGISTER FROM READ DISTRIBUTOR	9W21	00	אור אינו אינו אינו מער מער אינו אינו מידו אינו אינו אינו אינו אינו אינו אינו אינ
8W05	90			9W21	90	
BWOS	91			9W21	91	
SWOS	92			9W21	92	
3W05	93			9W21	93	and ago, and daily and days and supplied who have also also also and and also have been also also are supplied to the and the area.
WOS.	94			9W21	94	
WOS	95			9W21	95	the same and and also the same same same same same same same sam
WO5	96			9W21	96	
W05	97			9W21	97	
W05	98			9421	98	
WOS	99			9W21	99	
W05	900	A01 2	2+36 MEMORY TO REGISTER	9W21	900	133 28
WOS	901	A01 26	2+37 MEMORY TO REGISTER	9W21	901	134 28
W05	902	A02 2	2+38 MEMORY TO REGISTER	9W21	902	135-28
W05	903	A02 26	2+39 MEMORY TO REGISTER	9W21	903	136 28
3W05	904	A03 2	2+40 MEMORY TO REGISTER	9W21	904	138 28
WOS	905	A03 26	2+41 MEMORY TO REGISTER	9W21	905	139 28
W05	906	A04 2	2+42 MEMORY TO REGISTER	9W21	906	140 28
W05	907	A04 26	2+43 MEMORY TO REGISTER	9W21	907	141 28
W 0 5	908	A05 2	2+44 MEMORY TO REGISTER	9W21	908	142 28

8 W O 6	00			UPPER REGISTER FROM READ DISTRIBUTO	DR 10W12	00	ting gaper, datase stripty, etchin stripty street stripty and stripty street
BW06	90	A05	26	2+45 MEMORY REGISTER	10W12	90	101 28
8 W O 6	91	A06	2	2+46 MEMORY REGISTER	10W12	91	102 28
3 W D 6	92	A06	26	2+47 MEMORY REGISTER	10W12	92	103 28
SWOS	93	A07	2	2+48 MEMORY REGISTER	10W12	93	104 28
WD6	94	AD7	26	2+49 MEMORY REGISTER	10W12	94	105 28
3W06	95	A 0 8	2	2+50 MEMORY REGISTER	10W12	95	108 28
WO6	96	80A	26	2+51 MEMORY REGISTER	10W12	96	109 28
W06	97	801	2	2+52 MEMORY REGISTER	10W12	97	110 28
WOS.	98	801	26	2+53 MEMORY REGISTER	10W12	98	111 28
W06	99	B02	2	2+54 MEMORY REGISTER	10W12	99	112 28
W 0 6	900	B 0 2	26	2+55 MEMORY REGISTER	10W12	900	114 28
WOS	901	B03	2	2+56 MEMORY REGISTER	10W12	901	115 28
WOS	902	B03	26	2+57 MEMORY REGISTER	10W12	902	116 28
WO6	903	B 0 4	2	2+58 MEMORY REGISTER	10W12	903	117 28
3W06	904	B 0 4	26	2+59 MEMORY REGISTER	10W12	904	118 28
W06	905				10W12	905	
W 0 6	906				10W12	906	
WO6	907				10W12	907	
W06	908				10W12	908	

8W07	00		<del>-</del>	REGIS	TER TO MI	ULTIPLY	الله المالية المالية المالية المالية (المالية (المالية (المالية (المالية (المالية (المالية (المالية (المالية (		6W07	0.0			
8W07	90	C09	3	2+36 F	REGISTER	TO MULTIPLY	OPERAND	1(XK)	6W07	90	H04	12	
8W07	91	C09	21	2+37 F	REGISTER	TO MULTIPLY	OPERAND	1(XK)	6W07	91	H04	14	
8W07	92	009	25	2+38 F	REGISTER	TO MULTIPLY			6W07	92	H04	16	
8W07	93	C10	3	2+39 F	REGISTER	TO MULTIPLY	OPERAND	1(XK)	6W07	93	H04	18	
8W07	94	C10	21	2+40 F	REGISTER	TO MULTIPLY	OPERAND	1(XK)	6W07	94	H05	12	
8W07	95	C10	25	2+41 F	REGISTER	TO MULTIPLY	OPERAND	1(XK)	6W07	95	H05	14	
8W07	96	C11	3	2+42 F	REGISTER	TO MULTIPLY	OPERAND	1(XK)	6W07	96	H05		
8W07	97	C11	21	2+43 F	REGISTER	TO MULTIPLY	OPERAND	1(XK)	6W07	97	H05	18	
8W07	98	C11	25	2+44 F	REGISTER	TO MULTIPLY	OPERAND	1(XK)	6W07	98	H06		
8W07	99	C12	3	2+45 F	REGISTER	TO MULTIPLY,	OPERAND	1(XK)	6W07	99	H06	14	
8W07	900	012	21	2+46 F	REGISTER	TO MULTIPLY	OPERAND	1(XK)	6W07	900	H06		
8W07	901	C12	25	2+47 F	REGISTER	TO MULTIPLY,			6W07	901	H06		
8W07	902	D12	25	2+59 F	REGISTER	TO MULTIPLY,	OPERAND	1(XK)	6W07	902	F07		
8W07	903							***************************************	6W07	903			
8W07	904								6W07	904			
8W07	905					About March Cough Cough Cough Cough Cough Cough Cough Cough Cough Cough Cough Cough Cough Cough Cough Cough Co			6W07	905			
8W07	906								6W07	906			
8W07	907				THE THE PAR THE SEE STEEL STEEL SHEET THE THE SECT OF	to the two two control who the two took too the two two two two two two two	t dieter 18 dags von 168 inter 166 inter dem geleit spelg a. a.		6W07	907			
8W07	908								6W07	908			

W 0 8	0.0		REGISTER FROM	MULTIPLY	6W08	00	e maar maar waar walke when ilaah dadh dadh dhoo ilaan mah- ilaah wool suur, uusu .	
W 0 8	90	805 2	2+36 MULTIPLY	TO REGISTER	6W08	90	F03 1	
W08	91	B05 26	2+37 MULTIPLY	TO REGISTER	6W08	91	F03 5	
WD8	92	B06 2	2+38 MULTIPLY	TO REGISTER	6W08	92	F03 25	
WOB	93	B06 26	2+39 MULTIPLY	TO REGISTER	6W08	93	F03 27	
WD8	94	B07 2	2+40 MULTIPLY	TO REGISTER	6W08	94	F04 1	
WOB	95	B07 26	2+41 MULTIPLY	TO REGISTER	6W08	95	F04 5	
W08	96	B08 2	2+42 MULTIPLY	TO REGISTER	6008	96	F04 25	
WO8	97	B08 26	2+43 MULTIPLY	TO REGISTER	6W08	97	F04 27	* * · · · · · · · · · · · · · · · · · ·
W D 8	98	C01 2	2+44 MULTIPLY	TO REGISTER	6W08	98	F05 1	
WO8	99	C01 26	2+45 MULTIPLY	TO REGISTER	6W08	99	F05 5	
W 0 8	900	C02 2		TO REGISTER	6W08	900	F05 25	
WD8	901	C02 26	2+47 MULTIPLY	# 1	6W08	901	F05 27	
W D B	902			, -	6W08	902		
WOB	903	i iki tang gerandaga dengenaga an ini samigas perandaga	ppermission and a complete construction of the		6W08	903		
WOB	904				6W08	904		
W D B	905			• • • • • • • • • • • • • • • • • • •	6W08	905	ng pang-mang mang pang at an entary pang-army pang-army pang-army pang-army	
WD8	906				6W08	906		
WO8	907				6W08	907		
WOB	908				6W08	908		

8W09	0.0			REGI	STER	TO M	ULT	IPLY			6W09	0.0		
8W09	90	E09	3	2+36	REG	STER	TO	MULTIPLY	OPERAND	(XJ)	6W09	90	J04	12
8W09	91	E09	21	2+37	REG	STER	TO	MULTIPLY,	OPERAND	(XJ)	6W09	91	J04	14
8W09	92	E09	25	2+38	REG	STER	ŤΟ		DPERAND	(XJ)	6W 09	92	J04	16
8W09	93	E10	3	2+39	REG	STER	10	MULTIPLY	OPERAND	(XJ)	6W09	93	J04	18
8W09	94	E10	21	2+40	REG	STER	TO	MULTIPLY	OPERAND	(XJ)	6W09	94	J05	12
8W09	95	E10	25	2+41	REG	STER	TO	MULTIPLY,	OPERAND	(XJ)	6W09	95	J05	14
8W09	96	E11	3	2+42	REG	STER	ŤO		OPERAND	(XJ)	6W09	96	J05	16
8W09	97	E11	21	2+43	REG	STER	TO	MULTIPLY	OPERAND	(XJ)	6W09	97	J05	18
BWOP	98	E11	25	2+44	REG	STER	ŤO	MULTIPLY	OPERAND	(XJ)	6W09	98	J06	12
8W09	99	E12	3	2+45	REG	STER	TO	MULTIPLY	OPERAND	(XJ)	6W09	99	J06	14
8W09	900	E12	21	2+46	REG	STER	ŤO	MULTIPLY	OPERAND	(XJ)	6W09	900	J06	16
8W09	901	E12	25	2+47	REG	STER	ŤO	MULTIPLY	DPERAND	(XJ)	6W09	901	J06	18
8W09	902	F12	25	2+59	REG	STER	ŤO	MULTIPLY	OPERAND	(LX)	6W09	902	F06	15
8W09	903										6W09	903		
8W09	904										6W09	904		
8W09	905										6W09	905		
8W09	906										6W09	906		
8409	907										6W09	907		and the second of
8W09	908										6W09	908		

W10	00		UPPER REGISTER TO MEMORY DISTRIBUTION	2W11	00	·····
W10	90	A09 25	2+38 REGISTER TO DISTRIBUTOR	2W11	90	A14 10
W10	91	A10 3	2+39 REGISTER TO DISTRIBUTOR	2W11	91	A14 21
W10	92	A10 21	2+40 REGISTER TO DISTRIBUTOR	2W11	92	A14 24
W10	93	A10 25	2 *41 REGISTER TO DISTRIBUTOR	2W11	93	A14 26
W10.	94	A11 3	2+42 REGISTER TO DISTRIBUTOR	2W11	94	A15 5
W10	95	A11 21	2+43 REGISTER TO DISTRIBUTOR	2W11	95	A15 7
W10	96	A11 25	2+44 REGISTER TO DISTRIBUTOR	2W11	96	A15 10
W10	97			2W11	97	
W10	98			2W11	98	
W10	99			2W11	99	
W10	900			2W11	900	
W10	901			2W11	901	
W10	902			2W11	902	
W10	903		emining (mire in the emining of the emininary of the emining of the emining of the emining of the eminion of the emining of the emininary of the emininary of the eminion o	2W11	903	Page programme and a grant of warring
W10	904			2W11	904	
W10	905		The second section of the second seco	2W11	905	
410	906	A09 3	2+36 REGISTER TO DISTRIBUTOR	2W11	906	A14 5
W10	907	A09 21	一个在个人,一点一直看上一个一点,一点一点一点一点,我们就像这	2W11	907	A14 7
W10	908			2W11	908	,

8W11	00		UPPER REGISTER TO MEMORY DISTRIBUTION	2W12	.00
8W11	90	B10 25	2+53 REGISTER TO DISTRIBUTOR	2W12	90 A16 26
8W11	91	B11 3	2+54 REGISTER TO DISTRIBUTOR	2W12	91 A17 5
8W11	92	B11 21	2455 REGISTER TO DISTRIBUTOR	2W12	92 A17 7
8W11	93	811 25	2+56 REGISTER TO DISTRIBUTOR	2W12	93 A17 10
8W11_	94	B12 3	2+57 REGISTER TO DISTRIBUTOR	2W12	94 A17 21
8W11	95	812 21	2+58 REGISTER TO DISTRIBUTOR	2W12	95 A17 24
8W11	96	B12 25	2+59 REGISTER TO DISTRIBUTOR	2W12	96 A17 26
8W11	97	,		2W12	97
8W11	98			2W12	94
8W11	99			2W12	99
8W11	900	A12 3	2+45 REGISTER TO DISTRIBUTOR	2W12	900 A15 21
8W11	901	A12 21	2+46 REGISTER TO DISTRIBUTOR	2W12	901 A15 24
8W11	902	A12 25	2+47 REGISTER TO DISTRIBUTOR	2W12	902 A15 26
8W11	903	809 3	2+48 REGISTER TO DISTRIBUTOR	2W12	903 A16 5
8W11	904	B09 21	2+49 REGISTER TO DISTRIBUTOR	2W12	904 A16 7
8W11	905	B09 25	2.50 REGISTER TO DISTRIBUTOR	2W12	905 A16 10
8W11	906	B10 3	2+51 REGISTER TO DISTRIBUTOR	2W12	906 A16 21
8W11	907	B10 21	2+52 REGISTER TO DISTRIBUTOR	2W12	907 A16 24
8W11	908			2W12	908

8W12	0 0		LONG ADD, ADD AND SHIFT FROM CONTROL	5W12	00		
8W12	90	H01 5	LONG ADD IN	5W12	90	E40 27	
8W12	91	H01 10	ADD IN	5W12	91	E40 25	A marger of employees (V Leth valid (V))
8W12	92	I19 9	MASK	5W12	92	E39 12	
8W12	93	H01 21	ROUND ADD	5W12	93	E39 21	
8W12	94	H01 24	ADD	5W12	94	E39 19	
8W12	95	G16 5	PACK	5W12	95	E39 8	
8W12	96	G16 10	UN-PACK	5W12	96	E39 10	
8W12	97	G16 21	SHIFT (JK)	5W12	97	E38 19	*********
8W12	98	G16 26	SHIFT NOMINAL	5W12	98	E38 23	
8W12	99	G16 24	SHIFT LEFT	5W12	99	E38 21	AND REAL PROPERTY AND ADDRESS.
8W12	900	H01 7		5W12	900	E39 6	
8W12	901	F01 24	TRANSMIT LONG ADD	5W12	901	J40 21	
8W12	902	G01 24	TRANSMIT SHIFT	5W12	902	J40 12	
8W12	903	G02 24	TRANSMIT ADD	5W12	903	J40 19	
	904	H06 4	REQUEST RELEASE SHIFT	5W12	904	J22 19	
8W12			LONG ADD - PLUS	5W12	905	E39 27	
8W12	905	I19 5		5W12	906	K22 19	
8W12	906	H06 8	REQUEST RELEASE LONG ADD		907	Approximately and the second s	
8W12	907	H01 26	SHIFT IN	5W12	• • •		
8W12	908	G16 7	NORM. (SHIFT)	5W12	908	E39 4	-

8W13	00		UPPER REGISTER FROM CONTROL	5W11	00	
BW13:	90	E06 26	INCR SIGN	5W11	90	F42 25
8W13	91			5W11	91	
8W13	92	G01:26	ADD MINUS	5W11	92	E39 23
3W13	93			5W11	93	ne and and and and and and and and and and
3W13	94			5W11	94	
5W13	95	THE PART AND ADDRESS OF THE PART AND ADDRESS OF THE PART AND ADDRESS OF THE PART AND ADDRESS OF THE PART AND ADDRESS OF THE PART AND ADDRESS OF THE PART ADDRESS OF TH		5W11	95	a desir della parti parti della matti anti inter inter anti anti inter inte inter in
3W13	96			5W11	96	
W13	97			5W11	97	
W13	98			5W11	98	
W13	99			5W11	99	
W13	900	H06 12	LONG ADD INDEFINITE	5W11	900	042 24
W13	901	HD6 10	LONG ADD INFINITE	5W11	901	042 21
W13	902			5W11	902	
W13	903			5W11	903	
W13	904			5W11	904	
W13	905			5W11	905	and which which have been highly been been been some their seed that we have the seed of t
W13	906			5W11	906	
W13	907			5W11	907	in the second section of the second section of the second
W13	908			5W11	908	

والمراجع والمراجع والمتعارض والمتعارض والمتعارض والمتعارض والمتعارض والمتعارض والمتعارض والمتعارض والمتعارض والمتعارض

8W18	0.0			REGISTER TO DIVIDE	2W22	00		
8W18	90	D09	3	2+48 X++K (EXPONENT)	2W22	90	M08	12
8W18	91	D09	21	2+49 X++K (EXPONENT)	2W22	91	M08	14
8W18	92	D09	25	2+50 X++K (EXPONENT)	2W22	92	MOB	16
8W18	93	D10	3	2+51 X++K (EXPONENT)	2W22	93	MOS	18
8W18	94	D10	21	2+52 X++K (EXPONENT)	2W22	94	M09	12
8W18	95	D10	25	2+53 X++K (EXPONENT)	2W22	95	M 0 9	14
8W18	96	D11	3	2+54 X++K (EXPONENT)	2W22	96	M09	16
8W18	97	D11	21	2+55 X++K (EXPONENT)	2W22	97	M09	18
8W18	98	D11	25	2+56 X++K (EXPONENT)	2M22	98	M10	12
8W18	99	D12	3	2+57 X++K (EXPONENT)	2W22	99	M10	14
8W18	900	D12	21	2+58 X++K (EXPONENT)	2M22	900	M10	16
BW18	901	D12	23	2+59 X++K (EXPONENT)	2W22	901	M10	18
8W18	902	C03	2	2+48 EXPONENT RESULT-REG.	2W22	902	K13	1
BW18	903	C03	26	2+49 EXPONENT RESULT-REG.	2W22	903	K13	6
BW18	904	C 0 4	2	2.50 EXPONENT RESULT-REG.	2W22	904	K13	23
8W18	905	C 0 4	26	2+51 EXPONENT RESULT-REG.	2W22	905	K13	28
8W18	906	C05	2	2+52 EXPONENT RESULT-REG.	2W22	906	K14	1
8W18	907	C 0 5	26	2+53 EXPONENT RESULT-REG.	2W22	907	K14	6
BW18	908	H06	19	ERROR (EXP = 1777 FROM CHASSIS 7)	2W22	908	E02	5

8W19	0.0			REGISTER TO DIVIDE	2W23	0.0	the state which have been been state and the state which the state of
8W19	90	F09	3	2+46 X++J (EXPONENT)	:2W23	90	L08 12
8W19	91	F09	21	2+49 X++J (EXPONENT)	2W23	91	L08 14
8W19	92	F09	25	2+50 X++J (EXPONENT)	2W23	92	L08 16
8W19	93	F10	3	2+51 X++J (EXPONENT)	2W23	93	L08 18
8W19.	94	F10	21	2+52 X++J (EXPONENT)	2W23	94	L09 12
8W19	95	F10	25	2+53 X++J (EXPONENT)	2W23	95	L09 14
8W19	96	F11	3	2+54 X++J (EXPONENT)	2W23	96	L09 16
8W19	97	F11	21	2+55 X++J (EXPONENT)	2W23	97	L09 18
8W19	98	F11	25	2+56 X++J (EXPONENT)	2W23	98	L10 12
8W19	99	F12	3	2+57 X++J (EXPONENT)	2W23	99	L10 14
8W19	900	F12	21	2+58 X++J (EXPONENT)	2W23	900	L10 16
8W19	901	F12	23	2+59 X++J (EXPONENT)	2W23	901	L10 18
8W19	902	006	2	2+54 EXPONENT RESULT=REG	2W23	902	K14 23
8W19	903	C06	26	2+55 EXPONENT RESULT-REG	2W23	903	K14 28
8W19	904	C07	2	2+56 EXPONENT RESULT-REG	2W23	904	K15 1
8W19	905	C07	26	2.57 EXPONENT RESULT-REG	2W23	905	K15 6
8W19	906	C08	2	2+58 EXPONENT RESULT-REG	2W23	906	K15 23
8W19	907	C08	26	2+59 EXPONENT RESULT-REG	2W23	907	K15 28
8W19	908	H06	21	ERROR (EXP = 3777 FROM CHASSIS 8)	2W23	908	E02 7

8W20	0.0			UPPER REGISTER FROM CONTROL	5W15	0.0	
8W20	90	G 0 5	17	MEMORY TO D GO	5W15	90	142
8W20	91	G 0 5	5	MEMORY TO D 0	5W15	91	142
BW20	92	G 0 5	4	MEMORY TO D 1	5W15	92	142 2
8W20	93	G 0 5	16	MEMORY TO D 2	5W15	93	142 2
W20	94	F 0.2	21	K TO MULTIPLY GO	5W15	94	J42 1
W20	95	F02	5	K TO MULTIPLY O	5W15	95	J42 3
W20	96	F02	7	K ŤO MÚĽTIPLŸ 1	5W15	96	J42 2
W20	97	F02	10	K TO MULTIPLY 2	5W15	97	J42 2
W20	98	G 0.7	16	ADD TO X 2	5W15	98	H39 2
W20	99				5W15	99	THE REAL PROPERTY AND ADDRESS OF THE PARTY AND
W20	900	121	12	CLOCK	5W15	900	J39 2
W20	901	G06	17	MULTIPLY TO X GO	5W15	901	H38
W20	902	G06	-5	MULTIPLY TO X 0	5W15	902	H38 ;
W20	903	G06	4	MULTIPLY TO X 1	5W15	903	H38 2
W20	904	G06	16	MULTIPLY TO X 2	5W15	904	H38 2
W20	905	G 0 7	17	ADD TO X GO	5W15	905	H39
W20	906	G 0 7	5	ADD TO X 0	5W15	906	H39 :
W20	907	G07	4	ADD TO X 1	5W15	907	H39 2
W20	908				5W15	908	

8W21	0.0			UPPE	REGISTE	R FROM CONTROL	5W17	0.0		
8W21	90	F01	5	X TO	ADD	0	5W17	90	K42	3
8W21	91	F01	7	X TO	ADD	1	5W17	91	K42	28
8W21	92	F01 1	0	X to	ADD	2	5W17	92	K42	24
8W21	93	F01 2	1	X TO	ADD	30	5W17	93	K42	7
8W21	94	G08 1	7	INCR	TO X GO		5W17	94	H40	7
8W21	95	G08	5	INCR	TO X O	and the same table	5W17	95	H40	3
8W21	96	G 0 8	4	INCR	TO X 1		5W17	96		28
8W21	97	G08 1	6	INCR	TO X 2		5W17	97	H40	
8W21	98	H06 2	3	LONG	ADD ZERO	CH 5	5W17	98	042	
8W21	99	H06 2	5	LONG	ADD SIGN	CH 5	5W17	99	042	7
8W21	900	G01 2	1	J TO	MULTIPLY	<b>60</b>	5W17	900	J41	7
8W21	901	G01	5	J TO	MULTIPLY	0	5W17	901	J41	3
8W21	902	G01	7	J TO	MULTIPLÝ	i	5W17	902	J41	28
8W21	903		0	J TO	MULTIPLY	2	5W17	903	J41	
8W21	904		1	X TO	ADD	ā O	5W17	904	K41	7
8W21	905	E01	3	X To	ADD	Õ	9W17	905	K41	3
8W21	906	E01	7	X TO	ADD	1	5W17	906	K41	28
8w21	907	E01 1	. 1	X TO		2	5W17	907	K41	
8W21	988			10		_	5W17	908		

8W22	0.0		UPPER REGISTER FROM CONTROL				
8W22	90		D++4 TO X++4				
8W22	91	E01 24	EXCHANGE	5W26	91	033	8
8W22	92	H02 4	ENTER (JK) 2+0	5W26	92	035	8
8W22	93	H02 6	ENTER (JK) 2+1	5W26	93	035	6
8W22	94	H02 7	ENTER (JK) 2+2	5W26	94	035	
8W22	95	H02 22	ENTER (JK) 2+3	5W26	95	035	27
8W22	96	H02 25	ENTER (JK) 2+4	5W26	96	035	25
8W22	97	H02 27	ENTER (JK) 2+5	5W26	97	035	23
8W22	98	H06 6	REQUEST RELEASE ADD	5W26	9.8	K23	19
8W22	99	H06 27	LONG ADD SER CONTROL	5W26	99	042	5
8W22	900		D to X++0				
8W22	901		D TO X++1				
8W22	902		D 70 X++2				
8W22	903		D TO X++3				
8W22	904	G02 21	X TO MEMORY GO	5W26	904	031	. 8
8W22	905	G02 5	X TO MEMORY O	5W26	905	031	3
8W22	906	002 7	X TO MEMORY 1	5W26	906		28
8W22	907	G02 10	X TO MEMORY 2	5W26	907	031	25
8W22	908			5w26	908		

8M52	0.0			D to x	nai and tro- c'e- co- pag and the first and and and and and and and and and and	7W30	0.0		and the second s
8W23	90	G11	5	D+1 TO X+1	1 UPPER	7w30	90	L26	8
8W23	91	G11	7	D+2 TO X+2	2 UPPER	7W30	91	L26	. 6
8W23	92	011	10		3 UPPER	7W30	92	L26	4
8W23	93	G11	21		4 UPPER	7W30	93	L26	27
6W23	94	011	24		5 UPPER	7W30	94	L26	
8W23	95	, son a <b>Pille (8)</b> a <b>Pil</b> le		aan ah 1800 Maria oo ah ah ah aa ah 1800 maria dha ah ah ah ah ah ah ah ah ah ah ah ah a		7W30	95		management of the second of th
8W23	96					ว์พิธีก	96		
8W23	97					7830	97		The state of the s
8W23	98					7830	98		
8W23	99			a eena siraa riinka dalka baasa <b>diikka saaka diinka diikka</b> nadan aabadi harka suusa sakuu, aabasa di	. Anny 1960. Anny April 1969, 1960, 1960, 1960, 1960, 1960, 1960, 1960, Anny 1960, 1	7830	99		
8W23	900					7830	900		
8W23	901			a company that may not make the law tool and upon may then w	way ship, man man galir man man its sun is an any time that then, contained one for the later with the media.	7₩30	901		
8w23	902					77430	902		
8W23	903					7W30	903		
8W23	904					7W30	904		
8W23	905			over return model traps drops about most about traps are some constraints and constraints are	dans while many angle and whose place work yang areal said. Addr gaths while land sold date you care does with my your pass and	7w3u			ages more place made state of participations are participated as a first of the contract of th
	906						905		
8W23	907					7W30	906		
	-			•		7W30	907		
8W23	908						908		

8W24	00			ADD	TO	REG	151	ER	.,	the coal title and title that has the date of the coal are the coal and the coal title part that you coal are now make may be	7W26	0.0		
8W24	90	M01	1	2+0	ADE	TO	RE	GÍ	STER		7W26	90	H37	2
8W24	91	MO1	6	2+1	ADD	10	RE	GI	STER	The state of the s	7W26	91	H37	26
8W24	92	M 0 1	23	2 + 2	ADD			G t	STER		7W26	92	H38	2
8W24	93	M01	28	2+3	ADI	70			STER	na dan mer man dan dan mer dan mer mer dan dan mer mer dan dan dan dan dan dan dan dan dan dan	7W26	93	H38	26
8 W 2 4	94	M02	1	2+4	ADI			GI	STER		7W26	94	H39	2
8W24	95	MO2	6	2+5	ADE	70			STER		7W26	95	H39	26
8W24	96	M02	23	2+6	ADD				STER		7W26	96	H40	2
8W24	97	M02	28	2+7	ADI	10			STER	<del>and a children freezie of the property of the control of the cont</del>	7W26	97	H40	26
8W24	98	MO3	<u></u>	2+8	ADD				STER		7W26	98	H41	2
8W24	99	MO3	6	2+9	ADE				STER	TV die das 100 500 bas 500 100 100 100 100 100 100 100 100 100	7W26	99	H41	26
8W24	900	MO3	23	2+10	A	D TO			ISTER		7W26	900	H42	2
8W24	901	MO3	28	2+11	A	D T			ISTER		7W26	901	H42	
8W24	902	MD4	1	2+12	AL	D TO			ISTER		7W26	902	J37	2
8W24	903	MO4	6	2+13		D 1			ISTER	Microsoft in 1984, process of the second collection of the second colle	7W26	903	J37	26
8W24	904	M04	23	2+14	A	D TO	) (	EG	ISTER		7W26	904	J38	
8W24	905	M04	28	2+1		ter ter in the second			ISTER		7W26	905	J38	26
BW24	906	M05	-1	2+16					ISTER		7W26	906	J39	-2
8W24	907	M05	6	2+17					ISTER		7W26	907	J39	26
8W24	908										7W26	908	20,	~ 0

8W25	0.0			ADD '	TO REG	īst	ER	7W27	0.0		
8w25	90	M05	23	2+18	ADD T	O R	EGISTER	7W27	90	J40	2
8W25	91	M05	28	2+19	ADD T	OR	EGISTER	7w27	91	J40	26
3W25	92	NO1	1	2+20	ADD T	0 R	EGISTER	7W27	92	J41	
W25	93	NO1	6	2+21	ADD T	0 R	EGISTER	7W27	93	J41	
W25	94	NO1	23	2.22	ADD T	0 R	EGISTER	7 W 2 7	94	J42	2
W25	95	NO1	28	2+23	ADD T	0 R	EGISTER	7W27	95	J42	
W25	96	N02	1	2+24	ADD T	OR	EGISTER	7W27	96	K37	2
W25	97	N02	6	2+25	ADD T	0 R	EGISTER	7W27	97	K37	26
W25	98	N02	23	2+26	ADD T	0 R	EGISTER	7W27	98	K38	2
W25	99	NO2	28	2+27	ADD T	0 R	EGISTER	7W27	99	K38	26
W25	900	No3	1	2+28	ADD T	0 R	EGISTER	7W27	900	K39	2
W25	901	N03	6	2+29	ADD T	OR	EGISTER	7W27	901	K39	26
W25	902	NO3	23	2+30	ADD T	0 R	EGISTER	·7W27	902	K40	2
W25	903	NO3	28	2+31	ADD T	OR	EGISTER	7W27	903	K40	26
W25	904	NO4	1	2+32	ADD T	0 R	EGISTER	7W27	904	K41	2
W25	905	N 0 4	6	2+33	ADD T	ÖR	EGISTER	7W27	905	K41	26
W25	906	N04	23	2+34	ADD T	0 R	EGISTER	7W27	906	K42	2
W25	907	NO 4	28	2+35	ADD T	OR	EGISTER	7W27	907	K42	26
W25	908							7W27	908		

											*				
8W26	0.0		-	ADD	FROM	REG	İSTE	R				7W24	0.0		
8W26	90	J01	11	2+0	REGI	STER	TO	ADD;	OPERAND	1 (XK)	)	7W24	90	M39	3
8W26	91	J01	13	2+1	REGI	STER	TO	ADD.	OPERAND	1(XK)	)	7W24	91	M39	25
8W26	92	J02	11	2+2	REGI	STER	TO	ADD.	OPERAND	1(XK)	)	7W24	92	M39	21
8W26	93	J02	13	2+3	REGI	STER	TO	ADD.	OPERAND	1(XK)	)	7W24	93	M40	3
8W26	94	103	11	2+4	REGI	STER	TO	ADD.	OPERAND	1(XK)	)	7W24	94	M40	25
8W26	95	J03	13	2+5	REGI	STER	TO	ADD.	OPERAND	1(XK)	)	7W24	95	M40	21
8W26	96	J04	. 8	2+6	REGI	STER	TO	ADD,	O-E-AND	1UOK)		7W24	96	M41	3
8W26	97	J04	24	2+7	REGI	STER	TO	ADD.	OPERAND	1(XK)	)	7W24	97	M41	25
8W26	98	J05	8	2+8	REGI		TO	ADD,	OPERAND	1(XK)	)	7W24	98	M41	21
8W26	99	J05	24	2 + 9	REGI	STER	TO	ADD.	OPERAND	1(XK)	) ,	7W24	99	M42	3
8W26	900	J06	. 8	2+1		ISTE		ADD			$\Theta$	7W24	900	M42	25
8W26	901	J06	24	2+1		ISTE					()	7W24	901	M42	21
8W26	902	J07	6	2+1	2 REG	ISTE					()	7W24	902	N35	3
8W26	903	J07	24	2+1		ISTE		DOD.		-	()	7W24	903	N35	25
8W26	904	J08	6	2+1	4 REG	ISTE	RT	ADD,			$\odot$	7W24	904	N35	21
8W26	905	J08	24	2+1		ISTE		ADD	OPERAN	D 1 (XK	$\odot$	7W24	905	N36	3
8W26	906	J09	6	2+1	6 REG	ISTE	R ÍC	ADD,	OPERAN!	D 1(XK	()	7W24	906	N36	25
8W26	907	J09	24	2 - 1	7 REG	ISTE	ŘŤĆ	ADD,	OPERAN:	D i(XK	O	7W24	907	N36	21
8W26	908											7W24	908		

8W27	00			ADD FROM REGISTER	7W25	00		MIC NOTE AND AND THE
8w27	90	J10	-6	2+18 REGISTER TO ADD, OPERAND 1(XK)	7W25	90	N37	3
8W27	91	J10	24	2+19 REGISTER TO ADD, OPERAND 1(XK)	7W25	91	N37	25
8W27	92	J11	6	2+20 REGISTER TO ADD, OPERAND 1(XK)	7W25	92	N37	21
8W27	93	J11	24	2+21 REGISTER TO ADD, OPERAND 1(XK)	7W25	93	N38	3
8W27	94	KO1	6	2+22 REGISTER TO ADD, OPERAND 1(XK)	7W25	94	N38	25
8W27	95	K01	24	2+23 REGISTER TO ADD, OPERAND 1(XK)	7W25	95	N38	21
8W27	96	K02	6	2+24 REGISTER TO ADD, OPERAND 1(XK)	7W25	96	N39	3
8W27	97	K02		2+25 REGISTER TO ADD, OPERAND 1(XK)	7W25	97	N39	25
8W27	98	Kg3	6	2+26 REGISTER TO ADD, OPERAND 1(XK)	7W25	98	N39	21
8W27	99	K03	24	2+27 REGISTER TO ADD, OPERAND 1(XK)	7W25	99	N40	3
8W27	900	K04	- 6	2+28 REGISTER TO ADD, OPERAND 1(XK)	7W25	900	N40	25
8W27	901	KO4	24	2+29 REGISTER TO ADD, OPERAND 1(XK)	7w25	901	N40	21
8W27	902	K05	6	2+30 REGISTER TO ADD, OPERAND 1(XK)	7W25	902	N41	3
8W27	903	K05	24	2+31 REGISTER TO ADD, OPERAND 1(XK)	7W25	703	N41	25
8W27	904	K06	6	2#32 REGISTER TO ADD, OPERAND 1(XK)	7W25	904	N41	21
8W27	905	KOS	24	2+33 REGISTER TO ADD, OPERAND 1(XK)		05		3
8W27	906	K07	6	2+34 REGISTER TO ADD, OPERAND 1(XK)	7W25	906	N42	25
8W27	907	KO7	24	2+35 REGISTER TO ADD, OPERAND 1(XK)	the state of the s	07	N42	- AND THE RES TO
8W27	908					806		

8W28	0.0	·· · · · · · · · · · · · · · · · · · ·		ADD	FROM REGISTER	7W28	0.0	
8W28	90	L09	14	2+0	REGISTER TO ADD, OPERAND 2(BJ+XJ)	7W28	90	L35 3
8W28	91	L09	18	2+1	REGISTER TO ADD, OPERAND 2(BJ+XJ)	7W28	91	L35 25
8W28	92	L10	14	2+2	REGISTER TO ADD, OPERAND 2(BJ+XJ)	7W28	92	L35 21
8W28	93	L10	18	2+3	REGISTER TO ADD, OPERAND 2(BJ+XJ)	7W28	93	L36 3
8W28	94	L11	14	2+4	REGISTER TO ADD, OPERAND 2(BJ+XJ)	7W28	94	L36 25
8W28	95	L11	18	2+5	REGISTER TO ADD, OPERAND 2(BJ+XJ)	7W28	95	L36 21
8W28	96	J04	7	2+6	REGISTER TO ADD, OPERAND 2(BJ+XJ)	7W28	96	L37 3
8W28	97	J04	21	2+7	REGISTER TO ADD, OPERAND 2(BJ+XJ)	7W28	97	L37 25
8W28	98	J05	7	2+8		7W28	98	L37 21
8W28	99	J05	21	2+9		7W28	99	L38 3
8W28	900	J06	7_	2+1		7W28	900	L38 25
8w28	901	J06	21	2+1	F 11 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1	7W28	901	L38 21
8W28	902	J07	7	2+12		7W28	902	L39 3
8W28	903	J07	21	2*1		7W28	903	L39 25
8W28	904	J08	7	2+1	the same training and the same training are the same training and the same training are the same training and the same training are the same training and the same training are the same training and the same training are	7W28	904	L39 21
8W28	905	J08	21	2+1		7W28	905	L40 3
8W28	906	J09	7	2+1		7W28	906	L40 25
8W28	907	117	23	2+1	REGISTER TO ADD, OPERAND 2(BJ+XJ)	7W28	907	L40 21
8W28	908	. Allow developes and these		wer street, carpet - w	NOTE TO A STREET OF THE PARTY O	7W28	908	os companyación de la companyación de la companyación de la companyación de la companyación de la companyación

8W29	0 0			ADD	FROM	REGIS	STE	₹			7W29	0 0			
8W29	90	J10	7	2+18	REGI	STER	TO	ADD,	OPERAND	S(XJ+BJ)	7W29	90	L41	3	
8W29	91	J10	21	2+19	REGI	STER	ŤΟ	ADD.	OPERAND	Z(XJ+BJ)	7W29	91	L41	25	•
8W29	92	J11	- <del>-</del> -	2+20		STER	ŤΟ	ADD.	OPERAND	2(XJ+BJ)	7W29	92	L41	21	
8W29	93	J11	21	2+21	REG	STER	ŤΟ	ADD.	OPERAND	2(XJ+BJ)	7W29	93	L42	3	
8W29	94	K01	- <del>-</del> -	2+22		STER	TO	ADD,	OPERAND	2(XJ+BJ)	7W29	94	L42	25	
8W29	95	K01	21	2+23		STER	ŤO	ADD.	OPERAND	2(XJ+BJ)	7W29	95	L42	21	
BW29	96	K02	7	2+24		STER	TO	ADD,	OPERAND	2(XJ+BJ)	7W29	96	M35	3	
8W29	97	-	21	2+25		STER	ŤÖ	ADD.	OPERAND	2(XJ+BJ)	7W29	97	M35	25	
BW29	98	K03	- <del>-</del> -	2+26		STER	TO	ADD,	OPERAND	2(XJ+BJ)	7W29	98	M35	21	
8W29	99	<del></del>	21	2+27		STER	ŤO	ADD.	OPERAND	S(XJ+BJ)	7W29	99	M36	3	
8W29	900	K04	7	2+28	-	STER	ŤO	ADD.	OPERAND	2(XJ+BJ)	7W29	900	M36	25	
8W29	901	_	21	2+29		STER	TO	ADD,	OPERAND	2(XJ+BJ)	7W29	901	M36	21	
8W29	902	Ka5	7	2+30			ŤΟ	ADD.	OPERAND	2(XJ+BJ)	7W29	902	M37	3	
8W29	903	Tallacques and the	21	2+31		STER	ŤÕ	ADD.	OPERAND	2(XJ+BJ)	7W29	903	1 4 7 7 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	25	• • •
8W29	904	K06	- 7	2+32			ŤO	ADD.	OPERAND	2(XJ+BJ)	7W29	904	M37	21	
8W29	905		21	2+33		STER	ŤÖ	ADD.	OPERAND	2(XJ+BJ)	7W29	905	M38	3	
8W29	906	K07	7	2+34		STER	ŤO	ADD.	OPERAND	2(XJ+BJ)	7W29	906	M38	25	
8W29	907		21	2+35		STER	ŤΟ	ADD.	OPERAND	2(XJ+BJ)	7W29	907	M38	21	
8W29	908		~ 1	200	, neu-	U I ER	, 0			ETAGTET	7W29	908	. 1150	- 1	

8W30	00			SHIFT TO REGISTER	7W31	00	
8W30	90	H03	1	2±0 SHIFT TO REGISTER	7w31	90	038 2
8W30	91	Hn3	5	2+1 SHIFT TO REGISTER	7W31	91	038 26
8W30	92	HD3	25	2+2 SHIFT TO REGISTER	7W31	92	039 2
8W30	93	HD3	27	2+3 SHIFT TO REGISTER	7W31	93	039 26
8W30	94	H04	1	2+4 SHIFT TO REGISTER	7W31	94	040 2
8W30	95	H04	5	2+5 SHIFT TO REGISTER	7W31	95	040 26
8W30	96	H 0 4	25	2+6 SHIFT TO REGISTER	7W31	96	041 2
BW30	97	H04	27	2*7 SHIFT TO REGISTER	7W31	97	041 26
8W30	98	H05	1	2+8 SHIFT TO REGISTER	7W31	98	042 2
8W30	99	H05	5	2#9 SHIFT TO REGISTER	7W31	99	042 26
8W30	900	H17	1	2+10 SHIFT TO REGISTER	7W31	900	P38 2
8W30	901	H17	5	2+11 SHIFT TO REGISTER	7W31	901	P38 26
8W30	902	H17	25	2*12 SHIFT TO REGISTER	7W31	902	P39 2
8W30	903	H17	27	2+13 SHIFT TO REGISTER	7W31	903	P39 26
8W30	904	H18	1	2±14 SHIFT TO REGISTER	7W31	904	P40 2
8W30	905	H18	5	2+15 SHIFT TO REGISTER	7W31	905	P40 26
8W30	906	H18	25	2+16 SHIFT TO REGISTER	7W31	906	P41 2
8W30	907	H18	27	2+17 SHIFT TO REGISTER	7W31	907	P41 26
8W30	908				7W31	908	

2 0 W	0,0		-	MEMORY FROM WRITE DISTRIBUTOR	13W14	00	
9W03	90	H21 8	24	2+8 FAN OUT TO MEMORY	13W14	90	133 28
EOWE	91	H22	2	2+9 FAN OUT TO MEMORY	13W14	91	134 24
EOW	92	H22 :	19	2+10 FAN OUT TO MEMORY	13W14	92	134 7
EDW.	93	H22	24	2+11 FAN OUT TO MEMORY	13W14	93	135 28
WO3	94	H24	2	2+12 FAN OUT TO MEMORY	13W14	94	136 24
WO3	95	H24 :	19	2+13 FAN OUT TO MEMORY	13W14	95	136 7
WO3	96	H24	24	2+14 FAN OUT TO MEMORY	13W14	96	137 28
W03	97				13W14	97	
WO3	98				13W14	98	
W03	99				13W14	99	
W03	900	H19	2	2+0 FAN OUT TO MEMORY	13W14	900	128 24
WO3	901	H19 :	19	2+1 FAN OUT TO MEMORY	13W14	901	128 7
W03	902	H19	24	2+2 FAN OUT TO MEMORY	13W14	902	129 28
WO3	903	H20	2	2+3 FAN OUT TO MEMORY	13W14	903	130 24
WO3	904	H20 :	19	2+4 FAN OUT TO MEMORY	13W14	904	130 7
EOW.	905	H20	24	2+5 FAN DUT TO MEMORY	13W14	905	131 28
WO3	906	H21	2	2+6 FAN OUT TO MEMORY	13W14	906	132 24
WO3	907	H21 :	19	247 FAN OUT TO MEMORY	13W14	907	132 7
WD3	908				13W14	908	

9 W O 4	0.0	n		MEMORY FROM WRITE DISTRIBUTOR	14W14	00	
9W04	90	H27	24	2+23 FAN OUT TO MEMORY	14W14	90	111 13
9W04	91	H29	2	2+24 FAN OUT TO MEMORY	14W14	91	112 3
9W04	92	H29	19	2+25 FAN OUT TO MEMORY	14W14	92	113 18
9W04	93	H29	24	2+26 FAN OUT TO MEMORY	14W14	93	113 13
9W04	94	H30	2	2+27 FAN OUT TO MEMORY	14414	94	114 3
9W04	95	H30	19	2+28 FAN OUT TO MEMORY	14W14	95	115 18
9W04	96	H30	24	2+29 FAN OUT TO MEMORY	14W14	96	115 13
9W04	97		<del></del>	The state of the s	14W14	97	
9W04	98				14W14	98	
9W04	99			and the second s	14W14	99	
9W04	900	H25	2	2+15 FAN OUT TO MEMORY	14W14	900	106 3
9W04	901	H25	19	2+16 FAN OUT TO MEMORY	14W14	901	107 18
9W04	902	H25	24	2+17 FAN OUT TO MEMORY	14W14	902	107 13
9W04	903	H26	2	2+18 FAN OUT TO MEMORY	14W14	903	108 3
9W04	904	H26	19	2+19 FAN OUT TO MEMORY	14W14	904	109 18
9W04	905	H26	24	2+20 FAN OUT TO MEMORY	14014	905	109 13
_	906	H27		2+21 FAN OUT TO MEMORY	14W14	906	110 3
9W04	-		2 19	2+22 FAN OUT TO MEMORY	14W14	907	111 18
9W04 9W04	907 908	H27	19	EWEZ PAN UUI IU MEMUNI	14W14	908	117 10

9W05	0.0		MEMORY FROM WRITE DISTRIBUTOR	15W14	0.0	n dies hell and eller dats have been der der den son.
9W05	90	H34-24	2+38 FAN OUT TO MEMORY	15W14	90	133 13
PWD5	91	H35 2	2+39 FAN OUT TO MEMORY	15W14	91	134 3
9WD5	92	H35 19	2+40 FAN OUT TO MEMORY	15W14	92	135 18
9W05	93	H35 24	2+41 FAN OUT TO MEMORY	15W14	93	135 13
9W05	94	H36 2	2+42 FAN OUT TO MEMORY	15W14	94	136 3
9W05	95	H36 19	2+43 FAN OUT TO MEMORY	15W14	95	137 18
9W05	96	H36 24	2+44 FAN OUT TO MEMORY	15W14	96	137 13
PWD5	97			15W14	97	
9W05	98			15W14	98	
9 W 0 5	99			15W14	99	
9W05	900	H31 2	2+30 FAN OUT TO MEMORY	15W14	900	128 3
9W05	901	H31 19	2+31 FAN OUT TO MEMORY	15W14	901	129 18
9W05	902	H31 24	2#32 FAN OUT TO MEMORY	15W14	902	129 13
9W05	903	H32 2	2+33 FAN OUT TO MEMORY	15W14	903	130 3
9W05	904	H32 19	2+34 FAN OUT TO MEMORY	15W14	904	131 18
9W05	905	H32 24	2+35 FAN OUT TO MEMORY	15W14	905	131 13
9 W D 5	906	H34 2	2+36 FAN OUT TO MEMORY	15W14	906	132 3
9 W 0 5	907	H34 19	2+37 FAN OUT TO MEMORY	15W14	907	133 18
9W05	908			15W14	908	

9W06	00			MEMORY FROM WRITE DISTRIBUTOR	16W14	00	
9W06	. 90	H40	24	2+53 FAN OUT TO MEMORY	16W14	90	111 13
9W06	91	H41	2	2+54 FAN OUT TO MEMORY	16W14	91	112 3
9W06	92	H41	19	2+55 FAN OUT TO MEMORY	16W14	92	113 18
9W06	93	H41	24	2+56 FAN OUT TO MEMORY	16W14	93	113 13
9 W D 6	94	H42	2	2+57 FAN OUT TO MEMORY	16W14	94	114 3
9W06	95	H42	19	2+58 FAN OUT TO MEMORY	16W14	95	115 18
PWD6	96	H42	24	2+59 FAN OUT TO MEMORY	16W14	96	115 13
9 W O 6	97				16W14	97	
9W06	98				16W14	98	
9 W O 6	99				16W14	99	
9 W 0 6	900	H37	2	2+45 FÅN OUT TO MEMORY	16W14	900	106 3
PWD6	901	H37	19	2+46 FAN OUT TO MEMORY	16W14	901	107 18
9W06	902	H37	24	2+47 FAN OUT TO MEMORY	16W14	902	107 13
9 W O 6	903	H39	2	2+48 FAN OUT TO MEMORY	16W14	903	108 3
9 W 0 6	904	H39	19	2+49 FAN OUT TO MEMORY	16W14	904	109 18
9W06	905	H39	24	2+50 FAN OUT TO MEMORY	16W14	905	109 13
9W06	906	H40	2	2+51 FAN OUT TO MEMORY	16W14	906	110 3
9W06	907	H40	19	2+52 FAN OUT TO MEMORY	16W14	907	111 18
9W06	908			• • • • • • • • • • • • • • • • • • • •	16W14	908	

9W07.	00			MEMORY TO READ DISTRIBUTOR	-3W07	0.0		
9W07	90	H21	28	2+8	3W07	90	135	7
9W07	91	H22	1	2+9	3W07	91	136	7
9W07	92	H22	8	2+10	3W07	92	138	7
9W07	93	H22	28	2+11	3W07	93	139	7
9 W Q 7	94	H24	1	2+12	3W07	94	140	7
9W07	95	H24	8	2+13	3W07	95	141	7
9W07	96	H24	28	2+14	3W07	96	142	7
9W07	97				3W07	97		
9W07	98				3W07	98		
9w07	99	G39	28	ACCEPT	3w07	99	123	7
9W07	900	H19	1	2+0	3W07	900	125	7
9W07	901	H19	8	2+1	3W07	901	126	7
9W07	902	H19	28	2+2	3w07	902	127	7
9W07	903	H20	1	2+3	3w07	903	128	7
9W07	904	H20	A	2+4	3W07	904	129	7
9W 07	905	H20	28	2+5	3w07	905	132	7
9W07	906	H21	4	2+6	3w07	906	133	j
9W07	907	H21	8	2+7	3W07	907	134	Ť
9W07	908		•	m /	3407	908		•

MOB	0.0			MEMORY TO READ DISTRIBUTOR	4W12	0.0		
WOB	90	H27	28	2*23	4W12	90	111	7
80W	91	H29	1	2+24	4W12	91	112	7
WOB	92	H29	8	2+25	4W12	92	114	7
408	93	H29	28	2+26	4W12	93	115	7
W 0 8	94	H30	1	2+27	4W12	94	116	7
W 0 8	95	H30	8	2+28	4W12	95	117	7
80 W	96	H30	28	2+29	4W12	96	118	7
108	97			- Charles and Char	4W12	97	an Tallinger, .	
108	98				4W12	98		
108	99				4W12	99		
8 D W	900	H25	1	2+15	4W12	900	101	7
08	901	H25	8	2+16	4W12	901	102	Ź
108	902	H25	28	2+17	4W12	902	103	7
108	903	H26	1	2+18	4W12	903	104	
W 0 8	904	H26	8	2+19	4W12	904	105	7
N 0 8	905	H26	28	2+20	4W12	905	108	7
108	906	H27	1	2+21	4W12	906	109	7
408	907	H27	8	2+22	4W12	907	110	7
W 0 8	908	_	-		4W12	908		•

9W09	0.0			MEMORY TO READ DISTRIBUTOR	10W15	0.0		
9W09	90	H40	28	2+53	10W15	90	111	7
9W09	91	H41	1	2+54	10W15	91	112	7
9W09	92	H41	8	2+55	10W15	92	114	7
9W09	93	H41	28	2+56	10W15	93	115	7
9409	94	H42	1	2+57	10W15	94	116	7
9W09	95	H42	8	2+58	10W15	95	117	7
9W09	96	H42	28	2+59	10W15	96	118	7
9W09	97			The second secon	10W15	97		and death as the branchise
9W09	98				10W15	98		
9W09	99				10W15	99		
9W09	900	H37	1	2+45	10W15	900	101	7
9W09	901	H37	8	2+46	10W15	901	102	7
9W09	902	H37	28	2+47	10W15	902	103	7
9W09	903	H39	1	2+48	10W15	903	104	7
9W09	904	H39	8	2+49	10W15	904	105	7
9 W D 9	905	H39	28	2+50	10W15	905	108	7
9 W 0 9	906	H40	1	2+51	10W15	906	109	7
9W09	907	H40	8	2+52	10W15	907	110	7
9009	908			•	10W15	908		

9W10	0.0			READ DISTRIBUTOR TO CONTROL	5w04	00		
9W10	90	135	26	2+38 BUFFER TO CONTROL	5w04	90	842	27
9W10	91	136	26	2+39 BUFFER TO CONTROL	5W04	91	C41	4
9W10	92	138		2+40 BUFFER TO CONTROL	5W04	92	C41	6
9W10	93	139	26	2+41 BUFFER TO CONTROL	5W04	93	C41	7
9W10	94	140	26	2+42 BUFFER TO CONTROL	5W04	94	C41	22
9W10	95	141	26	2-43 BUFFER TO CONTROL	5W04	95	C41	25
9W10	96	142	26	2+44 BUFFER TO CONTROL	5W04	96	C41	27
9W10	97	*** ** . ** <b>***</b>			5W04	97	2.1.	
9W10	98				5W04	98		
9W10	99				5W04	99		
9W10	900	125	26	2+30 BUFFER TO CONTROL	5W04	900	B41	22
9W10	901	126	26	2+31 BUFFER TO CONTROL	5w04	901	B41	25
9W10	902	127	26	2+32 BUFFER TO CONTROL	5w04	902	B41	27
9W10	903	128	26	2+33 BUFFER TO CONTROL	5W04	903	B42	4
9W10	904	129	26	2+34 BUFFER TO CONTROL	5W04	904	B42	6
9W10	905	132	26	2+35 BUFFER TO CONTROL	5W04	905	B42	7
9W10	906	133	26	2+36 BUFFER TO CONTROL	5W04	906	B42	22
9W10	907	134	26	2+37 BUFFER TO CONTROL	5W04	907	B42	25
9W10	908		_	•	5W04	908		

9W11_	00			CENTRAL MEMORY TO PERIPHERAL	1406	00		
9W11	90	135	1	2+38 CENTRAL TO PERIPHERAL DATA	1W06	90	<b>G</b> 39	10
9W11	91	136	1	2+39 CENTRAL TO PERIPHERAL DATA	1406	91	839	21
9W11	92	138	1	2+40 CENTRAL TO PERIPHERAL DATA	1006	92	<b>G39</b>	24
9W11	93	139	1	2+41 CENTRAL TO PERIPHERAL DATA	1406	93	<b>G39</b>	26
9W11	94	140	1_	2+42 CENTRAL TO PERIPHERAL DATA	1W06	94	040	
9W11	95	141	ī	2+43 CENTRAL TO PERIPHERAL DATA	1406	95	G40	7
9W11	96	142	1	2+44 CENTRAL TO PERIPHERAL DATA	1006	96	G40	10
9W11	97	C14 :	14	MEMORY MARGIN	1W06	97	H29	18
9W11	98				1106	98		
9W11	99				1406	99		
9W11	900	125	1	2+30 CENTRAL TO PERIPHERAL DATA	1W06 9	0.0	<b>G38</b>	5
9W11	901	126	1	2+31 CENTRAL TO PERIPHERAL DATA	1W06 9	01	G 3 8	7
9W11	902	127	1	2+32 CENTRAL TO PERIPHERAL DATA	1W06 9	02	G38	10
9W11	903	128	1	2+33 CENTRAL TO PERIPHERAL DATA	1W06 9	03	G38	21
9W11	904	129	1	2+34 CENTRAL TO PERIPHERAL DATA	1W06 9	04	G38	24
9W11	905	132	1	2+35 CENTRAL TO PERIPHERAL DATA	1W06 9	05	G 3 8	26
9W11	906	133	1	2+36 CENTRAL TO PERIPHERAL DATA	1WD6 9	06	G39	5
9W11	907	134	1	2+37 CENTRAL TO PERIPHERAL DATA	1W06 9	07	639	7
9W11	908				1W06 9	08		

00	READ DISTRIBUTOR TO LOWER REGISTER	7W07	00		
90		7W07	90		
91	TO NOTICE AND ADMINISTRATION OF A CONTROL OF	7407	91		
92		7W07	92		
93		7W07	93		
94		7W07	94		
95		7W07	95		
96		7W07	96		
97		7W07	97		
98	Constitution to the constitution will be the second management of the constitution of	7W07	98		
99		7W07	99		
900	125 28 2+30 MEMORY TO REGISTER		900	C40	1
901	126 28 2+31 MEMORY TO REGISTER	7W07	901	C40	20
902	127 28 2+32 MEMORY TO REGISTER	7407	902	C41	
903	128 28 2+33 MEMORY TO REGISTER	7W07	903	C41	26
904	129 28 2+34 MEMORY TO REGISTER	7407	904	C42	
905	132 28 2+35 MEMORY TO REGISTER	7407	905	C42	2
906		7W07	906		
907		7W07	907		
908		7W07	908		

9W13	0.0			READ DISTRIBUTOR FROM MEMORY	3w02	0.0		
9W13	90	135	5	2+38	3w02	90	H34	2 8
9W13	91	136	5	2+39	3402	91	H35	-
9W13	92	138	5	2+40	3402	-		3
9W13	93	139	- 5	2+41	and the second s	92	H35	!
9W13	94	140	5	2+42	3W02	93	H35	28
9W13	95	141		2+43	2M05	94	H36	. :
9W13	96	142	5	2+44	3W02	95	H36	(
	97	1 76	∴?	ERGS 4.	3W02	96	_H36	28
9W13	• •				3W02	97		- e-e ,Seller 2
9W13	98				3W02	98		
9W13	99	_			3402	99		
9W13	900	125	5	2+30	3W02	900	H31	4
9W13	901	126	5	2+31	3002	901	H31	
9W13	902	127	5	2+32	3402	902		
9W13	903	128	5	2+33			H31	25
9W13	904	129	. 65	2+34	3W02	903	H32	1
9W13	905	132	5	2+35	2M05	904	H32	8
9W13	906	133	.5	2+36	3W02	905	H32	. 28
9W13	907		. 2	A SECTION AND A SECTION AND A SECTION AND ASSESSMENT OF THE SECTION AND ASSESSMENT OF THE SECTION ASSESSMENT OF THE SECTIO	3W02	906	<b>H34</b>	1
		134	9	2+37	3W02	907	H34	8
9W13	908	NAMES OF TAXABLE PARTY.			3W02	908		•

9W14	0.0			READ	DISTRIBUTOR FROM MEMORY	4W08	0.0		
9W14	90	135	3	2+38		4w08	90	H16	28
9W14	91	136	3	2+39	The second commencer is the second second second second in the second second second second second second second	4W08	91	H17	
9W14	92	138	3	2+40		4W08	92	H17	1
9W14	93	139	3	2+41	The second secon	4008	93	H17	28
9W14	94	140	3.	2+42		4W08	94	H18	<b>4</b> 0
9W14	95	141	3	2+43		4W08	95	H18	. 1
9W14	96	142	. 3	2+44		4W08	96	H18	28
9W14	97	G38	21	GO		4W08	97		
9W14	98	G38	20	WRIT		4W08	98	I21 I24	20
9W14	99	124	12	READ	PERIPHERAL	4W08	99	121	. :5 9
9W14	900	125	3	2+30		4W08	900		4
9W14	901	126	3	2+31		4008	901	H13	1
9W14	902	127	3	2+32		4W08	902	H13 H13	8
9W14	903	128	3	2+33	Commence of the Commence of th	4W08	903		28
9W14	904	I 29	3	2+34		4W08	904	H14	1
9W14	905	132	3	2+35		4W08	905	H14	8
9W14	906	133	3	2+36				H14	28
9W14	907	134	3	2+37		4W08	906	H16	1
9W14	908				e de mare	4W08 4W08	907 908	H16	8

9W15	0,0			READ DISTRIBUTOR FROM MEMORY	10009	00	
9W15	90	135	9	2+38	.10w09	90	H16 28
9W15	91	136	9	2+39	10009	91	H17 1
9W15	92	138	9	2+40	10009	92	H17 8
9W15	93	139	9	2+41	10009	93	H17 28
9W15	94	140	9	2+42	10009	94	H18 1
9W15	95	141	9	2+43	10009	95	H18 8
9W15	96	142	9	2+44	10009	96	H18 28
9W15	97				10009	97	
9W15	98				10009	98	
9W15	99				10009	99	
9W15	900	125	9	2+30	10009	900	H13 1
9W15	901	126	9	2.31	10009	901	H13 8
9W15	902	127	9	2+32	10W09	902	H13 28
9W15	903	128	9	2+33	10009	903	H14 1
9W15	904	129	9	2+34	10009	904	H14 8
9W15	905	132	9	2+35	10W09	905	H14 28
9W15	906	133	9	2+36	10009	906	H16 1
9W15	907	134	9	2+37	10009	907	H16 8
9W15	908		•	•	10009	908	

9W16	0.0		READ DISTRIBUTOR FROM ME	MORY 13W08	0.0		
9W16	90	135 11	2+38	13w08	90	H34	28
9W16	91	136 11	2+39	13408	91	H35	1
W16	92	138 11	2+40	13408	92	H35	8
W16	93	139 11	2+41	13w08	93	H35	28
W16	94	140 11	2+42	13W08	94	H36	4
W16	95	141 11	2+43	13008	95	H36	8
W16	96	142 11	2+44	13W08	96	H36	_
W16	97		annumber und der der der der der der der der der de	13408	97	,inhalf-uire.	
W16	98			13W08	98		
W16	99			13W08	99		
W16	900	125 11	2+30	13408	900	H31	4
W16	901	126 11	2+31	13008	901	H31	8
W16	902	127 11	2*32	13008	902	H31	28
W16	903	128 11	2+33	13W08	903	H32	
W16	904	129 11	2.34	13408	904	H32	Ā
W16	905	132 11	2+35	13006	905	H32	28
W16	906	133 11	2+36	13408	906	H34	4
W16	907	134 11	2+37	13008	907	H34	
W16	908	10, 11	==0/	13408	908	707	0

9W17	0.0	00 TOO MAY SEE THE AND AND THE EAST SEE THE	READ DISTRIBUTOR FROM MEMORY	14W08	00	all your little was him own thin and wanty has been
9W17	90	135 19	2+38	14W08	90	H16 28
9W17	91	136 19	2+39	14W08	91	H17 1
9W17	92	138 19	2+40	14008	92	H17 8
9W17	93	139 19	2+41	14008	93	H17 28
W17	94	140 19	2+42	14008	94	H18 1
W17	95	141 19	2+43	14408	95	H18 8
W17	96	142 19	2+44	14W08	96	H18 28
W17	97			14008	97	
W17	98			14408	98	
W17	99			14W08	99	
W17	900	125 19	2*30	14WD8	900	H13 1
W17	901	126 19	2+31	14W08	901	H13 8
W17	902	127 19	2+32	14408	902	H13 28
W17	903	128 19	2+33	14W08	903	H14 1
W17	904	129 19	2+34	14W08	904	H14 8
W17	905	132 19	2+35	14W08	905	H14 28
W17	906	133 19	2+36	14W08	906	H16 1
W17	907	134 19	2+37	14W08	907	H16 8
W17	908			14008	806	_

W18	0.0			READ DÍSTRIBUTOR FROM MEM	DRY 15W08	0.0		
W18	90	135	21	2+38	15W08	90	H34	28
W18	91	136	21	2+39	15W08	91	H35	1
W18	92	138	21	2+40	15W08	92	H35	Ā
W18	93	139	21	2+41	15W08	93	H35	28
W18	94	140	21	2+42	15W08	94	H36	1
W18	95	141	21	2+43	19W08	95	H36	8
W18	96	142	21	2+44	15W08	96	H36	28
W18	97	Vi i grana		The state of the s	19W08	97		
W18	98				15W08	98		
W18	99				15W08	99		
V18	900	125	21	2+30	15W08	900	H31	1
W18	901	126	21	2+31	15W08	901	H31	8
W18	902	127	21	2+32	15W08	902	H31	28
W18	903	128	21	2+33	15W08	903	H32	4
W18	904	129	21	2+34	15W08	904	H32	ā
W18	905	132	21	2*35	15W08	905		28
W18	906	133	21	2+36	15W08	906	H34	1
W18	907	134	21	2+37	15W08	907	H34	8
W18	908		_		15W08	908	.,•	•

9W19	00		READ DISTRIBUTOR FROM MEMORY	16W08	00		
9W19	90	135 23	2+38	16W08	90	H16	28
9W19	91	136 23	2+39	16W08	91	H17	1
9W19	92	138 23	2+40	16W08	92	H17	8
9W19	93	139 23	2+41	16W08	93	H17	28
9W19	94	140 23	2+42	16W08	94	H18	1
9W19	95	141 23	2+43	16408	95	H18	8
9W19	96	142 23	2+44	16008	96	H18	28
9W19	97			16W08	97		
9W19	98			16408	98		
9W19	99			16W08	99		
9W19	900	125 23	2+30	16008	900	H13	4
9W19	901	126 23	2+31	16W08	901	H13	8
9W19	902	127 23	2*32	16W08	902		2 Š
9W19	903	128 23	2*33	16W08	903	H14	1
9W19	904	129 23	2+34	16W08	904	H14	ā
9W19	905	132 23	2+35	16W08	905	H14	
9W19	906	133 23	2+36	16008	906	H16	
9W19	907	134 23	2+37	16W08	907	H16	8
9W19	908			16W08	908	,,	_

9W20	0.0		MEMORY ADDRESS FROM CONTROL	5W30	0.0	
9W20	90	G40 18	2+8 ADDRESS TO MEMORY	5W30	90	036 22
9W20	91	G41 11	2+9 ADDRESS TO MEMORY	5W30	91	037 11
9W20	92	G41 14	2+10 ADDRESS TO MEMORY	5W30	92	Q37 26
9W20	93	G41 17	2+11 ADDRESS TO MEMORY	5W30	93	Q37 22
9W20	94	G41 18	2+12 ADDRESS TO MEMORY	5W30	94	Q38 11
9W20	95	G42 11	2+13 ADDRESS TO MEMORY	5W30	95	Q38 26
9W20	96	G42 14	2+14 ADDRESS TO MEMORY	5W30	96	038 22
9W20	97	G42 17	2+15 ADDRESS TO MEMORY	5W30	97	039 11
9W20	98	G42 18	2+16 ADDRESS TO MEMORY	5W30	98	039 26
9W20	99		,	5W30	99	
9W20	900	G35 12	CLOCK	5W30	900	M32 8
9W20	901	G39 18	2+0 BANK SELECT	5W30	.901	034 11
9W20	902	G39 22	2+1 BANK SELECT	5W30	902	Q34 26
9W20	903	G38 9	2+2 CHASSIS 9 SELECT (010)	5W30	903	Q34 22
9W20	904	G38 5	2+3 CHASSIS 9 SELECT (010)	5W30	904	Q35 11
9W20	905	G38 14	2+4 CHASSIS 9 SELECT (010)	5W30	905	035 26
9W20	906	G40 11	2+5 ADDRESS TO MEMORY	5W30	906	Q35 22
9W20	907	G40 14	2+6 ADDRESS TO MEMORY	5W30	907	Q36 11
9W20	908	G40 17	2+7 ADDRESS TO MEMORY	5W30	908	Q36 26

 9W21	00	READ DISTRIBUTOR TO UPPER REGISTER	8W05	00	
9W21	90		8W05	90	
9W21	91		8W05	91	AND DESCRIPTION OF THE PARTY OF
 9W21	92		8W05	92	
9W21	93		8W05	93	
 9W21	94		AW05	94	
9W21	95		8405	95	
9W21	96		8W05	96	
 9w21	97		8w05	97	Carried and Control of the Control o
 9W21	98		8W05	98	
 9W21	99		8W05	99	
9W21	900	133 28 2+36 MEMORY TO REGISTER	8W05	900	A01 2
 9W21	901	134 28 2+37 MEMORY TO REGISTER	8W05	901	A01 26
9W21	902	135 28 2+38 MEMORY TO REGISTER	8W05	902	A02 2
 9W21	903	136 28	8W05	903	A02 26
9W21	904	138 28 2+40 MEMORY TO REGISTER	8W05	904	A03 2
9W21	905	139 28 2+41 MEMORY TO REGISTER	8W05	905	A03 26
9W21	906	140 28 2+42 MEMORY TO REGISTER	8W05	906	A04 2
9W21	907	141 28 2443 MEMORY TO REGISTER	8w05	907	A04 26
9W21	908	142 28 2+44 MEMORY TO REGISTER	8W05	908	A05 2

9W34	00			926	SYN	C	70	ZHA	NEL (INPUT)
9W34	90	J35	7	2+0	INP	UT	DA	ÅΑ	
9W34	91	J35	5	2+1	INF	UT	DA	A	e Transport i nour Angelet de Marier de Marier de la restriction d
9W34	92	J35	3	2 * 2	INP		DA		
9W34	93	J35	28	2.3	INP	-	DA	A	
9W34	94	J35	26	2+4	INP	ŭΫ	DA	ΓA	
9W34	95	J35	24	2+5	INP	HT	DA	A	
9W34	96	J37	7	2+6	INP		DA	r A	
9W34	97	J37	5	2+7	INP	-	DA		and a distribution of the contrastion of the state of the state of the distribution of the state
9W34	98	J37	3	2+8	INP		DA		
9W34	99	J37	28	2+9	INP	u T	DA		
9W34	900	J37	26	2+1		PUT		TA	•
9W34	901	J37	24	2+11			D		The second secon
9W34	902	J33	28	ACT	• -		_		
9W34	903	J33	3	INAC	-	E			AND AND CONTRACTOR OF THE PROPERTY OF THE PROP
9W34	904	J33	ž	FULL		_			
9W34	905	J33	5	EMP	V				en en en en en en en en en en en en en e
9W34	906	J31	13		,				
9W34	907	J31	3	CLOC	K (	1 0 5	. ( 2		TO THE RESIDENCE OF THE PERSON
9W34	908		•	- 5	,		•		

9W35	00			626 SYNC TO CHANNEL (OUTPUT)
9w35	90	J40	3	2+0 OUTPUT DÄTA
9W35	91	J40	13	2+1 OUTPUT DATA
9W35	92	J40	16	2+2 OUTPUT DATA
9W35	93	J40	26	2+3 OUTPUT DATA
9W35	94	J41	3	2+4 OUTPUT DATA
9W35	95	J41	13	2+5 OUTPUT DATA
9W35	96	J41	16	2+6 OUTPUT DATA
9W35	97	J41	26	2+7 OUTPUT DATA
9W35	98	J42	3	2+8 OUTPUT DATA
9W35	99	J42	13	2+9 OUTPUT DATA
9W35	900	J42	16	2+10 OUTPUT DATA
9W35	901	J42	26	2+11 OUTPUT DATA
9W35	902	J38	13	ACTIVE
9W35	903	J38	16	INACTIVE
9W35	904	J38	26	FULL
9W35	905	J39	3	ENPTY
9W35	906	J38	3	FUNCTION
9W35	907	J39	13	MASTER CLEAR
9W35	908	-0.		70. E

9W36	0 0			626 SYNC PASSON (INPUT)
9W36	90	J35	23	2+0 INPUT DATA
9W36	91	J35	22	2+1 INPUT DATA
9W36	92	J35	21	2+2 INPUT DATA
9W36	93	J35	10	2+3 INPUT DATA
9W36	94	J35	9	2+4 INPUT DATA
9W36	95	J35	8	2.5 INPUT DATA
9W36	96	J37	23	2+6 INPUT DATA
9W36	97	J37	22	2+7 INPUT DATA
9W36	98	J37	21	2+8 INPUT DATA
9w36	99	J37	11	2+9 INPUT DATA
9W36	900	J37	9	2+10 INPUT DATA
9W36	901	J37	8	2+11 INPUT DATA
9w36	902	J33	10	ACTIVE
9W36	903	J33	21	INACTIVE
9W36	984	J33	23	FULL
9W36	905	J33	22	EMPTY
				EMP I Y
9W36	906	J31	15	range was the contract of the
9W36	907	J31	1	CLOCK (1 M SEC)
9W36	908			$\cdot$

9W37	0.0		maga arab salah kada salah salah s	626 SYNC PASSON (OUTPUT)
9 W 3 7	90	J40	1	2+0 OUTPUT DATA
9W37	91	J40	15	2+1 OUTPUT DATA
9W37	92	J40	14	2+2 OUTPUT DATA
9W37	93	J40	28	2+3 OUTPUT DATA
9W37	94	J41	1	2+4 OUTPUT DÂTA
9W37	95	J41	15	2+5 OUTPUT DATA
9W37	96	J41	14	2+6 OUTPUT DATA
9W37	97	J41	28	2+7 OUTPUT DATA
9W37	98	J42	1	2+8 OUTPUT DATA
9W37	99	J42	15	2+9 OUTPUT DATA
9W37	900	J42	14	2+10 OUTPUT BATA
9W37	901	J42	28	2+11 OUTPUT DATA
9W37	902	J38	15	ACTIVE
9W37	903	J38	14	INACTIVE
9W37	904	J38	28	Pull
9W37	905	J39	1	EMPTY
9W37	906	J38	1	FUNCTION
9W37	907	J39	15	MASTER CLEAR
9W37	908			

9W39	00			MEMORY TO READ DISTRIBUTOR	9W39	0.0		
9W39	90	H34	28	2+38	9839	90	135	7
9W39	91	H35	1	2+39	9W39	91	136	7
9W39	92	H35	8	2+40	9839	92	138	7
9W39	93	H35	28	2+41	9W39	93	139	7
9EW9	94	H36	1	2+42	9W39	94	140	7
9W39	95	H36	A	2+43	9W39	95	141	7
W39	96	H36	28	2+44	9839	96	142	7
W39	97			The state of the s	9W39	97		n ree land
W39	98				9W39	98		
W39	99				9W39	99		
W39	900	H31	4	2+30	9439	900	125	7
W39	901	H31	8	2+31	9W39	901	126	7
W39	902	H31	28	2+32	9W39	902	127	7
W39	903	H32	1	2 + 3 3	9839	903	ĪŽB	7
W39	904	H32	Ā	2+34	9 4 3 9	904	129	7
W39	905	H32	28	2+35	9W39	905	132	·
W39	906	H34	-1	2+36	9839	906	133	7
W39	907	H34	<del></del>	2+37	9W39	907	134	ý.
W39	908				9839	908		,

9W39	0 0			READ	DÍSTRIBÚTOR FROM MEMORY	9W39	0.0		
9W39	90	135	7	2+38		9W39	90	H34	28
9W39	91	136	7	2+39	1	9W39	91	H35	1
9W39	92	138	7	2+40		9W39	92	H35	8
9W39	93	139	7	2+41		9839	93	H35	28
9W39	94	140	7	2+42		9W39	94	H36	1
9W39	95	141	7	2+43		9439	95	H36	8
9W39	96	142	7	2+44		9439	96	H36	28
9W39	97				TO THE PERSON OF	9W39	97		
9W39	98					9W39	98		
9W39	99					9EW9	99		
9W39	900	125	7	2+30		9439	900	H31	1
9W39	901	126	7	2+31		9839	901	H31	ã
9W39	902	127	7	2+32		9439	902	H31	28
9W39	903	128	7	2+33	The second control of the second control of	9W39	903	H32	1
9W39	904	129	7	2+34	,	9W39	904	H32	Ā
9W39	905	132	7	2+35	The state of the s	9839	905	H32	28
9W39	906	133	7	2*36		9W39	906	H34	1
9W39	907	134	7	2+37	the state of the s	9W39	907	H34	ā
9W39	908					9W39	908		•

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10W03	00	***	MEMORY FROM WRITE DISTRIBUTOR	13W15	0.0	
10W03	90	H03-24	2+8 FAN OUT TO MEMORY	13W15	90	133 13
10W03	91	H04 2	2+9 FAN OUT TO MEMORY	13W15	91	134 3
10W03	92	H04 19	2+10 FAN OUT TO MEMORY	13W15	92	135 18
10W03	93	H04 24	2+11 FAN OUT TO MEMORY	13W15	93	135 13
10W03	94	H06 2	2+12 FAN OUT TO MEMORY	13W15	94	136 3
10W03	95	H06 19	2+13 FAN OUT TO MEMORY	13W15	95	137 18
10W03	96	H06 24	2+14 FAN OUT TO MEMORY	13W15	96	137 13
10W03	97	•		13W15	97	
10W03	98			13W15	98	
10W03	99			13W15	99	
10W03	900	H01 2	2+0 FAN OUT TO MEMORY	13W15	900	128 3
10W03	901	H01 19	2+1 FAN OUT TO MEMORY	13W15	901	129 18
10W03	902	H01 24	2+2 FAN OUT TO MEMORY	13W15	902	129 13
10W03	903	H02 2	2+3 FAN OUT TO MEMORY	13W15	903	130 3
10W03	904	H02 19	2+4 FAN OUT TO MEMORY	13W15	904	131 18
10W03	905	H02 24	2+5 FAN OUT TO MEMORY	13W15	905	131 13
10W03	906	H03 2	2+6 FAN OUT TO MEMORY	13W15	906	132 3
10W03	907	H03 19	2+7 FAN OUT TO MEMORY	13W15	907	133 18
10W03	908			13W15	908	

10W04	0.0			MEMO	RY F	ROM	WRI	TE DISTRIBUTOR	14W15	00		
10W04	90	H09	24	2+23	FAN	OUT	TO	MEMORY	14W15	90	111	11
10W04	91	H11	2	2+24	FAN	OUT	TO	MEMORY	14W15	91	112	5
10W04	92	H11	19	2+25	FAN		TO	MEMORY	14W15	92	113	
10W04	93	H11	24	2+26	FAN	OUT	TO	MEMORY	14W15	93	113	
10W04	94	H12	2	2+27	FAN	OUT	TO	MEMORY	14W15	94	114	
10W04	95	H12	19	2+28	FAN	OUT	TO	MEMORY	14W15	95	115	
10W04	96	H12	24	2+29	FAN	OUT	TO	MEMORY	14W15	96		11
10W04	97			Taranan antru tur				The committee designation of the contract of t	14W15	97		
10W04	98								14W15	98		
10W04	99								14W15	99		
10W04	900	H07	2	2+15	FAN	OUT	TO	MEMORY	14W15	900	106	- 5
10W04	901	H07	19	2+16		OUT	TO	MEMORY	14W15	901	107	20
10W04	902	H07	24	2+17	FAN	OUT	TO	MEMORY	14W15	902	107	11
10W04	903	HOB	2	2+18		OUT	TO	MEMORY	14W15	903	108	5
10W04	904	80H	19	2+19	FAN	OUT	TO	MEMORY	14W15	904	109	20
10W04	905	HO8	24	2+20	FAN	OUT	TO	MEMORY	14W15	905	109	11
10W04	906	H09	2	2+21	FAN	OUT	TO	MEMORY	14W15	906	110	5
10W04	907	H09	19	2+22			TO	MEMORY	14W15	907	111	20
10W04	908					- '			14W15	908		- 0

10W05	00		MEMORY FROM WRITE DISTRIBUTOR	15W15	0.0	a title tille population and alle soon over the con-
10W05	90	H16 24	2+38 FAN OUT TO MEMORY	15W15	90	133 11
10W05	91	H17 2	2+39 FAN OUT TO MEMORY	15W15	91	134 5
LOW05	92	H17 19	2+40 FAN OUT TO MEMORY	15W15	92	135 20
0W05	93	H17 24	2+41 FAN OUT TO MEMORY	15W15	93	135 11
0W05	94	H18 2	2+42 FAN OUT TO MEMORY	15W15	94	136 5
0W05	95	H18 19	2+43 FAN OUT TO MEMORY	15W15	95	137 20
0W05	96	H18 24	2+44 FAN OUT TO MEMORY	15W15	96	137 11
0W05	97			15W15	97	
0W05	98			15W15	98	
0W05	99			15W15	99	
0W05	900	H13 2	2+30 FAN OUT TO MEMORY	15W15	900	128 5
0W05	901	H13 19	2+31 FAN OUT TO MEMORY	15W15	901	129 20
0W05	902	H13 24	2+32 FAN OUT TO MEMORY	15W15	902	129 11
0W05	903	H14 2	2+33 FAN OUT TO MEMORY		903	130 5
0W05	904	H14 19	2+34 FAN OUT TO MEMORY	15W15	904	131 20
0W05	905	H14 24	2+35 FAN OUT TO MEMORY		905	131 11
0W05	906	H16 2	2#36 FAN OUT TO MEMORY	15W15	906	132 5
0W05	907	H16 19	2+37 FAN OUT TO MEMORY	15W15	907	133 20
LOW05	908			15W15	908	

LOW06	0.0			MEMO	Y F	ROM	WRI'	TE DISTRIBUTOR	16W15	0.0		
LOW06	90	H22	24	2+53	FAN	OUT	TO	MEMORY	16W15	90	111	11
LOWOS	91	H23	2	2+54	FAN	OUT	TO	MEMORY	16W15	91	112	5
. OW06	92	H23	19	2+55	FAN	OUT	TO	MEMORY	16W15	92	113	20
0W06	93	H23	24	2+56	FAN	OUT	TO	MEMORY	16W15	93	113	11
0 W 0 6	94	H24	. 2	2+57	FAN	OUT	TO	MEMORY	16W15	94	114	5
0W06	95	H24	19	2+58	FAN	OUT	TO	MEMORY	16W15	95	115	20
0W06	96	H24	24	2+59	FAN	OUT	TO	MEMORY	16W15	96	115	11
0 W 0 6	97								16W15	97		
0W06	98								16W15	98		
0W06	99								16W15	99		
0W06	900	H19	2	2+45	FAN	OUT	TO	MEMORY	16W15	900	106	5
0 W 0 6	901	H19	19	2+46	FAN	OUT	TO	MEMORY	16W15	901	107	20
0 W 0 6	902	H19	24	2+47	FAN	OUT	TO	MEMORY	16W15	902	107	11
0W06	903	H21	2	2+48	FAN	OUT	10	MEMORY	16W15	903	108	5
0 W 0 6	904	H21	19	2+49	FAN	OUT	TO	MEMORY	16W15	904	109	20
0 W 0 6	905	H21	24	2+50	FAN	OUT	TO	MEMORY	16W15	905	109	11
0 W 0 6	906	H22	2	2+51	FAN	OUT	TO	MEMORY	16W15	906	110	5
0W06	907	H22	19	2+52	FAN	OUT	TO	MEMORY	16W15	907	111	20
0W06	908								16W15	908		

0W07	0,0			MEMORY TO READ DISTRIBUTOR	3w08	0.0		
0W07	90	H03	28	2+8	3w08	90	135	9
0W07	91	H04	1	2+9	3408	91	136	9
0W07	92	H04	8	2+10	3W08	92	138	9
0W07	93	H04	28	2+11	3W08	93	139	9
0W07	94	H06	. 1	2+12	3WD8	94	140	9
0W07	95	H06	8	2+13	3W08	95	141	9
0W07	96	H06	28	2+14	3W08	96	142	9
0W07	97				3W08	97		
0 W 0 7	98				3W08	98		
0W07	99				3W08	99		
0 W 0 7	900	H01	1	2+0	3W08	900	125	9
OWO7	901	H01	8	2+1	3W08	901	126	9
0W07	902	H01	28	2+2	3W08	902	127	9
0W07	903	H02	1	2+3	3408	903	128	9
0W07	904	H02	8	2 * 4	3W08	904	129	9
OWO7	905	H02	28	2+5	3 W 0 8	905	132	9
0W07	906	H03	1	2+6	3W08	906	133	9
0W07	907	H03	8	2+7	3W08	907	134	ğ
0W07	908	_	_		3W08	908		

10W08	0.0			MEMORY	TO REAL	D DISTRIBUTOR		4W13	0.0			
10W08	90	H09	28	2+23				4W13	90	111	9	
10W08	91	H11	1	2+24			•	4W13	91	112	9	
10W08	92	H11	8	2+25				4W13	92	114	9	
10W08	93	H11	28	2+26				4W13	93	115	9	
10W08	94	H12		2+27				4W13	94	116	9	
10008	95	H12	8	2+28				4W13	95	117	9	
10008	96	H12	-	2+29				4W13	96	118	9	
10W08	97	., + =	<u>e.</u> .o	<del></del>			1.00	4W13	97	i To	7	
10008	98								98			
10w08	99					***		4W13	99			
	900	H07	4	2+15				4W13			_	
10W08		_	1					4W13	900	101	9	
10W08	901	H07	8	2+16				4W13	901	102	9	
10W08	902	H07	28	2+17				4W13	902	103	9	
10W08	903	H08	1	2*18				4W13	903	104	9	
10W08	904	H 0 8	8	2+19				4W13	904	105	9	
10W08	905	H08	28	2+20				4W13	905	108	9	
10W08	906	H09	-7	2+21					906	-		
	-	-	1					4W13		109	9	
10W08	907	H09	8	2 + 22				4W13	907	110	9	
10W08	908							4W13	908			

10009	0.0			MEMORY TO READ DISTRIBUTOR	9W15	00		
10W09	90	H16	28	2+38	9W15	90	135	.9
10W09	91	H17	1	2+39	9W15	91	136	9
10W09	92	H17	8	2+40	9W15	92	138	9
10W09	93	H17	28	2+41	9W15	93	139	9
10409	94	H18	1	2+42	9W15	94	140	9
10W09	95	H18	8	2+43	9W15	95	141	9
10W09	96	H18	28	2+44	9W15	96	142	9
10W09	97				9W15	97		
10W09	98				9W15	98		
10W09	99				9W15	99		
10009	900	H13	1	- 2 <b>-</b> 3 ĝ	9W15	900	125	9
10W09	901	H13	8	2+31	9W15	901	126	9
10009	902	H13	28	2+32	9W15	902	127	9
10W09	903	H14	1	2*33	9W15	903	128	9
10009	904	H14	8	2+34	9W15	904	129	9
10W09	905	H14	28	2+35	9W15	905	132	9
10009	906	H16	1	2*36	9W15	906	133	9
10009	907	H16	8	2+37	9W15	907	134	9
10009	908	-			9W15	908		-

10W10	0.0			READ DISTRIBUTOR TO CONTROL	5W03	0.0		
10W10	90	111	26	2+53	5 W 0 3	90	A42	7
10W10	91	112	26	2+54	5w03	91	A42	22
10W10	92	114	26	2+55	5w03	92	A42	25
10W10	93	115	26	2*56	5w03	93	A42	27
10W10	94	116	26	2+57	5w03	94	B41	4
10W10	95	117	26	2+58	5w03	95	B41	6
10W10	96	118	26	2+59	5W03	96	B41	7
LOW10	97			- Magain and Andrew Company of the	5w03	97	THE PERSON NAMED IN COLUMN	no maio
10W10	98				5w03	98		
LOW10	99				5w03	99		
LOW10	900	101	26	2+45	5w03	900	A41	. 4
10W10	901	102	26	2+46	5w03	901	A41	6
10W10	902	103	26	2+47	5W03	902	A41	7
10W10	903	104	26	2+48	5w03	903	A41	22
10W10	904	Ĭ 0 5	26	2+49	5 W 0 3	904	A41	25
10W10	905	108	26	2+50	5w03	905	A41	27
10W10	906	109	26	2+51	5w03	906	A42	4
10W10	907	110	26	2+52	5w03	907	A42	6
10W10	908				5w03	908		_

10W11	00			GENTRAL MEMORY TO PERIPHERAL	1W07	00	
10W11	90	111	1	2453 CENTRAL TO PERIPHERAL DATA	1W07	90	041 26
10W11	91	112	1	2+54 CENTRAL TO PERIPHERAL DATA	1W07	91	042 5
10W11	92	114		2+55 CENTRAL TO PERIPHERAL DATA	1W07	92	042 7
10W11	93	115	1	2+56 CENTRAL TO PERIPHERAL DATA	1407	93	042 10
LOW11	94	116		2457 CENTRAL TO PERIPHERAL DATA	1W07	24	842 21
10W11	95	117	1	2+58 CENTRAL TO PERIPHERAL DATA	1807	95	042 24
LOW11	96	118	1	2.59 CENTRAL TO PERIPHERAL DATA	1807	96	G42 26
LOW11	97	C14	14	MEMORY MARGÍN	1W07	97	H29 16
LOW11	98				1W07	98	
.OW11	99				1407	99	
.OW11	900	101	1	2+45 CENTRAL TO PERIPHERAL DATA	1W07	900	G40 21
OW11	901	102	1	2+46 CENTRAL TO PERIPHERAL DATA	1807	901	G40 24
0W11	902	103	1	2+47 CENTRAL TO PERIPHERAL DATA	.1W07	902	G40 26
OW11	903	104	1	2+48 CENTRAL TO PERIPHERAL DATA		903	G41 5
LOW11	904	105	1	2+49 CENTRAL TO PERIPHERAL DATA	1W07	904	G41 7
0W11	905	108	1	2+50 CENTRAL TO PERIPHERAL DATA	1W07	905	G41 10
0W11	906		1	2+51 CENTRAL TO PERIPHERAL DATA		906	G41 21
0W11	907	110	1	2+32 CENTRAL TO PERIPHERAL DATA		907	G41 24
OW11	908		-			908	

0W12	00			READ	DISTRI	BUTOR	TO UPPER	REGISTER	8W06	00		
.0W12	90	101	28	2+45	MEMORY	REGI	STER		8W06	90	A05	26
0W12	91	102	28	2+46	MEMORY	REGI	STER	TO THE PERSON NAMED IN COLUMN TWO PARTY AND ADMINISTRATION AND A STREET OF THE PERSON NAMED IN CO.	8406	91	A06	2
0W12	92	103	28	2+47	MEMORY	REGI	STER		8W06	92	A06	26
0W12	93	104	28	2+48	MEMORY	REGI	STER		8W06	93	A07	2
0W12	94	105	28	2+49	MEMORY	REGI	STER		8 W 0 6	94	A07	26
0W12	95	108	28	2+50	MEMORY	REGI	STER		8W06	95	A08	2
0W12	96	109	28	2+51	MEMORY	REGI	STER		8406	96	A 0 8	26
0W12	97	110	28	2+52	MEMORY	REGI	STER	tick, and a four times of the optimization is an particular temperature property	8W06	97	B01	2
0W12	98	111	28	2+53	MEMORY	REGI	STER		8 W D 6	98	B01	26
0W12	99	112	28	2+54	MEMORY	REGI	STER		8W06	99	B02	2
0W12	900	114	28	2+55	MEMORY	REGIS	STER		8W06	900	B02	26
0W12	901	115	28	2+56	MEMORY	REGI	STER		8006	901	803	2
0 W1 2	902	116	28	2+57	MEMORY	REGI	STER		8W06	902	B03	26
0W12	903	117	28	2+58	MEMORY	REGI	STER	TO SECURE OF THE PERSON OF THE	8406	903	B04	2
0W12	964	118	28	2+59	MEMORY	REGI	STER		8006	904	B04	26
0W12	905	. # # I	₹ ₹			:: <del></del> ::			8006	905	·	
0W12	906								8006	906		
0W12	907		no. 10 to 10 to 10			***************************************			8006	907		
0W12	908								8 w 0 6	908		

0W13	0.0			READ DISTRIBUTOR FROM MEMORY	3W04	0.0	entre <mark>nan min hat min hat hat hat hat hat hat hat ha</mark> hat hat he had hat hat hat hat hat hat hat hat hat hat
0W13	90	111	15	2+53	3W04	90	H40 28
0W13	91	112	5	2+54	3W04	91	H41 1
0W13	92	114	- 5	2+55	3W04	92	H41 8
0W13	93	115	5	2+56	3W04	93	H41:28
0W13	94	116	5_	2+57	3w04	94	H42 1
0W13	95	117	5	2+58	3W04	95	H42 8
0W13	96	118	5	2+59	3W04	96	H42 28
0W13	97	G 05	28	ACCEPT FROM CHAS, 10	3W04	97	123 9
0W13	98				3W04	98	
0W13	99				3W04	99	
0W13	900	101	5	2+45	3w04	900	H37 1
0W13	901	102	5	2+46	3W04	901	H37 8
0W13	902	103	- 5	2+47	3W04	902	H37 28
0W13	903	104	5	2+48	3W04	903	H39 1
0W13	904	105	- 5	·2+49·	3W04	904	H39 8
0W13	905	108	5	2+50	3W04	905	H39 28
0W13	906	109	5	2+51	3W04	906	H40 1
0W13	907	110	5	2+52	3W04	907	H40 8
0W13	908				3W04	908	

10W14	00			READ DISTRIBUTOR FROM MEMORY	4W11	0.0	po ggga para apar kipa nain kang njan dan aran nain na	÷
10W14	90	111	3	2+53	4W11	90	H22 28	
10W14	91	112	3	2+54	4W11	91	H23 1	
10W14	92	114	3	2+55	4W11	92	H23 8	
10W14	93	115	3	2+56	4W11	93	H23 28	
10W14	94	116	3	2+57	4W11	94	H24 1	Carrier and the control of the contr
10W14	95	117	3	2 + 58	4W11	95	H24 8	
10W14	96	118	3	2+59	4W11	96	H24 28	
10W14	97	G 0 4	21	GO	4W11	97	121 18	. ,
10W14	98	G 0 4	20	WRITE	4W11	98	124 3	***
10W14	99	119	12	READ PERIPHERAL	4W11	99	121 7	
10W14	900	101	3	2+45	4W11	900	H19 1	
10W14	901	102	3	2+46	4W11	901	H19 8	
10W14	902	103	- 3	2+47	4W11	902	H19 28	
10W14	903	104	3	2+48	4W11	903	H21 1	Secretary of the contribution of all
10W14	904	105	3	2+49	4W11	904	H21 8	
10W14	905	108	3	2+50	4W11	905	H21 28	
10W14	906	109	3	2+51	4W11	906	H22 1	
10W14	907	110	3	2+52	4W11	907	H22 8	
10W14	908	_			4W11	908		

0W15	0.0			READ DISTRIBUTOR FROM M	EMORY 9W09	0.0		
0W15	90	111	7	2+53	9409	90	H40	28
0W15	91	112	7	2+54	9W09	91	H41	1
0W15	92	114	7_	2+55	9 W Q 9	92	H41	8
0W15	93	115	7	2+56	9W09	93	H41	28
W15	94	116	<b>. 7</b>	2+57	9 W D 9	94	H42	1
0W15	95	117	7	2+58	9W09	95	H42	8
0W15	96	118	7	2+59	9 4 0 9	96	H42	28
0W15	97				9W09	97		
W15	98					98		
W15	99				9 4 0 9	99		
W15	900	101	7	2+45	PWOP	900	H37	1
0W15	901	102	7	2+46	9409	901	H37	8
0W15	902	103	7	2+47	9₩09	902	H37	28
0W15	903	104	7	2+48	9 4 0 9	903	H39	1
0W15	904	105	7	2+49	9W09	904	H39	8
0W15	905	108	7	2+50	9W09	905	H39	28
0W15	906	109	7	2+51	9W09	906	H40	1
0W13	907	110	7	2+52	9 4 9 9	907	H40	8
0W15	908				9409	908		

0W16	0.0			READ DISTRIBUTOR FROM MEM	DRY 13W09	00		
QW16	90	111	11	2+53	13W09	90	H40	28
0W16	91	112	11	2+54	13w09	91	H41	4
0W16	92	114	11	2+55	13W09	92	H41	ā
0W16	93	115	11	2+56	13009	93	H41	28
0W16	94	116	11	2+57	13409	94	H42	
W16	95	117	11	2+58	13w09	95	H42	<u>.</u>
W16	96	118	11	2+59	13009	96	H42	28
W16	97			and the form of the second second second second second second second second second second second second second	13009	97		= 2
W16	98				13409	98		
W16	99			TO THE STORY OF THE TOTAL STORY OF THE STORY	13009	99		
W16	900	101	11	2+45	13009	900	H37	4
W16	901	102	11	2+46	13009	901	H37	1
W16	902	103		2+47	13009	902	H37	28
W16	903	104	11	2.48	the second control of the second control of	903	H39	_ E 9
		105			13009			l
W16	904		11	2 * 4 9	12M09	964	H39	. 0
1W16	905	108	11	2+50	13409	905	H39	28
)W16	906	109	11	2+51	13009	906	H40	1
)W16	907	I10	11	2*52	13409	907	H40	8
0 W 1 6	908				13W09	908		

and the second control of the second control

0.0		READ DISTRIBUTOR FROM MEMORY	14009	0.0	Miles with which was made with their state over the same of the sa	en em em las es men mas qual men las e
90	111 19	2+53	14409	90	H22 28	
91	112 19	2+54	14W09	91	H23 1	The E. P. Let See Section Co., proof to all classified the colonial fluid and the colonial fluid and the colonial fluid and the colonial fluid and the colonial fluid and the colonial fluid and the colonial fluid and the colonial fluid
	114 19	2+55				the same paties after page of the same same size of the same state.
93	115 19	2+56	\$4W09			
94	116 19	2 + 5 7	14009			a de como todo pero tercoran esta esta esta el seguir
95	117 19	2+58	14W09		H24 8	
96	118 19	2+59	14009		H24 28	Market May and Additional Conference of the State of
97			14W09			
98			14W09		ng <del>tingg ngan paga 1889 alam pagg niga</del> n <del>ana</del> anga ngan nana nana pag nana nana pag-	THE PARTY NAME AND PARTY AND A TAXABLE PARTY.
99			14W09			
900	101 19	2+45	14W09	900	H19 1	
901	102 19	2+46	14W09	901	H19 8	
902	103 19	2+47	14W09	902	H19 28	
903	104-19	2+48	1409	903	H21 1	
904	105 19	2+49	14W09	904	H21 8	
905	108 19	2+50	14009	905	H21 28	
906	109 19	2+51	14W09	906	H22 1	
907	110 19	2+52	14009	907	H22 8	
908			14W09	908		
9999	91 92 93 94 95 96 97 98 99 00 001 002 003 004 005 006	91 112 19 92 114 19 93 115 19 94 116 19 95 117 19 96 118 19 97 98 99 900 101 19 901 102 19 902 103 19 903 104 19 904 105 19 905 108 19 906 109 19 907 110 19	90	90	90	90

0W18	00			READ DISTRIBUTOR FROM MEMORY	15W09	0.0	9 44 44 70 W W W	AND FOR AND SHEE PROPERTY AND THE STORY AND AND AND AND AND A STORY AND A
0W18	90	111	21	2+53	15W09	90	H40	28
0W18	91	112	21	2+54	15W09	91	H41	1
0W18	92	114	21	2+55	15W09	92	H41	8
0W18	93	115	21	2+56	15W09	93	H41	28
0W18	94	116	21	2+57	15W19	94	H42	1_
0W18	95	117	21	2*58	15W09	95	H42	8
0W18	96	118	21	2+59	15W09	96	H42	28
0W18	97				15W09	97		The second secon
0W18	98				15W09	98		
0W18	99				15W09	99		
0W18	900	101	21	2+45	15W09	900	H37	1
0W18	901	102	21	2+46	15W09	901	H37	8
0W18	902	103	21	2+47	15W09	902	H37	28
0W18	903	104	21	2+48	15W09	903	H39	1
0W18	904	105	21	2+49	15W09	904	H39	8
0W18	905	108	21	2+50	15W09	905	H39	28
0W18	906	109	21	2+51	15W09	906	H40	.1
0W18	907	110	21	2+52	15W09	907	H40	8
0W18	908		_	-	15W09	908		-

10W19	00		READ DISTRIBUTOR FROM MEMORY	16009	0.0		
10W19	90	111 23	2+53	16W09	90	H22	28
10W19	91	112 23	2+54	16009	91	H23	1
10W19	92	114 23	2+55	16W09	92	H23	8
10W19	93	115 23	2+56	16409	93	H23	28
10W19	94	116 23	2+57	16009	94	H24	1
10W19	95	117 23	2+58	16W09	95	H24	8
10W19	96	118 23	2+59	16009	96	H24	28
10W19	97			16409	97		
10W19	98			16W09	98		
10W19	99			16W09	99		
10W19	900	101 23	2+45	16W09	900	H19	1
10W19	901	102 23	2+46	16W09	901	H19	8
10W19	902	103 23	2+47	16W09	902	H19	28
10W19	903	104 23	2+48	16009	903	H21	1
10W19	904	105 23	2+49	16W09	904	H21	8
10W19	905	108 23	2+50	16W09	905	H21	28
10W19	906	109 23	2+51	16W09	906	H22	1
10W19	907	110 23	2+52	16W09	907	H22	8
10W19	908		- · - ·	16009	908		•

10W20	00		MEMORY ADDRESS FROM CONTROL	5W31	0.0	
10W20	90	G01 18	2+8 ADDRESS TO MEMORY	5W31	90	Q36 <b>24</b>
10W20	91	G02 11	2+9 ADDRESS TO MEMORY	5w31	91	Q37 13
10W20	92	G02 14	2+10 ADDRESS TO MEMORY	5W31	92	037 28
10W20	93	G02 17	2+11 ADDRESS TO MEMORY	5W31	93	Q37 24
10W20	94	G02 18	2+12 ADDRESS TO MEMORY	5W31	94	Q38 13
10W20	95	G03 11	2+13 ADDRESS TO MEMORY	5W31	95	Q38 28
10W20	96	G03 14	2+14 ADDRESS TO MEMORY	5W31	96	Q38 24
10W20	97	G03 17	2+15 ADDRESS TO MEMORY	5W31	97	039 13
10W20	98	G03 18	2+16 ADDRESS TO MEMORY	5w31	98	Q39 28
10W20	99			5W31	99	
10W20	900	G13 12	CLOCK	9W31	900	M32 6
10W20	901	G05 18	2+0 BANK SELECT	5W31	901	Q34 13
10W20	902	G05 22	2+1 BANK SELECT	5W31	902	Q34 28
10W20	903	G04 8	2+2 CHASSIS 10 SELECT (011)	5w31	903	034 24
10W20	904	G04 5	2+3 CHASSIS 10 SELECT (011)	5W31	904	Q35 13
10W20	905	G04 14	2+4 CHASSIS TO SELECT (011)	5W31	905	035 28
10W20	906	G01 11	2+5 ADDRESS TO MEMORY	5W31	906	Q35 24
10W20	907	G01 14	2+6 ADDRESS TO MEMORY	5W31	907	036 13
10W20	908	G01 17	2+7 ADDRESS TO MEMORY	5W31	908	Q36 28

and a second residence of the second

10W23	0.0		DISK SÝNO TO DÍSK FILE (INPUT)	the transfer of the same of the second was
10W23	90	J01 1	2+0 READ DATA	
10W23	91	J01 !	2+1 READ DATA	The state of the s
10W23	92	J01 2	_ T T T X T X T X T X T X T X T X	
10W23	93	Jn1:10		
10W23	94	J02 1	2+4 READ DATA	
10W23	95	J02	2+5 READ DATA	e des fine des une dan des las las las las fine de payant des las las las las las las las las las la
	96		그 요즘 한 그 그림을 그 전화되었다.	
10W23		J02 2		and the second s
10W23	97	J02 19		
10W23	98	<u> </u>		The state and the test and the state and the
10W23	99	J03 :		
10W23	900	J03 20	2+10	a annu 1990 1990 1990 1990 1990 1990 1990 199
10W23	901	J03 19	2*11	
10W23	902	J04 12	REV MARK	
10W23	903	J04 5	GLOCK	
10W23	904		SECTOR MARK	
10W23	905	name annu annu annu annu annu annu annu ann		a mention and the same and the same and the same and the same experience of the body date of the
10W23	906			
10W23	907	n yan yar isang ann ann arm ann ann ann agus	## (# # # # # # # # # # # # # # # # # #	a season and their season of the season season of the seas
10W23	908			
TAMER	700			the state of the second includes the same and the same an

10W24	0.0			DISK SŸNC TO DISK FILE (OUTPUT)
10W24	90	J06	8	2+0 WRITE DATA
10W24	92	J06	4	2+2 WRITE DATA
10W24	91	J06	6	
10W24	93	J06	27	2+3 WRITE DATA
10W24	94	J06	25	2+4 WRÎTE DATA
10W24	95	J06	23	2+5 WRITE DATA
10W24	96	J05	. 8	2+6 WRITE DATA
10W24	97	J05	6	2+7 WRITE DATA
10W24	98	J05	4	2+8 WRÎTE DATA
10W24	99	J05	27	2*9 WRITE DATA
10W24	900	J05	25	2+10
10W24	901	J05		2+11
10W24	902	Jo7	8	POS CONTROL
10W24	903	J07	6	GROUP
10W24	904	Joz		
10W24	905	J07		CLOCK
10W24	906	- •		•
10W24	907			
10W24	908			
* A 25 * 1	7 T . T .	the service of the service	er somore a su	

-	10W25	0.0		DISK SYNC PASSON (OUTPUT)
	10W25	90	K02 8	2+0 OUTPUT DATA
	10W25	91	K02 6	2+1 OUTPUT DATA
	10W25	92	K02 4	2+2 OUTPUT DATA
	10W25	93	K02 27	2+3 OUTPUT DATA
	10W25	94	K02 25	2+4 OUTPUT DATA
	10W25	95	K02 23	2+5 OUTPUT DATA
	10W25	96	K03 8	2+6 OUTPUT DATA
	10W25	97	K03 6	2+7 OUTPUT DATA
	10W25	98	K03 4	Rea OUTPUT DATA
	10W25	99	K03 27	2+9 OUTPUT DATA
	10W25	900	K03 25	2+10 OUTPUT DATA
	10W25	901	K03 23	2+11 OUTPUT DATA
	10W25	902	K01 23	ACTIVE
	10W25	903	K01 8	INACTIVE
	10W25	904	K01 6	FULL
	10W25	905	K01 27	EMPTY
	10W25	906	K01 4	FUNCTION
	10W25	907	K01 25	MASTER CLEAR
	10W25	908		

10W26	00	a en para som med gan som		DISK SYNC PASS	ON (INPUT)
10W26	90	L09	-5	2+0 INPUT DATA	
10W26	91	L09	7	2+1 INPUT DATA	THE PROPERTY OF THE PARTY OF TH
10W26	92	L09	10	2+2 INPUT DATA	
10W26	93	L09	21	2+3 INPUT DATA	
10W26	94	L09	24	2+4 INPUT DATA	
10W26	95	L09	26	2+5 INPUT DATA	The state of the s
10W26	96	L08	5	2+6 INPUT DATA	
10W26	97	L08	7	2+7 INPUT DATA	от меже и се в водине в производителнителнителнителнителнителнителнителн
10W26	98	L08	10	2+8 INPUT DATA	
10W26	99	L08	21	2+9 INPUT DATA	
10W26	900	LO8	24	2+10 INPUT DAT	A
10W26	901	L08	26	2+11 INPUT DAT	
10W26	902			ACTIVE	
10W26	903	LO7	5	INACTIVE	- Design Committee Committ
10W26	904	L07	7	FULL	
10W26	905			EMPTY	The second secon
10W26	906	L26	1		
10W26	907	L26			
10W26	908				

10W27	00			DISK SYNC TO CHANNEL (INPUT)
10W27	90	K05	8	2+0 INPUT DATA
10W27	91	K05	6	2+1 INPUT DATA
10W27	92	K05	4	2+2 INPUT DATA
10W27	93	K05	27	2+3 INPUT DATA
10W27	94	K05	25	2+4 INPUT DATA
10W27	95	K05	23	2+5 INPUT DATA
10W27	96	K06	8	2+6 INPUT DATA
10W27	97	K06	6	2+7 INPUT DATA
10W27	98	K06	- 4	2+6 INPUT DATA
10W27	99	K06	27	2.9 INPUT DATA
10W27	900	K06	25	2+10 INPUT DATA
10W27	901	K06	23	2+11 INPUT DATA
10W27	902	K04	4	ACTIVE
10W27	903	K04	8	INACTIVE
10W27	904	K 0 4	6	FULL:
10W27	905	K04	27	EMPTY
10W27	906	L26	: 3	
10W27	907	L26	13	
10W27	908			

10W28	00			DISK SYNC TO CHANNEL (OUTPUT)	
L0W28	90	L01	-5	2+0 OUTPUT DATA	
10W28	91	L01	7	2+1 OUTPUT DATA	
10W28	92	L01	10	2+2 OUTPUT DATA	
10W28	93	L01	21	2+3 QUTPUT DATA	
10W28	94	L01	24	2+4 OUTPUT DATA	
10W28	95	L01	26	2÷5 OUTPUT DATA	
10W28	96	L02	5	2+6 OUTPUT DATA	
10W28	97	L02	7	2+7 OUTPUT DATA	,
10W28	98	L02	10	2+8 OUTPUT DATA	
10W28	99	L02	21	2+9 OUTPUT DATA	
10W28	900	L02	24	2+10 OUTRUT DATA	
10W28	901	L02	26	2+11 OUTPUT DATA	
10W28	902	L03	7	ACTIVE	
10W28	903	L03	21	INACTIVE	
10W28	904	L03	10	FULL	
10W28	905	L03	26	EMPTY	
10W28	906	L03	5	FUNCTION	
10W28	907	L03	24	MASTER CLEAR	
10W28	908	-			

10W39	0 0			MEMORY	TO	READ	Dis	†RIBUȚ	OR		. 1	0W39	0 0		
10W39	90	H22	28	2+53							Ĭ	0 W 3 9	90	111	9
10W39	91	H23	1	2+54							1	0 W 3 9	91	112	9
10W39	92	H23	8	2+55							1	0W39	92	114	9
10W39	93		28	2+56						 	Ĭ	0W39	93	115	9
10W39	94	H24	1	2+57							. 1	0W39	94	116	9
10W39	95	H24	8	2+58							1	0W39	95	117	9
10W39	96		28	2+59							1	1W39	96	118	<u>.</u> 2
10W39	97										1	0W39	97		•
10W39	98									 	1	0W39	98		
10W39	99										1	0W39	99		
10W39	900	H19	4	2+45							1	9EW6	900	101	9
10W39	901	H19	8	2+46							1	0W39	901	102	9
10039	902		28	2+47							1	0W39	902	103	9
10W39	903	H21	1	2+48						 	1	0W39	903	104	9
10W39	904	H21	8	2+49							1	0W39	904	105	9
10W39	905		28	2+50							1	QW39	905	108	9
10W39	906	H22	1	2+51							11	9EW0	906	109	9
10W39	907	H22	ā	2+52							1	0W39	907	110	9
10W39	908		_				, serve management o			r teger to comp	1	0W39	908		

10W39	0 0			READ	DISTRIBUTOR	FROM	MEMORY	•	10W39	0 0			
10W39	90	111	9	2+53					10W39	90	H22	28	
10W39	91	112	9	2+54	4				10W39	91	H23	1	
10W39	92	114	9	2+55					10W39	92	H23	8	
10W39	93	115	Ó	2+56			•		10W39	93	H23	28	
10W39	94	116	ģ	2+57					10W39	94	H24	4	
10W39	95	117	ģ	2+58					10W39	95	H24	8	
10W39	96	118	ģ	2+59					10W39	96	H24	28	
10W39	97		•						10W39	97		-	
10W39	98								10W39	98			
10W39	99							•	10W39	99			
10439	900	101	9	2+45					10W39	900	H19	1	
10W39	901	102	ģ	2+46					10W39	901	H19	8	
10W39	902	103	ģ	2+47					10W39	902	H19	28	
10W39	903	104	9	2+48				•	10W39	903	H21	1	
10W39	904	105	ģ	2+49					10W39	904	H21	8	
10W39	905	108	ģ	2+50					10W39	905	H21	28	
10W39	906	109	ģ	2+51					10W39	906	H22	1	
10W39	907	110	9	2+52					10W39	907	H22	8	
10W39	908		•						10839	908	_		

12W01	00			DISPLAY SYNC 1 TO CHANNEL (OUTPUT)
12W01	90	A01	3	2+0 OUTPUT DATA
12W01	91	A01	13	2+1 OUTPUT DATA
12W01	92	A01	16	2+2 OUTPUT DATA
12W01	93	A01	26	2+3 OUTPUT DATA
12001	94	A02	3	2+4 OUTPUT DATA
12W01	95	A02	13	2+5 OUTPUT DATA
12W01	96	A02		2+6 OUTPUT DÂTA
12001	97	A02	_	2+7 OUTPUT DATA
12001	98	· · · ·	3	2+8 OUTPUT DATA
12W01	99		13	2+9 OUTPUT DATA
12W01	900	· · · - ·	16	2+10 OUTPUT DATA
12W01	901	**	26	2+11 OUTPUT DATA
12W01	902	B01	3	ACTIVE
12001	903	B01		INACTIVE
12001	904	B01		FULL
12W01	905	B01		EMPTY
	906	B02		FUNCTION
12W01	7 . 7	the age of the page and the	3	
12W01	907	802	13	MASTER CLEAR
12W01	908			

2W02	0.0			DISPLAY SYNC 1 TO CHANNEL (INPUT)
2W02	90	E02	7	2+0 INPUT DATA
2W02	91	E02	5	2+1 INPUT DATA
2002	92	E02	3	2+2 INPUT DATA
2W02	93	E02	28	2+3 INPUT DATA
2W02	94		26	2+4 INPUT DATA
2W02	95	E02		2+5 INPUT DATA
2002	96	E01	7	2+6 INPUT DATA
2W02	97	E01	<u>-</u>	2+7 INPUT DATA
2W02	98	E01	3	2+8 INPUT DATA
2W02	99	E01	28	2+9 INPUT DATA
2W02	900	E 0 1	26	2+1n INPUT DATA
2402	901	En1	24	2+11 INPUT DATA
2002	902	E03	7	ACTIVE
2W02	903	E 0 3	5	INACTIVE
2W02	904	E03	3	FULL
2M05	905	The second second second	28	EMPTY
-	906			
2W02		D01	12	
5 M O S	907	003	12	RESYNC (1US)
2 W O 2	908			Company to the company of the compan

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12W03
          00
                         DISPLAY SYNC 1 PASSON (OUTPUT)
          90
12W03
                         2+0 OUTPUT DATA
                        2+1 OUTPUT DATA
2+2 OUTPUT DATA
          91
12W03
               A01 15
          92
12W03
               A01 14
          93
12W03
                        243 OUTPUT DATA
               A01 28
12W03
         94
                         2+4 OUTPUT DATA
               A02
                        2+5 OUTPUT DATA
2+6 OUTPUT DATA
12W03
          95
               A02
                   15
         96
12W03
               Anz
12W03
          97
               A02 28
                         2+7 OUTPUT DATA
                         2+8 OUTPUT DATA
          98
12W03
              An3
                         2+9 OUTPUT DATA
12W03
         99
              A 0 3
                   15
                        2+10 OUTPUT DATA
2+11 OUTPUT DATA
ACTIVE
INACTIVE
12W03
        900
               AD3
                   14
12W03
               A03
                   28
        901
12W03
        902
              B01
12W03
        903
              B01
                   15
                        FULL
12W03
        904
                   14
              B01
        905
12W03
              B01
                         FUNCTION
        906
              B02
12W03
                         MASTER CLEAR
              B02 15
12W03
        907
12W03
        908
```

12W04	0.0			DISPLAY SYNC 1 PASSON (INPUT)
12W04	90	<b>€</b> 02	23	2+0 INPUT DATA
12W04	91	E02	22	2+1 INPUT DATA
12004	92	E02		2+2 INPUT DATA
12W04	93	E02		2+3 INPUT DATA
12W04	94	E02	9	2+4 INPUT DATA
	95	E02		The state of the s
12W04	_		8	
12W04	96	E01		2+6 INPUT DATA
12W04	97	E01		2+7 INPUT DAȚA
12W04	98	E01	21	2+8 INPUT DATA
12W04	99	E01	10	2+9 INPUT DATA
12W04	900	E01	9	2+10 INPUT DATA
12W04	901	E01	8	2+11 INPUT DATA
12W04	902	E03	23	ACTIVE
12W04	903	E03	22	INACTIVE
		-		FULL
12W04	904	€03	21	· · · · · · · · · · · · · · · · · · ·
12W04	905	E03		EMPTY
12W04	906	D07	7	
12W04	1907	D07	5	
12W04	806			
	7.5 %		SAME AL SHEET ST. TH	THE PROPERTY OF THE PROPERTY AND THE PROPERTY OF THE PROPERTY

```
12W05 00
                       DISPLAY SYNC 1 TO CONSOLE 0 (OUTPUT)
                      X+0
         90
12W05
         91
                      X+1
12W05
             F16 2
             F16 14
F19 10
                       X+2
12W05
         92
         93
                      X+3
12W05
         94
            F19 11
                       X+4
12005
                      X+5
X+6
12W05
         95
             F19
                  .8
             F19
         96
12W05
                  19
         97
             F19
12W05
                  18
                       X+7
12W05
             F19 21
                       X + 8
         99
             F16 19
                       Y+0
12W05
                       Y+1
Y+2
12W05
        900
             F16 27
             F16 28
12W05
        901
             F22
                       Y . 3
12W05
        902
             F22
                       Y+4
12W05
        903
             F22 14
                       Y+5
12W05
        904
             F22
12W05
        905
                  19
                       Y . 6
             F22 27
                       Y+7
12W05
        906
12W05
        907
             F22 28
                       Y +8
             F07 25
                       FOCUS AND ASTIGMATISM
12W05
        908
```

```
DISPLAY SYNC 1 TO CONSOLE 0 (INPUT)
12W06
                         KEYBOARD BIT 0
12W06
               F05
                     1
               F05
                         KEYBOARD BIT 1
12W06
          91
                     4
                         KEYBOARD BIT 2
KEYBOARD BIT 3
               F 0 5
12W06
          92
                     5
12W06
          93
               F05
                    24
12W06
          94
               F05
                    25
                         KEYBOARD BIT 4
                         KEYBOARD BIT 5
          95
               F05 28
12W06
          96
               F05
                         KEY DOWN
                   19
12
12W06
12W06
                         UNBLANK LEFT CONSOLE OUNBLANK RIGHT CONSOLE O
12W06
               E35 14
          98
          99
               E35 20
12W06
                         SMALL SIZE
MED. SIZE
               F13 10
12W06
         900
               F13 11
12W06
         901
        902
903
12W06
                         VERT. PUSH ANALOG
Vert. Pull analog
               F27
12W06
               F27 15
12W06
         904
         905
               F27 14
                         HORIZ. PUSH ANALOG
12W06
               F27 13
                         HORIZ. PULL ANALOG
12W06
         906
        907
12W06
12W06
        908
```

12W07	00			DÍSPLAŸ SYNC 1 TO CONSOLE 1 COŬTPŮŤ)
12W07	90	F16	3	X+0 .
12W07	91	F16	6	X+1
12W07	92	F16	10	X+2
12W07	93	F19		X+3
12W07	94	F19	1.3	X+4
12W07	95	F19	7	X+5
12W07	96	F19	17	X+6
12W07	97	F19	20	X+7
12W07	98	F19	1.6	N+6
12W07	99	F16	17	Y • 0
12W07	900	F16	25	Y <b>♦1</b>
12W07	901		26	<u></u>
12W07	902	F22	# <del>-</del>	Y+3.
				Y•4
12W07	903	F22		
12W07	904	F22		Y+5
12W07	905	F22		Y+6
12W07	906	F22		<b>*                                    </b>
12W07	907	F22		Y+8
12W07	908	F07	27	FOCUS AND ASTIGMATISM

12W08	00	40 00 1/4 40 40 6/2		DISPLAT SYNC 1 TO CONSOLE 1 (INPUT)
12W08	90	F06	1	KEYBOARD INPUT BIT O
12W08	91	F06	-4	KEYBOARD INPUT BIT 1
12W08	92	F06	. 5	KEYBOARD INPUT BIT 2
12W08	93	F06	24	KEYBOARD INPUT BIT 3
12W08	94	F06	25	KEYBOARD INPUT BIT 4
12W08	95	F06	28	KEYBOARD INPUT BIT 5
12W08	96	F06	19	KEY DOWN
12W08	97	F06	12	KEY UP
12W08	98	E34	14	UNRLANK LEFT
12W08	99	E34	20	UNBLANK RIGHT
12W08	900	F13		SMALL SIZE
12W08	901	F13	13	MED. SİZE
12W08	902			
12W08	903	F26		VERT. PUSH ANALOG
12W08	904	F26		VERT. PULL ANALOG
12W08	905	F26		HORIZ. PUSH ANALOG
12W08	906	F26	13	HORIZ, PULL ANALOG
12W08	907			
12W08	908	. > DT-00 T-00-00-00		

12W14	. 0.0			DISPLAY SYNC 2 TO CHANNEL (OUTPUT)
12W14	90	G01	3	2+0 QUTPUT DATA
12W14	91	001	13	2+1 OUTPUT DATA
12W14	92	001	16	2+2 OUTPUT DATA
12W14	93	G01	26	2+3 OUTPUT DATA
12W14	94	002	3_	204 OUTPUT DATA
12W14	95	002	13	2+5 OUTPUT DATA
12W14	96	602		2.6 OUTPUT DATA
12W14	97	002	26	2+7 OUTPUT DATA
12W14	98	003	3	2+8 QUTPUT DATA
12W14	99	003	13	2+9 OUTPUT DATA
12W14	900	003	16	2+10 GUTPUT DATA
12W14	901	003	26	2+11 OUTPUT DATA
12W14	902	H01	3	ACTIVE
12W14	903	H01	13	INACTIVE
12W14	904	H01	16	FULL
12W14	905	H01	26	EMPTY
12W14	906	H02	3	FUNCTION
12W14	907	H02	13	MASTER CLEAR
12W14	908	_	-	

12W15	0.0			DISPLAY SYNC 2 TO CHANNEL (INPÚT)
12W15	90	K02	7	2+0 INPUT DATA
12W15	91	K02	5	2+1 INPUT DATA
12W15	92	K02	3	2+2 INPUT DATA
12W15	93	K02	28	2+3 INPUT DATA
12W15	94	K02	26	2+4 INPUT DATA
12W15	95	K02	24	2+5 INPUT DATA
12W15	96	K01	7	Z+6 INPUT DAŤA
12W15	97	K01	5	2+7 INPUT DATA
12W15	98	K01	3	2+8 INPUT DAŤA
12W15	99	K01	28	2.9 INPUT DATA
12W15	900	K01	26	2+10 INPUT DATA
12W15	901	K01	24	2+11 INPUT DATA
12W15	902	K03	7	ACTÍVE
12W15	903	K03	5	INACTIVE
12W15	904	K03	3	FULL
12W15	905	K03	28	EMPTY
12W15	906	J01	12	CLOCK (100US)
12W15	907	J03	12	RESYNC (1US)
12W15	908			

12W16	0.0		DISPLAY SYNC 2 PASSON (OUTPUT)	*** ******
12W16	90	G01 1	2+0-OUTPUT DATA	
12W16 12W16	91 92	G01 15 G01 14	201 OUTPUT DATA 202 OUTPUT DATA	
12W16 12W16	93 94	G01 28 G02 1	2+3 OUTPUT DATA 2+4 OUTPUT DATA	
12W16	95	G02 15	2+5 OUTPUT DATA	we will have been to
12W16	96	G02 14 G02 28	2+6 OUTPUT DATA	S-REPUBLIC RESIDENCE OF ST
12W16	98	G03 1 G03 15	2+8 OUTPUT DATA 2+9 OUTPUT DATA	
12W16 12W16	900	G03 14 G03 28	2+10 :OÙTPUT DATA 2+11 OÙTPUT DATA	
12W16	902	H01 1	ACTIVE INACTIVE	
12W16	904	H01 14	PÜLL	
12W16 12W16	905 906	H01 28 H02 1	EMPTY Function	
12W16 12W16	907 908	H02 15	MASTER CLEAR	
	~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~			(

12W17	00			DISPLAY SYNC 2 PASSON (INPUT)
12W17	90	K02	23	2+0 INPUT DATA
12W17	91	K02	22	2+1 INPUT DATA
12W17	92	K02	21	2+2 INPUT DATA
12W17	93	K02	10	2+3 INPUT DATA
12W17	94	K02	ğ	2+4 INPUT DATA
12W17	95	K02	8	2+5 INPUT DATA
12W17	96	K01	23	2+6 INPUT DATA
12W17	97	K01	22	2+7 INPUT DATA
12W17	98	K01	21	2+8 INPUT DATA
12W17	99	KO1	10	2+9 INPUT DATA
12W17		K01		
	900		9	The state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the s
12W17	901	K01	8	2+11 INPUT DATA
12W17	902	K <sub>0</sub> 3		ACTIVE
12W17	903	K03	22	INACTIVE
12W17	904	K03	21	<u> </u>
12W17	905	K03	10	EMPTY
12W17	906	J07	7	
12W17	907	J07	5	
12W17	908			

```
DISPLAY SYNC 2 TO CONSOLE 0 (OUTPUT)
12W18 00
  12W18
           90
                        X + 0
               L16
           91
                        X+1
  12W18
               L16
                    . 2
  12W18
           92
               L16 14
                        X+2
                        X+3
           93
               L19 10
  12W18
 12W18
           94
               L19 11
                        X±4
           95
               L19
                        X+5
  12W18
               L19
  12W18
           96
                        X+6
  12W18
           97
               L19
                    18
                        X+7
           98
               L19 21
  12W18
                        X+8
           99
  12W18
               L16 19
                         Y + 0
  12W18
                        Y+1
               L16 27
          900
  12W18
          901
               L16 28
                        Y + 2
                        Y+3
  12W18
          902
               L22
               L22
          903
                         Y+4
  12W18
                    . 2
               L22 14
  12W18
          904
                        Y .5
          905
               L22 19
                        Y+6
  12W18
  12W18
          906
               L22 27
                        Y#7
               L22 28
L07 25
  12W18
          907
                        Y+8
                        FOCUS AND ASTIGMATISM
          908
  12W18
```

```
DISPLAY SYNC 2 TO CONSOLE 0 (INPUT)
12W19 00
         90
             L05
12W19
                      KEYBOARD BIT O
             L05
                      KEYBOARD BIT 1
12W19
         91
                   4
                      KEYBOARD BIT 2
KEYBOARD BIT 3
12W19
         92
             L05
                      KEYBOARD BIT
         93
             L05
12W19
                 24
         94
             L05
                      KEYBOARD BIT 4
12W19
                  25
                       KEYBOARD BIT
12W19
         95
             L05
                 28
             L05
                      KEY DOWN
12W19
         96
                 19
             L05 12
12W19
         97
                      KEY UP
                      UNBLANK REFT
             K35 14
12W19
         98
             K35 20
12W19
         99
                      SMALL SIZE
12W19
        900
             L13 10
                      MED. SIZE
12W19
             L13 11
        901
12W19
        902
12W19
        903
             L27 16
                       VERT. PUSH ANALOG
       904
                      VERT. PULL ANALOG
Horiz, push analog
12W19
             L27 15
             L27 14
12W19
                      HORIZ. PULL ANALOG
12W19
        906
             L27 13
12W19
        907
12W19
        908
```

12W20	0.0			DISPLAT SYNC 2 TO CONSOLE 1 (OUTPUT)	the second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second secon
12W20	90	L16	3	X+0	•
12W20	91	L16	- 6	X+1	
12W20	92	L16	10	X+2	
12W20	93	L19	4	X+3	
12W20	94	L19	13	X+4	
12W20	95	L19	7	X+5	
12W20	96	L19	17	X+6	
12W20	97	L19	20	*X*7	
12W20	98	L19	15	X + 8	
12W20	99	L16	17	Y • 0	
12W20	900	L16	25	Y+1	
12W20	901		26	Y • 2	
12W20	902	L22	3	·Y+3	
12W20	903	L22	6	Y•4	
12W20	904	L22	10	`Y <b>≑</b> 5	
12W20	905		17	Y•6	
12W20	906	L22		Y+7	
12W20	907	L22		Y+8	
12W20	908	L07	27	FOCUS AND ASTIGMATISM	

2W21	00			DISPLAŸ SYNC 2 TO CONSOLE 1 (IŇPŮT)
2W21	90	L06	1	KEŸBOARD BIT 0
12W21	91	L06	: 4	KEYBOARD BIT 1
12W21	92	L06	- 5	KEŸBOAŔD BIŤ 2
12W21	93	L06	24	KEYBOARD BIT 3
12W21	94	L06	25	KEYBOARD BIT 4
12W21	95	L06	28	KEYBOARD BIT 5
12W21	96	L06	19	KEŸ DOWN
12W21	97	L06	12	KEA Nb
12W21	98	K34	14	UNBLANK LEFT
12W21	99	K34	20	UNBLANK RIGHT
12W21	900	L13	12	SMALL SIZE
12W21	901	L13	13	MED. SIZE
12W21	902	-		
12W21	903	L26	16	VERT. PUSH ANALOG
12W21	904	L26	15	VERT, PULL ANALOG
12W21	905	L26	14	HORIZ, PUSH ANALOG
12W21	906	L26	13	HORÍZ. PULL ÀNÁLOG
12W21	907		er 100 mm	
12W21	908			

13W03	0.0		MEMORY FROM WRITE DISTRIBUTOR	14W11 00
13W03	90	H27 24	2+23 FAN OUT TO MEMORY	14W11 90 I11 3
13W03	91	H29 2	2+24 FAN OUT TO MEMORY	14W11 91 112 18
L3W03	92	H29 19	2#25 FAN OUT TO MEMORY	14W11 92 112 13
3W03	93	H29 24	2+26 FAN OUT TO MEMORY	14W11 93 113 3
3W03	94	H30 2	2+27 FAN OUT TO MEMORY	14W11 94 114 18
3W03	95	H30 19	2+28 FAN OUT TO MEMORY	14W11 95 114 13
3W03	96	H30 24	2429 FAN OUT TO MEMORY	14W11 96 I15 3
3W03	97			14W11 97
3W03	98			14W11 98
3W03	99			14W11 99
3W03	900	H25 2	2+15 FAN OUT TO MEMORY	14W11 900 In6 18
3W03	901	H25 19	2+16 FAN OUT TO MEMORY	14W11 901 106 13
3W03	902	H25 24	2+17 FAN OUT TO MEMORY	14W11 902 107 3
3W03	903	H26 2	2+18 FAN OUT TO MEMORY	14W11 903 108 18
3W03	904	H26 19	2+19 FAN OUT TO MEMORY	14W11 904 IO8 13
3W03	905	H26 24	2+20 FAN OUT TO MEMORY	14W11 905 109 3
3W03	906	H27 2	2+21 FAN OUT TO MEMORY	14W11 906 I10 18
3W03	907	H27 19	2.22 FAN OUT TO MEMORY	14W11 907 I10 13
3WO3	908			14W11 908

13W04	0.0			MEMORY FROM WRITE DISTRIBUTOR	15W11	0.0		
3W04	90	H34	24	2+38 FAN OUT TO MEMORY	15W11	90	133	3
3W04	91	H35	2	2+39 FAN OUT TO MEMORY	- 15W11	91	134	18
3W04	92	H35	19	2+40 FAN OUT TO MEMORY	19W11	92	134	13
3W04	93	H35	24	2+41 FAN OUT TO MEMORY	15W11	93	135	3
3 W D 4	94	H36	. 2	2+42 FAN OUT TO MEMORY	15W11	94	136	18
3 W 0 4	95	H36	19	2+43 FAN OUT TO MEMORY	15W11	95	136	
3W04	96	H36	24	2+44 FAN OUT TO MEMORY	15W11	96	137	-3
3W04	97	- 12-12-1		The second lines and the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the second lines are the secon	15W11	97		
WO4	98				15W11	98		
5W04	99				15W11	99		
5W04	900	H31	2	2+30 FAN OUT TO MEMORY	15W11	900	128	1.8
3W04	901	H31	19	2+31 FAN OUT TO MEMORY	15W11	901	128	13
3 W 0 4	902	H31	24	2+32 FAN OUT TO MEMORY	15W11	902	129	3
3W04	903	H32	2	2+33 FAN OUT TO MEMORY	15W11	903		18
3W04	904	H32	19	2+34 FAN OUT TO MEMORY	15W11	904	130	13
3W04	905	H32	24	2+35 FAN OUT TO MEMORY	15W11	905	131	3
3W04	906	H34	2	2+36 FAN OUT TO MEMORY	15W11	906	132	18
3W04	907	H34	19	2+37 FAN OUT TO MEMORY	15W11	907	132	
3W04	908				15W11	908		

13W05	0.0		MEMORY FROM WRITE DISTRIBUTOR	16W11 00
13W05	90	H40 24	2+53 FAN OUT TO MEMORY	16W11 90 I11 3
13W05	91	H41 2	2+54 FAN OUT TO MEMORY	16W11 91 I12 18
13W05	92	H41 19	2455 FAN OUT TO MEMORY	16W11 92 [12 13
13W05	93	H41 24	2+56 FAN OUT TO MEMORY	16W11 93 113 3
13W05	94	H42 2	2-57 FAN OUT TO MEMORY	16W11 94 I14 18
13W05	95	H42 19	2058 FAN OUT TO MEMORY	16W11 95 I14 13
13w05	96		2+39 FAN OUT TO MEMORY	16W11 96 115 3
13W05	97			16W11 97
13W05	98			16W11 98
13W05	99			16W11 99
13W05	900	H37 2	2+45 FAN OUT TO MEMORY	16W11 900 IO6 18
13W05	901	H37 19	2446 FAN OUT TO MEMORY	16W11 901 106 13
13W05	902	H37 24	2+47 FAN OUT TO MEMORY	16W11 902 107 3
13W05	903	H39 2	2+48 FAN OUT TO MEMORY	16W11 903 IO8 18
13W05	904	H39 19	2#49 FAN OUT TO MEMORY	16W11 904 IO8 13
13W05	905	H39 24	2+50 FAN OUT TO MEMORY	16W11 905 109 3
13W05	906	H40 2	2451 FAN OUT TO MEMORY	16W11 906 I10 18
13W05	907	H40 19	2+52 FAN OUT TO MEMORY	16W11 907 [10 13
13W05	908			16W11 908

13W06	00			MEMORY TO READ DISTRIBUTOR	3w09	00	and was the man and the part and the part that had been been to have the men and the man	
13W06	90	H21	28	2+8	3409	90	135-11	
13W06	91	H22	1	2.9	3W09	91	136 11	
13W06	92	H22	8	:2+10	3W09	92	138 11	
13W06	93	H22	28	2+11	3409	93	139 11	
13W06	94	H24	_ 1	2+12	3409	94	140 11	
13W06	95	H24	8	2+13	3W09	95	141 11	
13W06	96	H24	28	2+14	3W09	96	142 11	
13W06	97				3409	97		
13W06	98				3409	98		
13W06	99				3409	99	The same state was the same same same same same same same sam	
13W06	900	H19	1	2+0	3409	900	125 11	
13W06	901	H19	8	2+1	3w09	901	126 11	*
13W06	902	H19	28	2+2	3409	902	127 11	
13W06	903	H20	1	2+3	3W09	903	128 11	
13W06	904	H20	ā	2+4	3409	904	129 11	
13W06	905	H20	28	2.5	3409	905	132 11	
13W06	906	H21	1	2+6	3W09	906	133 11	
13W06	907	H21	8	2+7	3W09	907	134 11	
13W06	908				3W09	908		

13W07	00			MEMORY TO READ DISTRIBUTOR	4W15	0.0	
13W07	90	H27	28	2+23	4W15	90	I11 1
13W07	91	H29	1	2+24	4W15	91	112 1
13W07	92	H29	8	2+25	4W15	92	114 1
13W07	93	H29	28	'2+26	4W15	93	115 1
13W07	94	H30	1	2+27	4W15	94	116 1
13W07	95	H30	8	2.28	4W15	95	117 1
13W07	96	H30	28	2+29	4W15	96	118 1
13W07	97	G38		MEMORY GO	4W15	97	121 2
13W07	98	G38		MEMORY WRITE	4W15	98	124 1
13W07	99				4W15	99	
13W07	900	H25	1	·2+15	4W15	900	101 1
13W07	901	H25	8	2*16	4W15	901	102 1
13W07	902	H25	28	2+17	4W15	902	103 1
13W07	903	H26	1	2+18	4W15	903	104 1
13W07	904	H26	8	2+19	4W15	904	105 1
13W07	905	H26	28	2+20	4W15	905	108 1
13W07	906	H27	1	8+21	4W15	906	109 1
13W07	907	H27	8	2+22	4W15	907	110 1
13W07	908		_		4W15	908	

13W08	00			MEMORY TO READ DISTRIBUTOR	9W16	00	*************************
13W08	90	H34	28	2+38	9W16	90	135 11
13W08	91	H35	1	2+39	9W16	91	136 11
13W08	92	H35	8	2+40	9W16	92	138 11
13W08	93	H35	28	2+41	9W16	93	139 11
13W08	94	H36	1	2+42	9W16	94	140 11
13W08	95	H36	. 8	2+43	9W16	95	141 11
13W08	96	H36	28	2+44	9W16	96	142 11
13W08	97				9W16	97	
13W08	98				9W16	98	MA AND AND 1070 MAY 1000 MAY 1070 AND 1070
13W08	99				9W16	99	
13W08	900	H31	1	2+30	9W16	900	125 11
13W08	901	H31	8	2+31	9W16	901	126 11
13W08	902	H31	28	2+32	9W16	902	127 11
13W08	903	H32	1	2+33	9W16	903	128 11
13W08	904	H32	8	2+34	9W16	904	129 11
13W08	905	H32	28	2+35	9W16	905	132 11
13W08	906	H34	1	2+36	9W16	906	133 11
13W08	907	H34	8	2+37	9W16	907	134 11
13W08	908				9W16	908	

13W09	0.0			MEMORY TO READ DISTRIBUTOR	10W16	0.0	ghi digga digga digga pang pang pang mana mana sana sana mana dana dana danay gapa, yapa, rana ra	a was the state against the first time the state of
13W09	90	H40	28	2+53	10W16	90	111 11	
13W09	91	H41	1	2+54	10W16	91	112 11	t de verte anny paragraphistation en aus de diriément de Ci
13W09	92	H41	ā	2+55	10W16	92	114 11	
13W09	93	H41	28	2+56	10W16	93	115 11	I have no as the class that the their sole think have man the
13W09	94	H42	1	2.57	10W16	94	116 11	
13W09	95	H42	8	2+58	10W16	95	117 11	
13W09	96	H42	28	2+59	10W16	96	118 11	
13W09	97				10W16	97	The state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the s	and constraining contract policies, some representation of
13009	98				10W16	98		
13W09	99				10W16	99	Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie Marie	and the second of the second of the second
13W09	900	H37	4	2+45	18W16	900	101 11	
13W09	901	H37	8	2+46	10W16	901	102 11	
13W09	902	H37	28	2+47	10W16	902	103 11	
13W09	903	H39	1	2.48	10W16	903	104 11	AND THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF T
13W09	904	H39	Ā	2+49	10W16	904	105 11	
13009	905	H39	28	2+50	10W16	905	108 11	
13009	906	H40	-4	2+51	10W16	906	109 11	
13W09	907	H40	<b>-</b>	2+52	10W16	907	110 11	
13W09	908		-	TO THE	10W16	908		

13W10	0.0		DISTRIBUTOR	FROM WRITE BUFFER	2W05	00
13W10	90	139 4	2+0 DATA TO N	MEMORY FAN OUT	2W05	90 B12 8
13W10	91	139	2+1 DATA TO	MEMORY FAN OUT	2W05	91 812 6
13W10	92	139 7	2+2 DATA TO I	MEMORY FAN OUT	2W05	92 B12 4
13W10	93	139 27	2+3 DATA TO	MEMORY FAN OUT	2W05	93 B12 27
13W10	94	139 25	2+4 DATA TO 1	MEMORY FAN OUT	2W05	94 B12 25
13W10	95	139 22	2+5 DATA TO	MEMORY FAN OUT	2W05	95 B12 23
13W10	96	140 4	2+6 DATA TO N	MEMORY FAN OUT	2W05	96 B13 8
13W10	97	140	2+7 DATA TO	MEMORY FAN OUT	2W05	97 813 6
13W10	98	140 7	2+8 DATA TO	MEMORY FAN OUT	2W05	98 B13 4
13W10	99	140 27	2+9 DATA TO	MEMORY FAN OUT	2W05	99 B13 27
13W10	900	140 25	2+10 DATA TO	MEMORY FAN OUT	2W05 9	00 B13 25
13W10	901	140 22	2+11 DATA TO	MEMORY FAN OUT	2W05 9	01 B13 23
13W10	902	141	2+12 DATA TO	MEMORY FAN OUT	2W05 9	02 B14 8
13W10	903	141 6	2+13 DATA TO	MEMORY FAN OUT	2W05 9	03 B14 6
13W10	904	141 7	2+14 DATA TO	MEMORÝ FAN OUT	2W05 9	04 B14 4
13W10	905	C14 14	MEMORY MARGIN	V	2W05 9	05 B22 22
13W10	906				2W05 9	06
13W10	907			•	2W05 9	07
13W10	908				2W05 9	08

13W11	00	n, gan 1004 gay 1000 000 day 1000 day 1000 day 1000 day	WRITE DISTRIBUTOR TO MEMORY	14W03	00	
13W11	90	133:11	2+8 FAN OUT TO MEMORY	14W03	90	H03-24
13W11	91	134 5	2+9 FAN DUT TO MEMORY	14W03	91	H04 2
13W11	92	135 20	2+10 FAN OUT TO MEMORY	14W03	92	H04 19
13W11	93	135 11	2+11 FAN OUT TO MEMORY	1403	93	H04 24
13W11	94	136 5	2+12 FAN OUT TO MEMORY	14003	94	H06 2
13W11	95	137 20	2+13 FAN OUT TO MEMORY	1403	95	H06 19
13W11	96	137 11	2+14 FAN OUT TO MEMORY	14W03	96	H06 24
13W11	97			1403	97	
13W11	98			14W03	98	
13W11	99			1403	99	
13W11	900	128 5	2+0 FAN OUT TO MEMORY	1403	900	H01 2
13W11	901	129 20	2+1 FAN OUT TO MEMORY	14W03	901	H01 19
13W11	902	129 11	2+2 FAN OUT TO MEMORY		902	H01 24
13W11	903	130 5	2+3 FAN OUT TO MEMORY	14403	903	H02 2
13W11	904	131 20	2.4 FAN OUT TO MEMORY	14403	904	H02 19
13W11	905	131 11	2+5 FAN OUT TO MEMORY		905	H02 24
13W11	906	132 5	2+6 FAN OUT TO MEMORY		906	H03 2
13W11	907	133 20	2+7 FAN OUT TO MEMORY		907	H03 19
13W11	908			€F <del>-</del>	908	·

13W12	0.0		WRITE DISTRIBUTOR TO MEMORY	4W03	00	The first date of the same of the same same of
13W12	90	133 5	2+8 FAN OUT TO MEMORY	403	90	H03 24
13W12	91	134 20	2+9 FAN OUT TO MEMORY	4W03	91	H04 2
13W12	92	134 11	2+10 FAN OUT TO MEMORY	4W03	92	H04 19
13W12	93	135 5	2+11 FAN OUT TO MEMORY	4003	93	H04 24
13W12	94	136 20	2+12 FAN OUT TO MEMORY	4403	94	H06 2
13W12	95	136 11	2+13 FAN OUT TO MEMORY	4WC3	95	H06 19
13W12	96	137 5	2+14 FAN OUT TO MEMORY	4003	96	H06 24
3W12	97			4W03	97	
13W12	98			4003	98	
3W12	99			4W03	99	
13W12	900	128 20	2+0 FAN OUT TO MEMORY	4003	900	H01 2
JW12	901	128 11	2+1 FAN OUT TO MEMORY	4003	901	H01 19
13W12	902	129 5	2+2 FAN OUT TO MEMORY	4003	902	H01 24
JW12	903	130 20	2+3 FAN OUT TO MEMORY	4W03	903	H02 2
3W12	904	130 11	2.4 FAN OUT TO MEMORY	4W03	904	H02 19
3W12	905	131 5	2+5 FAN OUT TO MEMORY	4003	905	H02 24
3W12	906	132 20	2+6 FAN OUT TO MEMORY	4003	906	H03 2
3W12	907	132 11	2+7 FAN OUT TO MEMORY	4W03	907	H03 19
13W12	908		The second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second secon	4W03	908	.,07

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13W13	00	an taun agan wan man man taifi taab tany agan dari	WRITE DISTRIBUTOR TO MEMORY	3W15	0.0	ang kata dalah angguntan dalah dalah daga penar man man man, ranggungg nang anggunga man mar res	nga 1880 - 187 200 18
13W13	90	133 3	2+6 FAN OUT TO MEMORY	3W15	90	H21 24	
13W13	91	134 18	2+9 FAN QUT TO MEMORY	3W15	91	H22 2	participal education
13W13	92	134-13	2+10 FAN OUT TO MEMORY	3W15	92	H22 19	
13W13	93	135 3	2+11 FAN OUT TO MEMORY	3W15	93	H22 24	
13W13	94	136 18	2+12 FAN OUT TO MEMORY	3W15	94	H24 2	
13W13	95	136 13	2+13 FAN OUT TO MEMORY	3W15	95	H24 19	
13W13	96	137 3	2+14 FAN OUT TO MEMORY	3W15	96	H24:24	
13W13	97	G39 28		3W15	97	123 19	
13W13	98			3W15	98		
13W13	99			3W15	99		
13W13	900	128 18	2+0 FAN OUT TO MEMORY	3W15	900	H19 2	
13W13	901	128 13	2+1 FAN OUT TO MEMORY	3W15	901	H19 19	
13W13	902	129 3	2+2 FAN OUT TO MEMORY	3W15	902	H19 24	
13W13	903	130 18	2+3 FAN OUT TO MEMORY	3W15	903	H20 2	****
13W13	904	130 13	2+4 FAN OUT TO MEMORY	3W15	904	H20 19	
13W13	905	131 3	2+5 FAN OUT TO MEMORY	3W15	905	H20 24	
13W13	906	132 18	2+6 FAN OUT TO MEMORY	3W15	906	H21: 2	
13W13	907	132 13	2+7 FAN OUT TO MEMORY	3W15	907	H21 19	
13W13	908			3W15	908		

13W14	00		WRITE DISTRIBUTOR TO MEMORY	903	0.0	
13W14	90	133 28	2+8 FAN OUT TO MEMORY	9W03	90	H21 24
13W14	91	134 24	2+9 FAN OUT TO MEMORY	9W03	91	H22 2
13W14	92	134 7	2+10 FAN OUT TO MEMORY	9W03	92	H22 19
13W14	93	135 28	2+11 FAN OUT TO MEMORY	9W03	93	H22 24
13W14	94	136 24	2+12 FAN OUT TO MEMORY	9 W D 3	94	H24 2
13W14	95	136 7	2+13 FAN OUT TO MEMORY	9W03	95	H24 19
3W14	96	137 28	2+14 FAN OUT TO MEMORY	9W03	96	H24 24
3W14	97	The state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the s	THE RESIDENCE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF THE STATE OF T	9W03	97	
3W14	98			9W03	98	
3W14	99			9W03	99	
3W14	900	128 24	2+0 FAN OUT TO MEMORY	903	900	H19 2
13W14	901	128 7	2+1 FAN OUT TO MEMORY	9W03	901	H19 19
13W14	902	129 28	2+2 FAN OUT TO MEMORY	9 W D 3	902	H19 24
13W14	903	130 24	2+3 FAN OUT TO MEMORY	903	903	H20 2
13W14	904	130 7	2+4 FAN OUT TO MEMORY	9403	904	H20 19
13W14	905	131 28	2+5 FAN OUT TO MEMORY	9W03	905	H20 24
13W14	906	132 24	2+6 FAN OUT TO MEMORY	9403	906	H21 2
3W14	907	132 7	2+7 FAN OUT TO MEMORY	9W03	907	H21 19
13W14	908			9W03	908	

13W15	0.0	~~~~~~~	WRITE DISTRIBUTOR TO MEMORY	10W03	00	
13W15	90	133 13	2+8 FAN OUTSTO MEMORY	10W03	90	H03 24
13W15	91	134 3	2+9 FAN OUT TO MEMORY	10003	91	H04 2
13W15	92	135 18	2+10 FÄN OUT TO MEMORY	10W03	92	H04 19
13W15	93	135 13	2+11 FAN OUT TO MEMORY	10W03	93	H04 24
13W15	94	136 3	2+12 FAN OUT TO MEMORY	10w03	94	H06 2
13W15	95	137 18	2+13 FAN OUT TO MEMORY	10003	95	H06 19
13W15	96	137 13	2+14 FAN OUT TO MEMORY	10003	96	H06 24
13W15	97			10W03	97	
13W15	98			10W03	98	
13W15	99			10w03	99	
13W15	900	128 3	2±0 FAN OUT TO MEMORY	10W03	900	H01 2
13W15	901	129 18	2+1 FAN OUT TO MEMORY	10003	901	H01 19
13W15	902	129 13	2#2 FAN OUT TO MEMORY	10W03	902	H01 24
13W15	903	130 3	2#3 FAN DUT TO MEMORY	10W03	903	H02 2
13W15	904	131 18	2#4 FAN OUT TO MEMORY	10W03	904	H02 19
13W15	905	131 13	2+5 FAN OUT TO MEMORY	10003	905	H02 24
13W15	906	132 3	2+6 FAN OUT TO MEMORY	10W03	906	H03 2
13W15	907	133 18	2+7 FAN OUT TO MEMORY	10W03	907	H03 19
13W15	908			10003	908	

13W16	0.0		WRITE DISTRIBUTOR TO MEMORY	15W03	00		
L3W16	90	133	9 2+8 FAN OUT TO MEMORY	15W03	90 1	H21	24
3W16	91	134 2				H22	2
3W16	92	135 2				H22	19
3W16	93	135	9 2+11 FAN OUT TO MEMORY	15W03	93	H22	24
3W16	94	136 2	6 2+12 FAN OUT TO MEMORY	15W03	94	H24	2
3W16	95	137 2	2 2+13 FAN OUT TO MEMORY	15W03	95	H24	19
3W16	96		9 2+14 FAN OUT TO MEMORY	<b>-</b>			24
3W16	97	*******			97		
3W16	98				98		
3W16	99				99		
3W16	900	128 2	6 2+0 FAN OUT TO MEMORY			H19	12
3W16	901	129 2	2 2+1 FAN OUT TO MEMORY			H19	19
3W16	902		9 2+2 FAN OUT TO MEMORY			H19	2
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13W17	94	136 28	2+12 FAN OUT TO MEMORY	16W03 94 H06 2	
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13W17	903	130 28	2+3 FAN OUT TO MEMORY	16W03 903 H02 2	* -
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94 95 96 97 98	G41 18 G42 11 G42 14 G42 17	2+12 ADDRESS TO MEMORY 2+13 ADDRESS TO MEMORY 2+14 ADDRESS TO MEMORY 2+15 ADDRESS TO MEMORY	5W32 5W32 5W32 5W32	94 95 96 97	R41 R41 R42	7 3 18 7
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97 98	G42 17	2+14 ADDRESS TO MEMORY 2+15 ADDRESS TO MEMORY	5W32	97	R42	7
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			5w32	99		. <b></b>
0.0	G35 12	CLOCK	5w32	900	M32	4
01	G39 18	2+0 BANK SELECT	5W32	901	040	7
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03						18
04		2+3 CHASSIS 43 SELECT (100)				7
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0000	3 4 5	G38 9 4 G38 4 5 G38 13 6 G40 11 7 G40 14	3 G38 9 2+2 CHASSIS 13 SELECT (100) 4 G38 4 2+3 CHASSIS 13 SELECT (100) 5 G38 13 2+4 CHASSIS 13 SELECT (100) 6 G40 11 2+5 Address to memory 7 G40 14 2+6 Address to memory	3 G38 9 2+2 CHASSIS 13 SELECT (100) 5W32 4 G38 4 2+3 CHASSIS 13 SELECT (100) 5W32 5 G38 13 2+4 CHASSIS 13 SELECT (100) 5W32 6 G40 11 2+5 ADDRESS TO MEMORY 5W32 7 G40 14 2+6 ADDRESS TO MEMORY 5W32	3 G38 9 2+2 CHASSIS 13 SELECT (100) 5W32 903 4 G38 4 2+3 CHASSIS 13 SELECT (100) 5W32 904 5 G38 13 2+4 CHASSIS 13 SELECT (100) 5W32 905 16 G40 11 2+5 ADDRESS TO MEMORY 5W32 906 17 G40 14 2+6 ADDRESS TO MEMORY 5W32 907	3   G38   9   2+2   CHASSIS   13   SELECT (100)   5   32   903   G40   4   G38   4   2+3   CHASSIS   13   SELECT (100)   5   32   904   G41   G40   11   2+5   ADDRESS   TO MEMORY   5   32   905   G41   G40   14   2+6   ADDRESS   TO MEMORY   5   5   907   G42   907   G42   907   G42   907   G42   907   G42   907   G42   907   G42   907   G42   907   G42   907   G42   907   G42   907   G42   907   G42   907   G42   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   907   9

13W34	00			626 SYNC TO CHANNEL (INPUT)
13W34	90	J35	7	2+0 INPUT DÄTA
13W34	91	J35	- 5	2+1 INPUT DATA
13W34	92	J35	3	2+2 ÎNPŮŤ DÁŤA
13W34	93	J35	28	2+3 INPUT DATA
13W34	94	J35	26	∴2+4°ÎNPŮŤ DAŤA
13W34	95	J35	24	2+5 INPUT DATA
13W34	96	J37	7	2+6 INPUT DATA
13W34	97	J37	-5	2+7 INPUT DATA
13W34	98	J37	3	2+8 ÎNPUT DATA
13W34	99	J37	28	2+9 INPUT DATA
13W34	900	J37	26	2+în înput dâta
13W34	901	J37	24	2+11 INPUT DATA
13W34	902	<b>J33</b>	28	ACTIVE
13W34	903	J33	3	INACTIVE
13W34	904	J33	7	PULL
13W34	905	J33	5	ENPTY
13W34	906	J31	13	
13W34	907	J33	9	CLOCK (1 MS)
13434	908		•	

13W35	0.0			626 SYNC TO CHANNEL (OUTPUT)	
13W35	90	J40	3	2+0 OUTPUT DATA	
13W35	91	J40	13	2+1 OUTPUT DATA	•
13W35	92	J40	16	2+2 OUTPUT DATA	
13W35	93	J40	26	2+3 OUTPUT DATA	-
13W35	94	J41	3	2+4 OUTPUT DATA	
13W35	95	J41	13	2+5 OUTPUT DATA	
13W35	96	J41	16	2+6 OUTPUT DATA	
13W35	97	J41	26	2+7 OUTPUT DATA	
13W35	98	J42	3	248 OUTPUT DATA	
13W35	99	J42	13	2+9 OUTPUT DATA	
13W35	900	J42	16	2+10 OUTPUT DATA	
13W35	901	J42	26	2+11 OUTPUT DATA	
13W35	902	<u> 138</u>	13	ACTIVE	
13W35	903	J38	16	INACTIVE	
13W35	904	J38	26	FULL	
13W35	905	139	3	EMPTY	
13W35	906	<b>J38</b>	3	FUNCTION	
13W35	907	J39	13	MASTER CLEAR	
13W35	908				
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 13W36	0.0			626 SYNC PASSON (INPUT)
13W36	90	J35	23	2+0 INPUT DATA
 13W36	91	J35	22	2+1 INPUT DATA
 13W36	92	J35	21	2+2 INPUT DATA
13W36	93	J35	10	2+3 INPUT DATA
13W36	94	J35	9	2+4 INPUT DATA
13W36	95	J35	8	2#5 ÎNPÛT DÂTA
13W36	96	<b>J37</b>	23	2+6 INPUT DATA
13W36	97	J37	22	2+7 INPUT DATA
 13W36	98	J37	21	2+8 ÎNPUT DATA
13W36	99	J37	10	2+9 INPUT DATA
13W36	900	J37	9	2÷10 ÎNPUT DÂTĂ
13W36	901	J37	8	2+11 INPUT DATA
13W36	902	J33	10	ACTIVE
 13W36	903	J33	21	INACTIVE
13W36	904	J33	23	rFullC-
13W36	905	J33	22	EMPTY
13W36	906	J31	15	
13W36	907	J31	1	CLOCK (1 M SEC)
13W36	908		_	
F NAME OF STREET	e contra e a <del>Managar</del> ange	N. HOLIN PROPERTY AND ADDRESS.	Marie Charles and a state Street Service Service	

13W37	0.0		*** *** *** ***	626 SYNC PASSON (OUTPUT)
13W37	90	J40	1	2+0 OUTPUT DATA
13W37	91	J40	15	2+1 OUTPUT DATA
13W37	92	J40	14	2+2 OUTPUT DATA
13W37	93	J40	28	2+3 OUTPUT DATA
13W37	94	J41	1	2+4 OUTPUT DATA
13W37	95	J41	15	2+5 OUTPUT DATA
13W37	96	J41	14	2+6 OUTPUT DATA
13W37	97	J41	28	2+7 OUTPUT DATA
13W37	98	J42	1	2+8 QUIPUT DATA
13W37	99	J42	15	249 DUTPUT DATA
13W37	900	J42	14	2±10 OUTPUT DATA
13W37	901	J42	28	2+11 OUTPUT DATA
13W37	902	J38	15	ACTIVE
13W37	903	J38	14	INACTIVE
13W37	904	138	28	FULL
13W37	905	139	1	EMPTY
13W37	906	J38	1	FUNCTION
13W37	907	<b>J</b> 39	15	MASTER CLEAR
13W37	908	The state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the s	maken kernapan sa	

13W39	00		WRITE DISTRIBUTOR TO MEMORY	13W39	00	
13W39	90	133 26	2+8 FAN OUT TO MEMORY	13W39	90	H21 24
13W39	91	134 22	249 FAN OUT TO MEMORY	13W39	91	H22 2
13W39	92	134 9	2+10 FAN OUT TO MEMORY	13W39	92	H22 19
13W39	93	135 26	2+11 FAN OUT TO MEMORY	13W39	93	H22 24
13W39	94	136 22	2+12 FAN OUT TO MEMORY	13W39	94	H24 2
13W39	95	136 9	2+13 FAN OUT TO MEMORY	13W39	95	H24 19
13W39	96	137 26	2+14 FAN OUT TO MEMORY	13W39	96	H24 24
13W39	97		Course The Art Course The Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Course Cour	13W39	97	
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13W39	900	128 22	2+0 FAN OUT TO MEMORY	13W39	900	H19 2
13439	901	128 9	2+1 FAN OUT TO MEMORY	13W39	901	H19 19
13W39	902	129 26	2+2 FAN OUT TO MEMORY	13W39	902	H19 24
13W39	903	130 22	2+3 FAN DUT TO MEMORY	13w39	903	H20 2
13W39	904	130 9	2+4 FAN OUT TO MEMORY	13W39	904	H20 19
13W39	905	131 26	2+5 FAN OUT TO MEMORY	13W39	905	H20 24
13W39	906	132 22	2+6 FAN OUT TO MEMORY	13439	906	H21 2
13W39	907	132 9	2+7 FAN OUT TO MEMORY	13W39	907	H21 19
13W39	908		was chief and the tractions	13439	908	1164 47

13W39	0.0		MEMORY FROM WRITE DISTRIBUTOR	13W39	00.	
13W39	90	H21 24	2+8 FAN OUT TO MEMORY	13W39	90	133 26
13W39	91	H22 2	2+9 FAN OUT TO MEMORY	13W39	91	134 22
13W39	92	H22 19	2+10 FAN OUT TO MEMORY	13W39	92	134 9
13W39	93	H22 24	2+11 FAN OUT TO MEMORY	13439	93	135 26
13W39	94	H24 2	2+12 FAN OUT TO MEMORY	13W39	94	136 22
13W39	95	H24 19	2+13 FAN OUT TO MEMORY	13W39	95	136 9
13W39	96	H24 24	2+14 FAN OUT TO MEMORY	13439	96	137 26
13W39	97		Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Section 1990 Secti	13439	97	
13W39	98			13439	98	
13W39	99			13439	99	
13w39	900	H19 2	2+0 FAN OUT TO MEMORY	13W39	900	128 22
13W39	901	H19 19	2+1 FAN OUT TO MEMORY	13839	901	128 9
13W39	902	H19 24	2+2 FAN OUT TO MEMORY	13W39	902	129 26
13W39	903	H20 2	2+3 FAN OUT TO MEMORY	13W39	903	130 22
13W39	904	H20 19	2+4 FAN OUT TO MEMORY	13W39	904	130 9
13W39	905	H20 24	2.5 FAN OUT TO MEMORY	13W39	905	131 26
13W39	906	H21 2	2+6 FAN OUT TO MEMORY	13839	906	132 22
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13W39	908			13W39	908	

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14W03	0.0		MEMORY FROM WRITE DISTRIBUTOR	13W11	00	
14W03	90	H03 24	2+8 FAN OUT TO MEMORY	13w11	90	133 11
14W03	91	H04 2	2+9 FAN OUT TO MEMORY	13W11	91	134 5
14W03	92	H04 19	2+10 FAN OUT TO MEMORY	13W11	92	135 20
14W03	93	H04 24	2+11 FAN OUT TO MEMORY	13W11	93	135 11
14403	9.4	H06 2	2+12 FAN OUT TO MEMORY	13W11	94	136 5
14W03	95	H06 19	2+13 FAN OUT TO MEMORY	13W11	95	137 20
14W03	96	H06 24	2+14 FAN OUT TO MEMORY	13W11	96	137 11
14W03	97			13W11	97	
14003	98			13W11	98	
14W03	99			13W11	99	
14W03	900	H01 2	2+0 FAN OUT TO MEMORY	13W11	900	128 5
14W03	901	H01 19	2+1 FAN OUT TO MEMORY	13W11	901	129 20
14W03	902	H01 24	2+2 FAN OUT TO MEMORY	13W11	902	129 11
14W03	903	H02 2	2+3 FAN OUT TO MEMORY		903	130 5
14W03	904	H02 19	2+4 FAN OUT TO MEMORY	13W11	904	131 20
14003	905	H02 24	2+5 FAN OUT TO MEMORY	13W11	905	131 11
14W03	906	H03 2	2+6 FAN OUT TO MEMORY		906	132 5
14W03	907	H03 19	2+7 FAN OUT TO MEMORY		907	133 20
14W03	908				908	

4W04	0.0			MEMOI	RY F	ROM	WRI'	TE DISTRIBUT	OR	15W16	0.0		
L4W04	90	H17	2	2+39	FAN	ouŤ	TO	MEMORY		15W16	90	134	26
4W04	91	H17	19	2+40	FAN	OUT	TO	MEMORY	and the second second second second	15W16	91	135	22
4W04	92	H17	24	2+41	FAN	OUT	TO	MEMORY		15W16	92	135	9
4W04	93	H18	2	2+42	FAN	OUT	TO	MEMORY	THE PART THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART OF THE PART	15W16	93	136	26
4W04	94	H18	19	2+43	FAN	OUT	TO	MEMORY		15W16	94	137	22
4404	95	H18	24	2+44	FAN	OUT	TO	MEMORY	·	15W16	95	137	9
4004	96									15W16	96		
4W04	97			THE ST PERSON NAMED IN				COLUMN TO THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PART		15W16	97		
4W04	98									15W16	98		
4W04	99									15W16	99		
4W04	900	H13	2	2+30	FAN	OUT	TO	MEMORY		15W16	900	128	26
4W04	901	H13	19	2+31	FAN	OUT	TO	MEMORY	•	15W16	901	129	22
4W04	902	H13	24	2+32	FAN	OUT	TO	MEMORY		15W16	902	129	9
4W04	903	H14	2	2+33	FAN	OUT	TO	MEMORY	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	15W16	903	130	26
4W04	904	H14	19	2+34	FAN	OUT	TO	MEMORY		15W16	904	131	22
4W04	905	H14	24	2+35	FAN	OUT	ŤO	MEMORY		15W16	905	131	9
4W04	906	H16	2	2+36	FAN	OUT	TO	MEMORY		15W16	906	132	26
4W04	907	H16	19	2+37	FAN	out	TO	MEMORY		15W16	907	133	22
4W04	908	H16	24	2+38	FAN	OUT	TO	MEMORY		15W16	908	133	9

14W05	00		MEMORY FROM WRITE DISTRIBUTOR	16W16 00
14W05	90	H23 2	2+94 FAN OUT TO MEMORY	16W16 90 I12 26
14W05	91	H23 19	2455 FAN OUT TO MEMORY	16W16 91 113 22
14W05	92	H23 24	2+56 FAN OUT TO MEMORY	16W16 92 I13 9
14W05	93	H24 2	2+57 FAN OUTSTO MEMORY	16W16 93 I14 26
14W05	94	H24 19	2+58 FAN OUT TO MEMORY	16W16 94 115 22
14W05	95	H24 24	2+59 FAN OUT TO MEMORY	16W16 95 I15 9
4W05	96		·	16W16 96
4W05	97			16W16 97
14W05	98			16W16 98
14W05	99			16W16 99
14W05	900	H19 2	2+45 FAN OUT TO MEMORY	16W16 900 In6 26
14W05	901	H19 19	2+46 FAN OUT TO MEMORY	16W16 901 107 22
14W05	902	H19 24	2+47 FAN OUT TO MEMORY	16W16 902 IO7 9
L4W05	903	H21 2	2+48 FAN OUT TO MEMORY	16W16 903 IO8 26
4W05	904	H21 19	2+49 FAN OUT TO MEMORY	16W16 904 109 22
14W05	905	H21 24	2+50 FAN OUT TO MEMORY	16W16 905 109 9
L4W05	906	H22 2	2+51 FAN OUT TO MEMORY	16W16 906 I10 26
L4W05	907	H22 19	2+52 FAN DUT TO MEMORY	16W16 907 111 22
LAWD5	908	H22 24	2+53 FAN OUT TO MEMORY	16W16 908 I11 9

14W06	0.0			MEMORY TO READ DISTRIBUTOR	3W10	00		184 Aug. Was 1844
14W06	90	H03	28	2+8	3W10	90	135	19
14W06	91	H04	1	2+9	3W10	91	136	19
14W06	92	H04	.8	2+10	3W10	92	138	19
14W06	93	H04	28	2+11	3W10	93	139	19
4W06	94	H06	1	2+12	3W10	94	140	19
14W06	95	H06	8	2.13	3W10	95	141	19
14W06	96	H06	28	2+14	3W10	96	142	
4W06	97	THE REAL PROPERTY.		THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY O	3W10	97		· · ·
4W06	98				3W10	98		
4W06	99				3W10	99		
L4W06	900	HO1	1	2+0	3W10	900	125	19
4W06	901	H01	8	2+1	3W10	901	126	19
4W06	902	HO1	28	2+2	3W10	902	127	19
4006	903	H02	1		3W10	903	128	19
4W06	904	H02	8	2+4	3w10	904	129	19
14W06	905	H02	28	2+5	3W10	905	132	
14W06	906	H03	1	2+6	3W10	906	133	
L4W06	907	H03	8	2.7	3W10	907	134	
L4W06	908		•		3w10	908		- 7

14W07	00			MEMORY TO READ DISTRIBUTOR	4W16	00	en delle delle ridle delle delle delle delle ridle delle delle delle delle delle delle delle delle delle delle
14W07	90	H09	28	2+23	4W16	90	I11 19
14W07	91	H11	1	2+24	-4W16	91	112 19
14W07	92	H11	8	2 * 25	4W16	92	114 19
14W07	93	H11	28	2+26	4W16	93	115 19
14W07	94	H12	1	2+27	4W16	94	116 19
14W07	95	H12	8	2+28	4W16	95	117 19
14W07	96	H12	28	2+29	4W16	96	118 19
14W07	97	G 0 4	21	GO MEMORY	4W16	97	121 26
14W07	98	G 0 4	20	WRÍTE MEMORŸ	4W16	98	124 20
14W07	99				4W16	99	
14W07	900	H <sub>0</sub> 7	1	2+15	4W16	900	101 19
14W07	901	HO7	8	2+16	4W16	901	102 19
14W07	902	H07	28	2+17	4W16	902	103 19
14W07	903	H08	1	2+18	4W16	903	104 19
14W07	904	H08	- 8	2+19	4W16	904	105 19
14W07	905	HOB	28	2+20	4W16	905	108 19
14W07	906	H09	1	2#21	4W16	906	109 19
14W07	907	H09	8	2+22	4W16	907	110 19
14W07	908	•	_	<del></del>	4W16	908	

14W08	00			MEMORY TO READ DISTRIBUTOR	9W17	00	
14W08	90	H16	28	2+38	9W17	90	135 19
14W08	91	H17	1	2+39	9W17	91	136 19
14W08	92	H17	8	2+40	9W17	92	138 19
14W08	93	H17	28	2+41	9W17	93	139 19
14W08	94	H18	1	2+42	9W17	94	140 19
L4WD8	95	H18	B	2.43	9W17	95	141 19
14W08	96	H18	28	2+44	9W17	96	142 19
4408	97		=	and provided the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the cont	9W17	97	
L4WDB	98				9W17	98	
L4W08	99			THE RESIDENCE OF THE PARTICULAR PARTICULAR PROPERTY OF THE PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICULAR PARTICU	9W17	59	
4408	900	H13	4	2+30	9W17	900	125 19
4008	901	H13	8	2+31	9817	901	126 19
4W08	902	H13	28	2+32	9W17	902	127 19
4008	903	H14		233	9W17	903	128 19
4408	904	H14	ā	2+34	9W17	904	129 19
4408	905	H14	28	2+35	9w17	905	132 19
4W08	906	H16	2.0	2+36	9W17	906	133 19
4408	907	H16	9	2+37	9W17	967	134 19
L4W08	908	M10	0	E WO /	9W17	908	104 19
T-MOO	400			AND TO AND THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE	741/	700	as thereas in the

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14409	00			MEMORY TO READ DISTRIBUTOR	10W17	0.0	en den meg den men den den men bisk som dag den ser mit spirk bild kan de Abbeton. 1888 i 7 i 22 million
4W09	90	H22	28	2+53	10W17	90	I11 19
14W09	91	H23	1	2+54	10W17	91	112 19
14W09	92	H23	A	2+55	10W17	92	I14-19
14W09	93	H23	28	2*56	10W17	93	115 19
14W09	94	H24	1	2+57	10W17	94	116-19
4409	95	H24	8	2+58	10W17	95	117 19
4409	96	H24	28	2+59	10W17	96	118:19
4409	97				10W17	97	The state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the s
14W09	98			<i>*</i>	10W17	98	
4W09	99			AND THE REAL PROPERTY AND AND THE REAL PROPERTY AND AND AND AND AND AND AND AND AND AND	10W17	99	THE REAL PROPERTY AND THE THE THE PROPERTY AND THE SET OF THE THE THE THE THE THE THE THE THE THE
4409	900	H19	1	2+45	10W17	900	101:19
4009	901	H19	8	2 • 46	10W17	901	102 19
4409	902	H19	28	2+47	10W17	902	103 19
L4W09	903	H21	1	2+48	10W17	903	104 19
L4W09	904	H21	8	2+49	10W17	904	105 19
4009	905	H21	28	2+50	10W17	905	108 19
4009	906	H22	4	2+51	10W17	906	109 19
4409	907	H22		2+52	10W17	907	110 19
4009	908		•		10W17	908	~ ma ~ mg r

4W10	00			DISTRIBUTOR FROM WRITE BUFFER	2W06	0.0		al engine
4W10	90	102	4	2+15 DATA TO MEMORY FAN OUT	2W06	90	B14	27
4W10	91	102	6	2+16 DATA TO MEMORY FAN OUT	2W06	91	B14	25
4W10	92	102	7	2+17 DATA TO MEMORY FAN OUT	2W06	92	B14	23
4W10	93	102	27	2+18 DATA TO MEMORY FAN OUT	2W06	93	815	8
4W10	94	102	25	2+19 DATA TO MAMORY FAN OUT	2W06	94	815	6
4W10	95	102	22	2+20 DATA TO MEMORY FAN OUT	2W06	95	B15	4
4W10	96	103	4	2+21 DATA TO MEMORY FAN OUT	2W06	96	B15	27
4W10	97	103	6	2+22 DATA TO MEMORY FAN OUT	2W06	97	815	25
4W10	98	103	7	2+23 DATA TO MEMORY FAN OUT	2W06	98	B15	23
4W10	99	103	27	2+24 DATA TO MEMORY FAN OUT	2W06	99	B16	8
4W10	900	103	25	2+25 DATA TO MEMORY FAN OUT	2006	900	B16	~ 6
4W10	901	103	22	2+26 DATA TO MEMORY FAN OUT	2W06	901	B16	4
4W10	902	I 0 4	4	2+27 DATA TO MEMORY FAN OUT	2W06	902	816	27
4W10	903	104	6	2+28 DATA TO MEMORY FAN OUT	5M06	903	B16	25
4W10	904	104	7	2+29 DATA TO MEMORY FAN OUT	2W06	904	816	23
4W10	905	C14	14	MEMORY MARGIN	2W06	905	822	20
4W10	906				2W06	906		-
4W10	907			The same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the sa	2W06	907		The second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second secon
4W10	908				2W06	908		

14W11	00		WRITE DISTRIBUTOR TO MEMORY	13W03	0 0
14W11	90	I11 3	2+23 FAN OUT TO MEMORY	13W03	90 H27 24
14W11	91	112 18	2+24 FAN DUT TO MEMORY	13403	91 H29 2
14011	92	112 13	2425 FAN OUT TO MEMORY		92 H29 19
14W11	93	113 3	2+26 FAN OUT TO MEMORY		93 H29 24
14W11	94	114 18	2+27 FAN OUT TO MEMORY		94 H30 2
14W11	95	114-13	2+28 FAN OUT TO MEMORY		95 H30 19
14W11	96	115 3	2+29 FAN OUT TO MEMORY		96 H30 24
14W11	97				97
14W11	98				98
14W11	99				99
14W11	900	106 18	2+15 FAN OUT TO MEMORY		00 H25 2
	901	106 13	2+16 FAN OUT TO MEMORY		01 H25 19
14411	902	107 3	2+17 FAN OUT TO MEMORY		02 H25 24
	903	108 18	2+18 FAN OUT TO MEMORY		03 H26 2
14W11	904	108 13	2+19 FAN OUT TO MEMORY		04 H26 19
	905	109 3	2+20 FAN OUT TO MEMORY		05 H26 24
14W11	906	110 18	2421 FAN OUT TO MEMORY		06 H27 2
14W11	907	110 13	2+22 FAN OUT TO MEMORY		07 H27 19
14W11	908				0.8

14W12	00		WRITE DISTRIBUTOR TO MEMORY	4W04 0	0
14W12	90	I11 5	2+23 FAN OUT TO MEMORY	4W04 9	0 H09 24
14W12	91	112 20	2+24 FAN OUT TO MEMORY	4404 9	1 H11 2
14W12	92	112 11	2+25 FAN OUT TO MEMORY	4W04 9	2 H11 19
14W12	93	113 5	2+26 FAN OUT TO MEMORY	4W04 9	3 H11 24
14W12	94	114 20	2+27 FAN OUT TO MEMORY	4W04 9	4 H12 2
14W12	95	114 11	. 2+28 FAN OUT TO MEMORY	4W04 9	5 H12 19
14W12	96	115 5	2 29 FAN OUT TO MEMORY	4W04 9	6 H12 24
14W12	97		,	4W04 9	7
14W12	98			4W04 9	8
14W12	99			4W04 9	9
14W12	900	106 20	2+15 FAN OUT TO MEMORY	4W04 90	0 H07 2
14W12	901	106 11	2+16 FAN OUT TO MEMORY	4W04 90	1 H07 19
14W12	902	107 5		4W04 90	2 H07 24
14W12	903	108 20	2+18 FAN OUT TO MEMORY	4W04 90	3 H08 2
14W12	904	108 11	2+19 FÄN OUT TO MEMORY	404 90	4 H08 19
14W12	905	109 5	2+20 FAN OUT TO MEMORY	4W04 90	5 HO8 24
14W12	906	110 20	2+21 FAN OUT TO MEMORY	4W04 90	6 H09 2
14W12	907	110 11	. 2+22 FAN OUT TO MEMORY	404 90	7 H09 19
14W12	908			4W04 90	8

4W13	00		WRITE DISTRIBUTOR TO MEMORY	3W16	0.0	
4W13	90	I11:28	2+23 FAN OUT TO MEMORY	3W16	90	.H27 :24
L4W13	91	112 24	2424 FAN OUT TO MEMORY	3W16	91	H29 2
4W13	92	112 7	2425 FAN OUT TO MEMORY	3W16	92	H29 19
4W13	93	113 28	2+26 FAN OUT TO MEMORY	3W16	93	H29 24
4413	94	114 24	2+27 FAN OUT TO MEMORY	3W16	94	H30 2
4W13	95	114 7	2+28 FAN OUT TO MEMORY	3W16	95	H30 19
4W13	96	115 28	2+29 FAN OUT TO MEMORY	3W16	96	H30 24
4W13	97	005 28	ACCEPT	3W16	97	123 21
4W13	98	40- 20		3W16	98	
4W13	99			3W16	99	
4W13	900	106-24	2+15 FAN OUT TO MEMORY	3W16	900	H25 2
4W13	901	106 7	2+16 FAN OUT TO MEMORY	3W16	901	H25 19
4W13	902	107 28	2+17 FAN OUT TO MEMORY	3W16	902	H25 24
4W13	903	108 24	2+18 FAN OUT TO MEMORY	3W16	903	H26 2
4W13	904	108 7	2+19 FAN OUT TO MEMORY	3W16	904	H26 19
4W13	905	109 28	2+20 FAN OUT TO MEMORY	3W16	905	H26 24
4W13	906	110 24	2+21 FAN OUT TO MEMORY	3W16	906	H27 2
4W13	907	110 7	2+22 FAN OUT TO MEMORY	3W16	907	H27 19
4W13	908	/	लालककका र त्यार कर्मा र प्याप्त र राज्य र राज्य पिकारी है। र	3W16	908	

4W14	0.0		WRITE DISTRIBUTOR TO MEMORY	9W04	00		** *** *** ***
4W14	90	I11 1	3 2+23 FAN OÙTETO MEMORY	9W04	90	H27	24
4W14	91	112	3 2+24 FAN OUT TO MEMORY	9W04	91	H29	2
4W14	92	113 1	2#25 FAN OUT TO MEMORY	9W04	92	H29	19
4W14	93	113 1	2 2426 FAN OUT TO MEMORY	9W04	93	H29	24
4W14	94	114	2+27 FÄN OUT TO MEMORY	9W04	94	H30	- 2
4W14	95	115 1	2+28 FAN OUT TO MEMORY	9W04	95	H30	19
4W14	96	115 1	2#29 FÂN OÚT TO MEMORY	9W04	96	H30	2
4W14	97	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,		9404	97		
4W14	98		`	9W04	98		
4W14	99			9404	99		
4W14	900	106	2+15 FAN OUT TO MEMORY	9W04	900	H25	1
4W14	901	107 1	2+16 FAN OUT TO MEMORY	9W04	901	H25	1
4W14	902	107 1	2 × 17 FAN OUT TO MEMORY	9W04	902	H25	2
4W14	903	108	2+18 FAN OUT TO MEMORY	9W04	903	H26	1
4W14	904	109 1	2+19 FAN OUT TO MEMORY	9 W O 4	904	H26	19
4W14	905	109 1	2-20 FAN OUT TO MEMORY	9W04	905	H26	2
4W14	906	110	3 2#21 FAN OUT TO MEMORY	9W04	906	H27	2
4W14	907	111 1	2+22 FAN OUT TO MEMORY	9W04	907	H27	1
4W14	908			9 W 0 4	908		_

14W15	0.0		WRITE DISTRIBUTOR TO MEMORY	10004	0.0	n den entre sette dels arte par protect est action de l'
14W15	90	111 11	2+23 FAN OUT TO MEMORY	10W04	90	H09 24
14W15	91	112 5	2+24 FAN OUT TO MEMORY	10W04	91	H11 2
14W15	92	113 20	2-25 FAN OUT TO MEMORY	10W04	92	H11 19
14W15	93	113 11	2+26 FAN OUT TO MEMORY	10W04	93	H11 24
14W15	94	114 5	2427 FAN OUT TO MEMORY	10W04	94	H12 2
14W15	95	115 20	2+28 FAN OUT TO MEMORY	10W04	95	H12 19
14W15	96	115 11	2+29 FAN OUT TO MEMORY	10004	96	H12 24
14W15	97			10W04	97	
14W15	98			10W04	98	
14W15	99			10W04	99	
14W15	900	106 5	2+15 FAN OUT TO MEMORY	10W04 9	900	H07 2
14W15	901	107 20	2+16 FAN OUT TO MEMORY	10W04	901	H07 19
14W15	902	107 11	2417 FAN OUT TO MEMORY	10W04	902	H07 24
14W15	903	108 5	2-18 FAN OUT TO MEMORY	10W04 9	903	H08 2
14W15	904	109 20	2419 FAN OUT TO MEMORY	10W04 9	904	H08 19
14W15	905	109 11	2+20 FAN OUT TO MEMORY	10W04 9	05	H08 24
14W15	906	110 5	2#21 FAN OUT TO MEMORY	10W04 9	906	H09 2
14W15	907	111 20	2+22 FAN OUT TO MEMORY	10W04 9	07	H09 19
14W15	908			10W04 9	809	

14W16	00		WRITE DISTRIBUTOR TO MEMORY	15004	00	
4W16	90	112 26	2+24 FAN OUT TO MEMORY	15W04	90	H29 2
4W16	91	113 22	2+25 FAN OUT TO MEMORY	15W04	91	H29 19
4W16	92	113 9	2+26 FAN OUT TO MEMORY	15W04	92	H29 24
4W16	93	114 26	2+27 FAN OUT TO MEMORY	15W04	93	H30 2
4W16	94	115 22	2+28 FAN OUT TO MEMORY	15W04	94	H30 19
4W16	95	115 9	2+29 FAN OUT TO MEMORY	15W04	95	H30 24
4W16	96	111 9	2+23 FAN OUT TO MEMORY	15W04	96	H27 24
4W16	97			15W04	97	
4W16	98		•	15W04	98	
4W16	99			15W04	99	
4W16	900	106 26	2+15 FAN OUT TO MEMORY	15W04	900	H25 2
4W16	901	107 22	2416 FAN OUT TO MEMORY	15W04	901	H25 19
4W16	902	107 9	2+17 FAN OUT TO MEMORY	15W04	902	H25 24
4W16	903	108 26	2+18 FAN OUT TO MEMORY	15W04	903	H26 2
4W16	904	109 22	2+19 FAN OUT TO MEMORY	15W04	904	H26 19
4W16	905	109 9	2+20 FAN OUT TO MEMORY	15W04	905	H26 24
4W16	906	110 26	2+21 FAN OUT TO MEMORY	15W04	906	H27 2
4W16	907	111 22	2+22 FAN OUT TO MEMORY	15W04	907	H27 19
4W16	908		The second section of the second section is the second section of the second section is the second section in the second section is the second section in the second section in the second section is the second section in the second section in the second section is the second section in the second section in the second section is the second section in the second section in the second section is the second section in the second section in the second section is the second section in the second section in the second section is the second section in the second section in the second section is the second section in the second section in the second section is the second section in the second section in the second section is the second section in the second section in the second section is the second section in the second section in the second section is the second section in the second section in the second section is the second section in the second section in the second section is the second section in the second section in the second section is the second section in the second section in the second section is the second section in the second section in the second section is the second section in the second section in the second section is the second section in the second section in the second section is the second section in the second section in the second section is the second section in the second section in the second section is the second section in the second section in the second section is the second section in the second section in the section is the second section in the section is the second section in the section is the section in the section in the section is the section in the section in the section in the section is the section in the section in the section in the section is the section in the section in the section in the section is the section in the section in the section in the section is the section in the section in the section in the section is the section in the section in the section in the s	15W04	908	

	0.0		WRITE DISTRIBUTOR TO MEMORY	16W04 00	
14W17	90	I12-28	2+24 FAN OUT TO MEMORY	16W04 90 H11 2	
14W17	91	113 24	2#25 FAN OUT TO MEMORY	16W04 91 H11 19	
14W17	92	113 7	2#26 FAN OUT TO MEMORY	16W04 92 H11 24	
14W17	93	114 28	2+27 FAN OUT TO MEMORY	16W04 93 H12 2	
14W17	94	115 24	2+28 FAN OUT TO MEMORY	16W04 94 H12 19	
14W17	95	115 7	2-29 FAN OUT TO MEMORY	16W04 95 H12 24	
14W17	96			16W04 96	
14W17	97			16W04 97	CALLED THE WORLD
14W17	98			16W04 98	
14W17	99			16W04 99	
14W17	900	106 28	2+15 FAN OUT TO MEMORY	16W04 900 H07 2	
14W17	901	107 24	2+16 FAN OUT TO MEMORY	16W04 901 H07 19	
14W17	902	107 7	2+17 FAN OUT TO MEMORY	16W04 902 H07 24	
14W17	903	108 28	2+18 FAN OUT TO MEMORY	16W04 903 H08 2	
14W17	904	109 24	2+19 FAN OUT TO MEMORY	16W04 904 H08 19	
14W17	905	109 7	2+20 FAN OUT TO MEMORY	16W04 905 H08 24	
14W17	906	I10 28	2-21 FAN OUT TO MEMORY	16W04 906 H09 2	
14W17	907	111 24	2-22 FAN OUT TO MEMORY	16W04 907 H09 19	
14W17	908	111 7	2+23 FAN OUT TO MEMORY	16W04 908 H09 24	

4W18	00		MEMORY ADDRESS FROM CONTROL	5W33	00	
4W18	90	G01 18	2+8 ADDRESS TO MEMORY	5W33	90	Q42 20
4W18	91	G02 11	2+9 ADDRESS TO MEMORY	5W33	91	R40 9
4W18	92	G02 14	2+10 ADDRESS TO MEMORY	5W33	92	R40 5
14W18	93	G02 17	2+11 ADDRESS TO MEMORY	5W33	93	R40 20
4W18	94	G02 18	2+12 ADDRESS TO MEMORY	5w33	94	R41 9
L4W18	95	G03 11	2+13 ADDRESS TO MEMORY	5w33	95	R41 5
4W18	96	G03 14	2-14 ADDRESS TO MEMORY	5W33	96	R41 20
4W18	97	G03 17	2+15 ADDRESS TO MEMORY	5W33	97	R42 9
4W18	98	Gn3 18	2+16 ADDRESS TO MEMORY	5w33	98	R42 5
4W18	99			5w33	99	
4W18	900	G13 12	GLOCK	5W33	900	M32 27
4W18	901	G05 18	2+0 BANK SELECT	5W33	901	Q40 9
4W18	902	005 22	2+1 BANK SELECT	5W33	902	Q40 5
4W18	903	G04 8	2+2 CHASSIS 14 SELECT (101)	5w33	903	040 20
4W18	904	004 4	2+3 CHASSIS 14 SELECT (101)	5W33	904	Q41 9
4W18	905	G04 13	264 CHASSIS 14 SELECT (161)	5W33	905	Q41 5
4W18	906	G01 11	2+5 ADDRESS TO MEMORY	5W33	906	Q41 20
4W18	907	001 17	2+6 ADDRESS TO MEMORY	5W33	907	Q42 5
4W18	908	G01 14	2+7 ADDRESS TO MEMORY	5W33	908	042 9

14W39	0 0			WRITE	ום	STRI	BUŤ	OR TO MEMORY	14W3	0 0		
14W39	90	111	26	2+23	FAN	OUT	TO	MEMORY	14W3	90	H09	24
14W39	91	112	22	2+24	FAN	DUT	ŤO	MEMORY	14W3	9 91	H11	2
14W39	92	112	9	2+25	FAN	OUT	TO	MEMORY	14W3	9 92	H11	19
14439	93	113	26	2+26	FÄN	OUT	TO	MEMORY	14W3	93	H11	24
14W39	94	114	22	2+27	FAN	OUT	TO	MEMORY	14W3	94	H12	2
14439	95	114	- 0	2+28	FAN	out	TO	MEMORY	1443		H12	-
14W39	96	115	26	2+29	FÄN	OUT	To	MEMORY	14W3		H12	_
14439	97		. s.y		1.24			- 1:57 ALEXLAND	14W3			
14W39	98								1443			
14439	99						•	THE P. LEWIS CO., LANSING MICH. AND LOSS CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO., LANSING CO.	14W3			
14W39	900	106	22	2*15	FAN	OUT	TO	MEMORY	14W3		H07	2
14W39	901	106	9		FÂN	OUT	To	MEMORY	1403		HOZ	ī 9
14W39	902	107	26	2+17	FAN	OUT	To	MEMORY	14W3		H07	24
14W39	903	108	22		FAN	OUT	To	MEMORY	14W3	-	HO8	2
14W39	904	108	- 2	2+19	FAN	OUT	To	MEMORY	14W3		H08	19
-	905	109			FÄN	OUT	Ťö	MEMORY	17M3 14W3		HO8	24
14W39		_	26	2+20				MEMORY			-	
14W39	906	110	22	2+21	FAN	OUT	TO	MEMORY	14W3		H09	, 2
14W39	907	110	9	2 + 22	FAN	OUT	TO	MEMUNI	14W3		H 0 9	19
14W39	908							- Commence and the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the co	14W3	908		

14W39	0 0			MEMORY FROM WRITE DISTR	IBUTOR 14W39	0.0	
14W39	90	H09	24	2+23 FAN OUT TO MEMORY	14w39	90	111 26
14W39	91	H11	2	2+24 FAN OUT TO MEMORY	14W39	91	112 22
14W39	92	H11	19	2+25 FAN OUT TO MEMORY	14W39	92	112 9
14W39	93	H11	24	2+26 FAN OUT TO MEMORY	14W39	93	113 26
14W39	94	H12	2	2+27 FAN OUT TO MEMORY	14W39	94	114 22
14W39	95	H12	19	2+28 FAN OUT TO MEMORY	14W39	95	114 9
14W39	96	H12	24	2+29 FAN OUT TO MEMORY	14W39	96	115 26
14W39	97	F			14439	97	
14W39	98			,	14W39	98	
14W39	99				14W39	99	
14W39	900	H07	2	2+15 FAN OUT TO MEMORY	14W39	900	106 22
14W39	901		19	2+16 FAN OUT TO MEMORY	14W39	901	106 9
14W39	902		24	2+17 FAN OUT TO MEMORY	14W39	902	107 26
14W39	903	The second of the second	2	2+18 FAN OUT TO MEMORY	14W39	903	108 22
14W39	904		19	2+19 FAN OUT TO MEMORY	14W39	904	108 9
14W39	905		24	2+20 FAN OUT TO MEMORY	14W39	905	109 26
14W39	906	H09	2	2+21 FAN OUT TO MEMORY	14W39	906	110 22
14439	907		19	2+22 FAN OUT TO MEMORY	14W39	907	I10 9
14439	908			, , , , , , , , , , , , , , , , , , , ,	14W39	908	

15W03	0 0		MEMORY FROM WRITE DISTRIBUTOR	13W16	0 0
17400			MENUAL LUAN MUTIE DIGILITA	10410	* *
15W03	90	H21:24	2+8 FAN OUT TO MEMORY	13W16	90 133
15W03	91	H22 2	2+9 FAN OUT TO MEMORY	13W16	134 2
15W03	92	H22 19	2+10 FÃN ĐƯỢ TO MEMORY		92 135 2
15W03	93	H22 24	2+11 FAN OUT TO MEMORY	13W16	3 135
15W03	94	H24 2	2+12 FAN OUT TO MEMORY		136 2
15W03	95	H24 19	2+13 FAN OUT TO MEMORY		5 137 2
15W03	96	H24 24	2+14 FAN OUT TO MEMORY		96 137
15W03	97				7
19W03	98				8
15W03	99				9
15W03	900	H19 2	2+0 FAN OUT TO MEMORY		00 128 20
15W03	901	H19 19	2+1 FAN OUT TO MEMORY	er er er er er er er er er er er er er e	1 129 2
15W03	902	H19 24	2+2 FAN OUT TO MEMORY	13W16 9	
15W03	903	H20 2	2+3 FAN OUT TO MEMORY	13W16 9	
15W03	904	H20 19	2+4 FAN OUT TO MEMORY	13W16 9	
15W03	905	H20 24	2+5 FAN OUT TO MEMORY	13W16 9	
15W03	906	H21 2	2+6 FAN OUT TO MEMORY	13W16 9	
15W03	907	H21 19	2+7 FAN OUT TO MEMORY	13W16 9	
15W03	908			13W16 9	

15W04	0 0			MEMORY FROM WRITE DISTRIBUTOR	14W16	00		
5w04	90	H29	2	2+24 FAN OUT TO MEMORY	14W16	90	112	26
5W04	91	H29	19	2+25 FAN OUT TO MEMORY	14W16	91	113	22
5W04	92	H29	24	2+26 FAN OUT TO MEMORY	14W16	92	113	9
5W04	93	H30	2	2+27 FAN OUT TO MEMORY	14W16	93	114	26
5W04	94	H30	19	2+28 FAN OUT TO MEMORY	14W16	94	115	22
5W04	95	H30	24	2+29 FAN OUT TO MEMORY	14W16	95	115	9
5W04	96	H27	24	2+23 FAN OUT TO MEMORY	14W16	96	111	9
5W04	97				14W16	97		m
5W04	98				14W16	98		
5W04	99				14W16	99		
5 W Q 4	900	H25	2	2+15 FAN OUT TO MEMORY	14W16	900	106	26
5W04	901	H25	19	2+16 FAN OUT TO MEMORY	14W16	901	107	22
5 W 0 4	902	H25	24	2+17 FAN OUT TO MEMORY	14W16	902	107	9
5 W 0 4	903	H26	2	2+18 FAN OUT TO MEMORY	14W16	903	108	26
5W04	904	H26	19	2+19 FAN OUT TO MEMORY	14W16	904	109	22
5W04	905	H26	24	2+20 FAN OUT TO MEMORY	14W16	905	109	9
5W04	906	H27	_ 2	2+21 FAN OUT TO MEMORY	14W16	906	110	26
5w04	907	H27	19	2+22 FAN OUT TO MEMORY		907		22
5W04	908					908		

15W05	00		MEMORY FROM WRITE DISTRIBUTOR	16W17 00	The sales made of the sales sales sales sales and sales sales and and another sales sales are sales and
15W05	90	H41 2	2+54 FANGOUT TO MEMORY	16W17 90 I	12 28
15W05	91	H41 19	2#55 FAN OUT TO MEMORY	16W17 91 I	13 24
15W05	92	H41 24	2456 FAN OUT TO MEMORY	16W17 92 I	13 7
15W05	93	H42 2	2+57 FAN OUT TO MEMORY	16W17 93 I	14 28
15W05	94	H42 19	2+58 FAN OUT TO MEMORY	16W17 94 I	15 24
15W05	95	H42 24	2059 FAN OUT TO MEMORY		15 7
15W05	96	H40 24		#	11 7
15W05	97			16W17 97	**
15W05	98			16W17 98	
15W05	99			16W17 99	
15W05	900	H37 2	2+45 FAN OUT TO MEMORY		06 28
15W05	901	H37 19	2446 FAN OUT TO MEMORY		07 24
15W05	902	H37 24	2+47 FAN OUT TO MEMORY		07 7
15W05	903	H39 2	2+48 FAN OUT TO MEMORY		08 28
15W05		H39 19	그 앞에 살림이 되워 한 사람들이 가면 아니도 무슨 물건.		09 24
	904	H39 24			
15W05	905				
15W05	906	H40 2	2+51 FAN OUT TO MEMORY		10 28
15W05	907	H40 19	:2452 FAN OUT TO MEMORY		11-24
15W05	908			16W17 908	

15W06	00			MEMORY TO READ DISTRIBUTOR	3W11	0.0	an film was supported from the print, the same with some pass was some some some was some some one one
15W06	90	H21	28	2+8	3W11	90	135-21-
15W06	91	H22	1	`2 <b>+</b> 9	3W11	91	136 21
15W06	92	H22	8	2+10	3W11	92	138 21
15W06	93	H22	28	2+11	3W11	93	139 21
15W06	94	H24	1	2+12	3W11	94	140 21
15W06	95	H24	8	2+13	3W11	95	141 21
15W06	96	H24	28	·2+14·	3W11	96	142 21
15W06	97				3W11	97	
15W06	98			•	3W11	98	
15W06	99				3W11	99	
15W06	900	H19	• 1	<sup>2</sup> 2 <b>•</b> 0	3w11	900	125 21
15W06	901	H19	8	2+1	3W11	901	126 21
15W06	902	H19	28	2+2	3W11	902	127 21
5W06	903	H20	1	2.3	3W11	903	128 21
15W06	904	H20	8	2+4	3w11	904	129 21
15W06	905	H20	28	72.5	3w11	905	132 21
5W06	906	H21	1	2*6	3W11	906	133 21
5W06	907	H21		-2.7	3W11	907	134 21
15W06	908		•		3W11	908	

15W07	0.0		MEMORY TO READ DISTRIBUTOR	4W17	0.0	and the second second second second second second second second second second second second second second seco
15W07	90	H27 28	2+23	4W17	90	111 21
15W07	91	H29 1	2+24	4W17	91	112 21
15W07	92	H29 8	2+25	4W17	92	114 21
15W07	93	H29 28	2+26	4W17	93	115 21
15W07	94	H30 1	2+27	4W17	94	116 21
15W07	95	H30 8	2+28	4W17	95	117 21
15W07	96	H30 28	2+29	4W17	96	I18 21
15W07	97	G38 21	90	4W17	97	121 5
15W07	98	G38 20	WRITE	4W17	98	124 22
15W07	99			4W17	99	
15W07	900	H25 1	2+15	4W17	900	101 21
19W07	901	H25 8	2+16	4W17	901	102 21
15W07	902	H25 28	2+17	-4W17	902	103 21
15W07	903	H26 1	2+18	4W17	903	104 21
15W07	904	H26 8	2+19	4W17	904	105 21
15W07	905	H26 28	2+20	4W17	905	108 21
15W07	906	H27 1	2*21	4W17	906	109 21
15W07	907	H27 8	2+22	4W17	907	110 21
15W07	908			4W17	908	nathagaghalaidh a canag artagan a gaith ann agus t-tainn 1974 (1977).

15W08	0.0			MEMORY TO READ DISTRIBUTOR	9W18	00	er den propi de desemble open propi gang nasa sara sa
15W08	90	H34	28	2+38	9W18	90	135 21
15W08	91	H35	1	2+39	9W18	91	136 21
15W08	92	H35	8	2+40	9W18	92	138 21
5W08	93	H35	28	2+41	9W18	93	139 21
15W08	94	H36	1	2+42	9W18	94	140 21
15W08	95	H36	8	2+43	9W18	95	141 21
5W08	96	H36	28	2+44	9W18	96	142 21
5W08	97	and the same		THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE PARTY OF THE P	9W18	97	
5w08	98			•	9W18	98	
LSW08	99				9W18	99	
5W08	900	H31	1	2+30	9W18	900	125 21
5w08	901	H31	8	2+31	9W18	901	126 21
5W08	902	H31	28	2+32	9W18	902	127 21
5W08	903	H32	1	2+33	9W18	903	128 21
15W08	904	H32	8	2+34	9W18	904	129 21
SWOB	905	H32	28	2+35	9W18	905	132 21
5W08	906	H34	1	2+36	9W18	906	133 21
15W08	907	H34	8	2+37	9W18	907	134 21
5W08	908		•	,	9W18	908	

15W09	00		MEMORY TO READ DISTRIBUTOR	10W18 00
15W09	90	H40 25	2+53	10W18 90 I11 21
15W09	91	H41 1	2.54	10W18 91 I12 21
15W09	92	H41 8	2+55	10W18 92 114 21
15W09	93	H41 28		10W18 93 115 21
15W19	94	H42 1	2 - 57	10W18 94 116 21
15W09	95	H42 8	2+58	10W18 95 117 21
15W09	96	H42 28		10W18 96 I18 21
15W09	97			10W18 97
15W09	98			10W18 98
15W09	99			10W18 99
15W09	900	H37 1	2+45	10W18 900 T01 21
15W09	901	H37		10W18 901 102 21
15W09	902	H37 28	_ ^ #	10W18 902 103 21
15W09	903	H39 1	2+48	10W18 903 104 21
15W09	904	H39	2.49	10W18 904 105 21
15W09	905	H39 28		10W18 905 108 21
15W09	906	H40 1	2+51	10W18 906 109 21
15W09	907	H40 8	2+52	10W18 907 I10 21
15W09	908			10W18 908

5W10	00		DISTRIBUTOR FROM WRITE BUFFER	2W07 :00
5W10	90	139 4	2+30 DATA TO MEMORY FAN OUT	2W07 90 B17 8
5W10	91	139 6	2+31 DATA TO MEMORY FAN OUT	2W07 91 817 6
W10	92	139 7	2+32 DATA TO MEMORY FAN OUT	2W07 92 817 4
W10	93	139 27	2+33 DATA TO MEMORY FAN OUT	2W07 93 B17 27
W10	94	139 25	2+34 DATA TO MEMORY FAN OUT	2W07 94 817 25
W10	95	139 22	2+35 DATA TO MEMORY FAN OUT	2W07 95 B17 23
W10	96	140 4	2+36 DATA TO MEMORY FAN OUT	2W07 96 B18 8
W10	97	140 6	2#37 DATA TO MEMORY FAN OUT	2W07 97 B18 6
W10	98	140 7	2+38 DATA TO MEMORY FAN OUT	2W07 98 B18 4
W10	99	140 27	2+39 DATA TO MEMORY FAN OUT	2W07 99 B18 27
W10	900	140 25	2+40 DATA TO MEMORY FAN OUT	2W07 900 B18 25
W10	901	140 22	2+41 DATA TO MEMORY PAN OUT	2W07 901 B18 23
W10	902	141 4	2+42 DATA TO MEMORY PAN OUT	2W07 902 B19 8
W10	903	141 6	2+43 DATA TO MEMORY FAN OUT	2W07 903 B19 6
W10	904	141 7	2+44 DATA TO MEMORY FAN OUT	2W07 904 B19 4
W10	905	C14 14	MEMORY MARGIN	2W07 905 822 18
W10	906			2w07 906
W10	907			2W07 907
W10	908		•	2W07 908

15W11	00		WRITE DISTRIBUTOR TO MEMORY	13w04 00
5W11	90	133 3	2+38 FAN OUT TO MEMORY	13W04 90 H34 24
15W11	91	134 18	2+39 FAN OUT TO MEMORY	13W04 91 H35 2
3W11	92	134 13	2+40 FAN OUT TO MEMORY	13W04 92 H35 19
15W11	93	135 3	2+41 FAN OUT TO MEMORY	13W04 93 H35 24
5W11	94	136 18	2+42 FAN OUT TO MEMORY	13W04 94 H36 2
L5W11	95	136 13	2+43 FAN OUT TO MEMORY	13W04 95 H36 19
5W11	96	137 3	2+44 FAN OUT TO MEMORY	13W04 96 H36 24
.5W11	97			13W04 97
5W11	98			<u> 13W04 98</u>
5W11	99			13W04 99
5W11	900	128 18	2+30 FAN OUT TO MEMORY	13W04 900 H31 2
5W11	901	128 13	2#31 FAN OUT TO MEMORY	13W04 901 H31 19
5W11	902	129 3	2+32 FAN OUT TO MEMORY	13W04 902 H31 24
5W11	903	130 18	2+33 FAN OUT TO MEMORY	13W04 903 H32 2
5W11	904	130 13	2+34 FAN OUT TO MEMORY	13W04 904 H32 19
5W11	905	131 3	2+35 FAN OUT TO MEMORY	13W04 905 H32 24
5W11	906	132 18	2+36 FAN OUT TO MEMORY	13W04 906 H34 2
5W11	907	132 13	2+37 FAN OUT TO MEMORY	13W04 907 H34 19
5W11	908			13W04 908

5W12	00			WRIT	E DI	STRI	BUŤ	OR TO MEMORY	4W09	0.0	non that sopic them sold how on a	
5W12	90	133	5	2+38	FAN	OUT	TO	MEMORY	-4W09	90	H16	24
5W12	91	134	20	2+39	FAN	OUT	TO	MEMORY	4W09	91	H17	2
5W12	92	134	11	2+40			TO	MEMORY	4W09	92	H17	
5W12	93	135	5	2+41	FAN	OUT	TO	MEMORY	4W09	93	H17	
5W12	94	136	20	2+42	FAN	OUT	TO	MEMORY	4W09	94	H18	2
5W12	95	136	11	2+43	FAN	OUT	TO	MEMORY	4W09	95	H18	19
5W12	96	137	5	2+44	FAN	DUT	TO	MEMORY	4W09	96	H18	24
5W12	97							The second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second secon	4409	97		a and a second
5W12	98								4W09	98		
5W12	99								4W09	99		
5W12	900	128	20	2+30	FAN	OUT	TO	MEMORY	4W09	900	H13	2
5W12	901	128	11	2+31	FAN	OUT	TO	MEMORY	4W09	901	H13	19
5W12	902	129	5	2+32	FAN	OUT	TO	MEMORY	4W09	902	H13	24
5W12	903	130	20	2+33	FAN	OUT	TO	MEMORY	4W09	903	H14	2
5W12	904	130	11	2+34	FAN	OUT	TO	MEMORY	4409	904	H14	19
5W12	905	131	5	2+35	FAN	OUT	TO	MEMORY	4W09	905	H14	24
5W12	906	132	20	2+36	FAN	OUT	TO	MEMORY	4W09	906	H16	2
5W12	907	132	11	2+37	FAN	OUT	TO	MEMORY	4409	907	H16	19
5W12	908		_						4409	908		

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15W13	00		WRITE DISTRIBUTOR TO MEMORY	3W17	00	
15W13	90	133 28	2+38 FAN OUT TO MEMORY	3W17	90	H34 24
15W13	91	134 24	2+39 FAN OUT TO MEMORY	3W17	91	H35 2
15W13	92	134 7	2+40 FAN OUT TO MEMORY	3W17	92	H35 19
15W13	93	135 28	2+41 FAN OUT TO MEMORY	3W17	93	H35 24
19W13	94	136 24	2+42 FAN OUT TO MEMORY	3W17	94	H36 2
15W13	95	136 7	2+43 FAN OUT TO MEMORY	3W17	95	H36 19
5W13	96		2+44 FAN OUT TO MEMORY	3W17	96	H36 24
15W13	97	G39 28	ACCEPT	3W17	97	123 23
15W13	98			3W17	98	
15W13	99			3W17	99	
15W13	900	128 24	2+30 FAN OUT TO MEMORY	3w17	900	H31 2
15W13	901	128 7	2+31 FAN OUT TO MEMORY	3W17	901	H31 19
5W13	902	129 28	2+32 FAN OUT TO MEMORY	3W17	902	H31 24
15W13	903	130 24	2+33 FAN OUT TO MEMORY	3W17	903	H32 2
19W13	904	130 7	2+34 FAN OUT TO MEMORY	3W17	904	H32 19
15W13	905	131 28	2+35 FAN OUT TO MEMORY	3W17	905	H32 24
15W13	906	132 24	2+36 FAN OUT TO MEMORY	3W17	906	H34 2
5W13	907	132 7	2+37 FAN OUT TO MEMORY	3W17	907	H34 19
5W13	908			3W17	908	

15W14	0.0		~ ~ ~ ~ ~ .	WRITE DISTRIBUTOR TO MEMORY	9W05 0	0	
15W14	90	133	13	2+38 FÄN OUT TO MEMORY	9W05 9	0 H34	4 24
15W14	91	134	3	2+39 FAN OUT TO MEMORY	9405 9	1 H35	5 2
5W14	92	135	18	2+40 FAN OUT TO MEMORY		2 H35	3 19
5W14	93	135	13	2+41 FAN OUT TO MEMORY		3 H35	
5W14	94	136	3	2+42 FAN OUT TO MEMORY		4 436	
5W14	95	137	18	2+43 FAN OUT TO MEMORY		5 H36	
5W14	96	137	13	2+44 FAN OUT TO MEMORY			5 24
5W14	97			e en approprietation de la constantina de la constantina de la constantina de la constantina de la constantina		7	
5W14	98			•		8	
5W14	99					9	J
5W14	900	128	3	2+30 FAN OUT TO MEMORY	9W05 90	0 H31	. 2
5W14	901	129	18	2+31 FAN OUT TO MEMORY	905 90		
5W14	902	129	13	2+32 FAN OUT TO MEMORY	9805 90		1 24
5W14	903	130	-3	2+33 FAN OUT TO MEMORY	9W05 90		
5W14	904	131	-	2+34 FAN OUT TO MEMORY	9W05 90		
5W14	905	131		2+35 FAN OUT TO MEMORY	9W05 90		e a main way i
5W14	906	132	3	2+36 FAN OUT TO MEMORY	9W05 90		
5W14	907	133	18	2+37 FAN OUT TO MEMORY	9W05 90		10 hard 10 miles 10 miles
5W14	908			- what is not well the complete to	9W05 90		

15W15	0.0	ne diel dels erits gene des aus une aus aus aus mer aus	WRITE DISTRIBUTOR TO MEMORY	10W05	00	
15W15	90	133 11	2+38 FAN OUT TO MEMORY	10W05	90 H16 2	24
15W15	91	134 5	2+39 FAN OUT TO MEMORY	10W05	91 H17	12
15W15	92	135 20	2+40 FAN OUT TO MEMORY		92 H17 1	
15W15	93	135 11	2+41 FAN OUT TO MEMORY		93 H17 2	
15W15	94	136 5	2+42 FAN OUT TO MEMORY		94 H18	2
15W15	95	137 20	2+43 FAN OUT TO MEMORY		95 H18 1	
15W15	96	137 11	2+44 FAN OUT TO MEMORY		96 H18 2	
15W15	97			10005	97	
15W15	98				98	
15W15	99				99	
15W15	900	128 -5	2+30 FAN OUT TO MEMORY		00 H13	. 2
15W15	901	129 20	2+31 FAN OUT TO MEMORY		01 H13 1	
15W15	902	129 11	2+32 FAN OUT TO MEMORY	<del>-</del>	02 H13 2	
15W15	903	130 5	2+33 FAN OUT TO MEMORY		03 H14	
15W15	904	131 20	2+34 FAN OUT TO MEMORY		04 H14 1	
15W15	905	131 11	2+35 FAN OUT TO MEMORY		05 H14 2	
15W15	906	132 5	2+36 FAN OUT TO MEMORY		06 H16	
15W15	907	133 20	2+37 FAN OUT TO MEMORY		07 H16 1	
15W15	908		where the same that the state of		08	• •

15W16	0.0		WRITE DISTRIBUTOR TO MEMORY	14W04 00	
15W16	90	134 2	6 2+39 FÄN DÜT TO MEMORY	14W04 90	H17 2
15W16	91	135 2	2 2+40 FAN OUT TO MEMORY	14W04 91	H17 19
15W16	92	135	9 2441 FAN OUT TO MEMORY	14404 92	H17 2
15W16	93	136 2	6 2+42 FAN OUT TO MEMORY	14004 93	H18 2
15W16	94	137 2	2 2+43 FAN OUT TO MEMORY	14W04 94	H18 19
15W16	95	137	9 2444 FAN OUT TO MEMORY	14W04 95	H18 24
15W16	96			14W04 96	
15W16	97			14W04 97	
15W16	98		·	14404 98	
15W16	99		·	1404 99	
15W16	900	128 2	S 2+30 FAN OUT TO MEMORY	14404 900	H13 2
15W16	901	129 2		14W04 901	H13 19
15W16	902	129	9 2+32 FAN OUT TO MEMORY	14404 902	H13 24
15W16	903	130 2	6 2+33 FAN OUT TO MEMORY	14004 903	H14 2
15W16	904	131 2		14004 904	H14 19
15W16	905	131	9 2+35 FAN OUT TO MEMORY	1404 905	H14 24
15W16	906	132 2	6 2#36 FAN OUT TO MEMORY	14W04 906	H16 2
15W16	907	133 2	2 2+37 FAN OUT TO MEMORY	14W04 907	H16 19
15W16	908	133	9 2+38 FAN OUT TO MEMORY	14004 908	H16 24

15W17	0.0		WRITE DISTRIBUTOR TO MEMORY	16W05 00
15W17	90	134 28	2+39 FÄN OUT TO MEMORY	16W05 90 H17 2
15W17	91	135 24	2+40 FAN OUT TO MEMORY	16W05 91 H17 19
15W17	92	135 7	2+41 FAN OUT TO MEMORY	16W05 92 H17 24
15W17	93	136 28	2+42 FAN OUT TO MEMORY	16W05 93 H18 2
15W17	94	137 24	2+43 FAN OUT TO MEMORY	16W05 94 H18 19
19W17	95	137 7	2+44 FAN OUT TO MEMORY	16W05 95 H18 24
15W17	96			16W05 96
15W17	97			16W05 97
15W17	98			16W05 98
15W17	99			16W05 99
15W17	900	128:28	2+30 FAN OUT TO MEMORY	16W05 900 H13 2
15W17	901	129 24	2+31 FAN OUT TO MEMORY	16W05 901 H13 19
15W17	902	129 7	2+32 FAN OUT TO MEMORY	16W05 902 H13 24
15W17	903	130 28	2+33 FAN OUT TO MEMORY	16W05 903 H14 2
15W17	904	131 24	2.34 FAN OUT TO MEMORY	16W05 904 H14 19
15W17	905	131 7	2+35 FAN OUT TO MEMORY	16W05 905 H14 24
15W17	906	132 28	2+36 FAN OUT TO MEMORY	16W05 906 H16 2
15W17	907	133 24	2+37 FAN OUT TO MEMORY	16W05 907 H16 19
19W17	908	133 7	2+38 FAN OUT TO MEMORY	16W05 908 H16 24

L5W18	0.0		MEMORY ADDRESS FROM CONTROL	5W34 00
5W18	90	G40 18	2+6 ADDRESS TO MEMORY	5W34 90 Q42 22
5W18	91	G41 11	2+9 ADDRESS TO MEMORY	5W34 91 R40 11
5W18	92	G41 14	2+10 ADDRESS TO MEMORY	5W34 92 R40 26
5W18	93	G41 17	2+11 ADDRESS TO MEMORY	5W34 93 R40 22
5W18	94	G41 18	2+12 ADDRESS TO MEMORY	5W34 94 R41 11
5W18	95	042 11	2+13 ADDRESS TO MEMORY	5W34 95 R41 26
5W18	96	G42 14	2+14 ADDRESS TO MEMORY	5W34 96 R41 22
5W18	97	G42 17	2+15 ADDRESS TO MEMORY	5W34 97 R42 11
5W18	98	G42 18	2+16 ADDRESS TO MEMORY	
5W18	99	976 10	WIG ADDRESS TO MEMOR!	
		675 46	FL AAV	
5W18	900	G35 12	grock	5W34 900 M32 25
5W18	901	G39 18	2+0 BANK SELECT	5W34 901 Q40 11
5W18	902	G39 22	2+1 BANK SELECT	5W34 902 Q40 26
5W18	903	G38 9	2+2 CHASSIS 15 SELECT (110)	5W34 903 Q40 22
5W18	904	G38 5	2+3 CHASSIS 15 SELECT (110)	5W34 904 Q41 11
5W18	905	G38 13	2+4 CHASSIS 15 SELECT (110)	5W34 905 Q41 26
5W18	906	G40 11	2+5 ADDRESS TO MEMORY	5W34 906 Q41 22
5W18	907	G40 14	2.6 ADDRESS TO MEMORY	5W34 907 Q42 11
5W18	908	040 17	2+7 ADDRESS TO MEMORY	
AMTO	700	440 7	KAI AUURESS IU MEMURI	5W34 908 Q42 26

15W39	0 0			MEMO	RY FI	ROM	WRI'	TE DISTRIBUTOR	15W39	0 0		
15W39	90	H34	24	2+38	FÄN	OUT	TO	MEMORY	15w39	90	133	26
15W39	91	H35	2	2+39	FAN	OUT	TO	MEMORY	15W39	91	134	22
15W39	92	H35	19	2+40	FAN	OUT	TO	MEMORY	15W39	92	134	9
15W39	93	H35	24	2+41	FAN	ÕŨŤ	ŤÕ	MEMORY	15W39	93	135	26
15W39	94	H36	2	2+42	FÄN	OUT	TO	MEMORY	15W39	94	136	22
15W39	95	H36	19	2+43	FAN	OUT	TO	MEMORY	15W39	95	136	9
15W39	96	H36	24	2+44	FAN	OUT	TO	MEMORY	15W39	96	137	26
15W39	97				- Landada		-		15W39	97		
15W39	98								15W39	98		
15W39	99								15W39	99	-	
15W39	900	H31	2	2.30	FAN	OUT	TO	MEMORY	15W39	900	128	22
15W39	901	H31	19	2+31	FAN	ΟŪŤ	TO	MEMORY	15W39	901	128	9
15W39	902	H31	24	2+32	FAN	OUT	TO	MEMORY	15W39	902	129	26
15W39	903	H32	2	2+33	FAN	DUT	TO	MEMORY	15W39	903	130	22
15W39	904	H32	19	2+34	FAN	OUT	TO	MEMORY	15W39	904	130	9
15W39	905	H32	24	2+35	FÄN	OUT	TO	MEMORY	15W39	905	131	26
15W39	906	H34	2	2+36	FAN	OUT	TO	MEMORY	15W39	906	132	22
15W39	907	H34	19	2+37	FÄN	OUT	To	MEMORY	15W39	907	132	- 9
15W39	908						_		15W39	908		•

15W39	0 0			WRITE	DI	STRI	BUŤ	OR TO MEMO	RY		15W39	0.0		
15W39	90	133	26	2+38	FÄN	OUT	TO	MEMORY			15W39	90	H34	24
15W39	91	134	22	2+39	FAN	OUT	TO	MEMORY		·	15W39	91	H35	2
15W39	92	134	9	2 * 4 0	FAN	OUT	TO	MEMORY			15W39	92	H35	19
15W39	93	135	26	2+41	FAN	OUŤ	TO	MEMORY			15W39	93	H35	24
15W39	94	136	22	2+42	FAN	OUT	TO	MEMORY			15W39	94	H36	2
15W39	95	136	9	2+43	FAN	OUT	TO	MEMORY			15W39	95	H36	19
15W39	96	137	26	2 ± 4 4	FAN	OUT	TO	MEMORY			15W39	96	H36	24
15W39	97										15W39	97		
15W39	98			·							15W39	98		
15W39	99										15W39	99		
15W39	900	128	22	2+30	FAN	OUT	TO	MEMORY			15W39	900	H31	2
15W39	901	128	9	2 * 31	FAN	OUT	TO	MEMORY			15W39	901	H31	19
15W39	902	129	26	2+32	FAN	OUT	TO	MEMORY			15W39	902	H31	24
15W39	903	130	22	2+33	FAN	OUT	TO	MEMORY	Charles of the Indianant to the sages were the		15W39	903	H32	2
15W39	904	130	9	2 * 3 4	FAN	OUT	TO	MEMORY			15W39	904	H32	19
15W39	905	131	26	2+35	FAN	OUŤ	TO	MEMORY			15W39	905	H32	24
15W39	906	132	22	2+36	FAN	OUT	TO	MEMORY			15W39	906	H34	2
15W39	907	132	9	2+37	FAN	OUT	TO	MEMORY			15W39	907	H34	19
15W39	908										15W39	908		

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16W03	0.0		MEMORY FROM WRITE DISTRIBUTOR	13W17	0.0	
16W03	90	H03-24	2+8 FAN OUT TO MEMORY	13W17	90	133 7
16W03	91	H04 2	2+9 FAN OUT TO MEMORY	13W17	91	134 28
16W03	92	H04 19	2+10 FAN OUT TO MEMORY	13W17	92	135 24
16W03	93	H04 24	2+11 FAN OUT TO MEMORY	13W17	93	135 7
16W03	94	H06 2	2+12 FAN OUT TO MEMORY	13W17	94	136 28
16W03	95	H06 19	2+13 FÄN OUT TO MEMORY	13W17	95	137 24
16W03	96	H06 24	2+14 FAN OUT TO MEMORY	13W17	96	137 7
16W03	97			13W17	97	
16W03	98			13W17	98	
16W03	99			13W17	99	
16W03	900	H01 2	2+0 FAN OUT TO MEMORY	13W17	900	128 28
16W03	901	H01 19	2+1 FAN OUT TO MEMORY	13W17	901	129 24
16W03	902	H01 24	2+2 FAN OUT TO MEMORY	13W17	902	129 7
16W03	903	H02 2	2+3 FAN DUT TO MEMORY	13W17	903	130 28
16W03	904	H02 19	244 FAN OUT TO MEMORY	13W17	904	131 24
16W03	905	H02 24	2+5 FAN OUT TO MEMORY	13W17	905	131 7
16W03	906	H03 2	2+6 FAN OUT TO MEMORY	13W17	906	132 28
16W03	907	H03 19	2+7 FAN OUT TO MEMORY	13W17	907	133 24
16W03	908			13W17	908	

6W04	00		MEMORY FROM WRITE DISTRIBUTOR	14W17	00	<b></b>
6W04	90	H11 2	2+24 FAN OUT TO MEMORY	14W17	90	112 28
6W04	91	H11 19	2+25 FAN OUT TO MEMORY	14W17	91	113 24
6W04	92	H11 24	2+26 FAN OUT TO MEMORY	14W17	92	113 7
6W04	93	H12 2		14W17	93	114 28
6W04	94	H12 19	2+28 FAN OUT TO MEMORY	14W17	94	115 24
6W04	95	H12 24	2+29 FAN OUT TO MEMORY	14W17	95	115 7
6W04	96			14W17	96	
6W04	97			14W17	97	-
6W04	98		•	14W17	98	
6W04	99			14W17	99	
6W04	900	H07 2	2+15 FAN OUT TO MEMORY		900	106 28
5W04	901	H07 19	2+16 FAN OUT TO MEMORY		901	107 24
6W04	902	H07 24	2+17 FAN OUT TO MEMORY		902	107 7
5W04	903	H08 2	2+18 FAN OUT TO MEMORY		903	108 28
6W04	904	H08 19	2+19 FAN OUT TO MEMORY		904	109 24
6W04	905	H08 24	2+20 FAN OUT TO MEMORY		905	109 7
5W04	906	H09 2	2+21 FAN OUT TO MEMORY		906	110 28
5W04	907	H09 19	2+22 FAN OUT TO MEMORY	The second of the contract of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of	907	111 24
6W04	908	H09 24	2+23 FAN OUT TO MEMORY		908	111 7

16W05	00		MEMORY FROM WRITE DISTRIBUTOR	15W17	00	
16W05	90	H17 2	2+39 FÄN OUT TO MEMORY	15W17	90	134 28
16W05	91	H17 19	2+40 FAN OUT TO MEMORY	15W17	91	135 24
16W05	92	H17 24	2+41 FAN OUT TO MEMORY	15W17	92	135 7
16W05	93	H18 2	2+42 FAN OUT TO MEMORY	15W17	93	136 28
16W05	94	H18 19	2+43 FAN OUT TO MEMORY	15W17	94	137 24
16W05	95	H18 24	2+44 FAN OUT TO MEMORY	15W17	95	137 7
16W05	96			15W17	96	
16W05	97			15W17	97	
16W05	98		· ·	15W17	98	
16W05	99			15W17	99	
16W05	900	H13 2	2+30 FAN OUT TO MEMORY	15W17	900	128-28
16W05	901	H13 19	2+31 FAN OUT TO MEMORY	15W17	901	129 24
16W05	902	H13 24	2+32 FAN OUT TO MEMORY	15W17	902	129 7
16W05	903	H14 2	2+33 FAN OUT TO MEMORY	15W17	903	130 28
16W05	904	H14 19	2+34 FAN OUT TO MEMORY	15W17	904	131 24
16W05	905	H14 24	2+35 FAN OUT TO MEMORY	15W17	905	131 7
16W05	906	H16 2	2+36 FAN OUT TO MEMORY	15W17	906	132 28
16W05	907	H16 19	2+37 FAN OUT TO MEMORY	15W17	907	133 24
16W05	908	H16 24	2+38 FAN OUT TO MEMORY	15W17	908	133 7

W 0 6	00			MEMORY TO READ DISTRIBUTOR	3W13	0.0	
W06	90	H03	28	2+8	3W13	90	135 23
W06	91	H04	1	2+9	3W13	91	136 23
WD6	92	H04	. 8	2*10	3W13	92	138 23
W06	93	H04	28	2+11	3W13	93	139 23
W06	94	H06	1	2+12	3W13	94	140 23
W06	95	H06	8	2+13	3W13	95	141:23
WO6	96	H06	28	2+14	3W13	96	142 23
W06	97			,	3W13	97	
WO6	98				3W13	98	
W06	99				3 W 1 3	99	
W 0 6	900	H01	. 1	2+0	3W13	900	125 23
W 0 6	901	H01	8	2+1	3W13	901	126 23
W 0 6	902	H01	28	2+2	3W13	902	127 23
W 0 6	903	H02	1	2+3	3W13	903	128 23
W06	904	H02	8	2+4	3W13	904	129 23
W 0 6	905	H 0 2	28	2+5	3W13	905	132 23
W 0 6	906	H03	1	2+6	3W13	906	133 23
W06	907	H03	8	2+7	3W13	907	134 23
W 0 6	908				3W13	908	

16W07	0.0		MEMORY TO READ DISTRIBUT	OR 4W21	0.0		
16W07	90	H09-1	28 2+23	4W21	90	111	23
16W07	91	H11	1 2+24	4W21	91	112	23
16W07	92	H11	8 2+25	4W21	92	114	23
16W07	93	H11	18 2+26	4W21	93	115	23
16W07	94	H12	1 2+27	4W21	94	116	23
16W07	95	H12	8 2+28	4W21	95	117	23
16W07	96	H12	8 2+29	4W21	96	118	23
16W07	97			4W21	97		
16W07	98			4W21	98		
16W07	99			4W21	99		
16W07	900	H07	1 2+15	4w21	900	101	23
16W07	901	H07	8 2+16	4W21	901	102	23
16W07	902	H07	28 2+17	4w21	902	103	23
16W07	903	H08	1 2+18	4W21	903	104	
16W07	904	HO8	8 2+19		904	105	
16W07	905	H08	8 2+20		905		23
16W07	906	H09	1 2+21	4W21	906	109	23
16W07	907	H09	8 2+22		907	110	23
16W07	908				908		

16W08	00			MEMORY TO READ DISTRIBUTOR	9W19	00		
16W08	90	H16	28	2+38	9W19	90	135	23
16W08	91	H17	1	2+39	9W19	91	136	23
16W08	92	H17	8	2+40	9W19	92		23
16W08	93	H17	28	2+41	9W19	93		23
16W08	94	H18	1	2+42	9W19	94		23
16W08	95	H18	8	2+43	9W19	95		23
16W08	96	H18	28	2+44	9W19	96	142	
6W08	97				9W19	97		. = . = .
LOWOS	98			·	9W19	98		
LOWDS	99				9W19	99		
6W08	900	H13	1	2+30	9W19	900	125	23
6W08	901	H13	8	2+31	9W19	901		23
L6WD8	902	H13	28	2+32	9419	902		23
LOWOB	903	H14	1	2+33	9W19	903		23
6W08	904	H14	8	2+34	9W19	904		23
LOWOB	905	H14		2+35	9W19	905		23
6W08	906	H16	. 1	2+36	9W19	906		23
6W08	907	H16	8	2+37	9019	907		23
LOWDB	908		ŭ		9W19	908		-0

16409	0.0		MEMORY TO READ DISTRIBUTO	R 10W19	0.0	into Anna andre Allina dinto dinto dana anno anno filori dano, espe pero anno suos como espera espera espera e
16W09	90	H22 2	2+53	10W19	90	111-23
16W09	91	H23	2+54	10W19	91	112 23
16W09	92	H23	2+55	10W19	92	114 23
16W09	93	H23 2	2+56	10W19	93	I15 23
16W09	94	H24	2+57	10419	94	116:23
16W09	95	H24	2+58	10419	95	117 23
16W09	96	H24 2	2+59	10W19	96	118 23
16W09	97			10W19	97	•
16W09	98			10W19	98	
16W09	. 99			10W19	99	
16W09	900	H19	2+45	10W19	900	101 23
16W09	901	H19	2+46	10W19	901	102 23
16W09	902	H19 2	2+47	10W19	902	103 23
16W09	903	H21	2+48	10W19	903	104 23
16W09	904	H21:	2+49	10W19	904	105 23
16W09	905	H21 2	2+50	10W19	905	108 23
16W09	906	H22	2+51	10W19	906	109 23
16W09	907	H22	2.52	10W19	907	110 23
16W09	908		·	ĨOW19	908	

16W10	00			DISTRIBUTOR FROM WRITE BUFFER	2W08	0.0	
16W10	90	102	. 4	2+45 DATA TO MEMORY FAN OUT	2W08	90	B19 27
16W10	91	102	6	2+46 DATA TO MEMORY FAN OUT	2W08	91	B19 25
16W10	92	102	7	2+47 DATA TO MEMORY FAN OUT	2W08	92	B19 23
16W10	93		27	2+48 DATA TO MEMORY FAN OUT	2W08	93	820 8
16W10	94	102	25	2+49 DATA TO MEMORY FAN OUT	2W08	94	B20 6
16W10	95	102	22	2+50 DATA TO MEMORY FAN OUT	2W08	95	820 4
16W10	96	103	4	2+51 DATA TO MEMORY FAN OUT	2W08	96	B20 27
L6W10	97	103	6	2+52 DATA TO MEMORY FAN OUT	2W08	97	B20 25
16W10	98	103	7	2+53 DATA TO MEMORY FAN OUT	2W08	98	B20 23
6W10	99	103	27	2+54 DATA TO MEMORY FAN OUT	2W08	99	B21 8
16W10	900	103	25	2+55 DATA TO MEMORY FAN OUT	2008	900	B21 6
16W10	901	103	22	2+56 DATA TO MEMORY FAN OUT	2W08	901	B21 4
16W10	902	104	4	2+57 DATA TO MEMORY FAN OUT	2008	902	B21 27
16W10	903	104	6	2+58 DATA TO MEMORY FAN OUT	2W08	903	B21 25
16W10	904	104	7	2+59 DATA TO MEMORY FAN OUT	2W08	904	B21 23
16W10	905	C14	14	MEMORY MARGIN	2W08	905	B22 16
16W10	906				2W08	906	
16W10	907			THE TAX I COMPANY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPE	2W08	907	•
16W10	908				2W08	908	

16W11	0.0		WRITE DISTRIBUTOR TO MEMORY	13W05 00
16W11	90	111 3	2+93 FÄN OUT TO MEMORY	13W05 90 H40 2
16W11	91	112 18	2+54 FAN OUT TO MEMORY	13W05 91 H41
16W11	92	112 13	2+55 FAN OUT TO MEMORY	13W05 92 H41 1
16W11	93	113 3	2+56 FAN OUT TO MEMORY	13W05 93 H41 2
16W11_	94	114 18	2+57 FAN OUT TO MEMORY	13W05 94 H42
16W11	95	114 13	2+58 FAN OUT TO MEMORY	13W05 95 H42 1
16W11	96	115 3	2+59 FAN OUT TO MEMORY	13W05 96 H42 2
16W11	97			13W05 97
16W11	98			13W05 98
16W11	99			13W05 99
16W11	900	106 18	2+45 FAN OUT TO MEMORY	13W05 900 H37
16W11	901	106 13	2+46 FAN OUT TO MEMORY	13W05 901 H37 1
16W11	902	107 3	2+47 FAN OUT TO MEMORY	13W05 902 H37 2
16W11	903	108 18	2+48 FAN OUT TO MEMORY	13W05 903 H39
16W11	904	108 13	2+49 FAN DUT TO MEMORY	13W05 904 H39 1
16W11	905	109 3	2+50 FAN OUT TO MEMORY	13W05 905 H39 2
16W11	906	110 18	2+51 FAN OUT TO MEMORY	13W05 906 H40
16W11	907	110 13	2+52 FAN OUT TO MEMORY	13W05 907 H40 1
16W11	908			13005 908

6W12	00			WRITE DISTRIBUTOR TO MEMORY	4W10	00		
6W12	90	111	15	2+53 FAN OUT TO MEMORY	4W10	90	H22	24
6W12	91	112	20	2+54 FAN OUT TO MEMORY	4W10	91	H23	2
6W12	92	112	11	2+55 FAN OUT TO MEMORY	4W10	92	H23	19
6W12	93	113	5	2+56 FAN OUT TO MEMORY	4W10	93	H23	
6W12	94	114	20	2+57 FAN OUT TO MEMORY	4W10	94	H24	2
6W12	95	114	11	2+58 FAN OUT TO MEMORY	4W10	95	H24	19
6W12	96	115	5	2+59 FAN OUT TO MEMORY	4W10	96	H24	24
6W12	97			The state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the s	4W10	97		
6W12	98	G 0 4	20	WRITE	4W10	98	124	24
6W12	99	G04	21	GO	4W10	99	121	3
6W12	900	106	20	2+45 FAN OUT TO MEMORY	4W10	900	H19	2
6W12	901	In6	11	2+46 FAN OUT TO MEMORY	4W10	901	H19	
6W12	902	107	- 5	2+47 FAN OUT TO MEMORY	4W10	902	H19	24
6W12	903	108	20	2+48 FAN OUT TO MEMORY	4W10	903	H21	2
6W12	904	108	11	2+49 FAN OUT TO MEMORY	4W10	904		19
6W12	905	109	- 5	2.50 FAN OUT TO MEMORY	4W10	905	H21	
6W12	906	110	20	2+51 FAN OUT TO MEMORY	4W10	906	H22	2
6W12	907	110	11	2+52 FAN OUT TO MEMORY	4W10	907	H22	
6W12	908		* *	THE TAIL ONL TO MEMONY	4W10	908	77.6	. ,

16W13	00		WRITE DISTRIBUTOR TO MEMORY	3W18 00
6W13	90	111 28	2+53 FAN OUT TO MEMORY	3W18 90 H40 24
L6W13	91	112 24	2+54 FAN OUT TO MEMORY	3W18 91 H41 2
6W13	92	112 7	2+55 FAN OUT TO MEMORY	3W18 92 H41 19
6W13	93	113 28		3W18 93 H41 24
6W13	94	114 24	2+57 FAN OUT TO MEMORY	3W18 94 H42 2
6W13	95	114 7	2+58 FAN OUT TO MEMORY	3W18 95 H42 19
6W13	96	115 28		3W18 96 H42 24
L6W13	97	G05 28		3W18 97 123 25
L6W13	98			3W18 98
6W13	99			JW18 99
6W13	900	106 24	2+45 FAN OUT TO MEMORY	3W18 900 H37 2
6W13	901	106 7	2+46 FAN OUT TO MEMORY	JW18 901 H37 19
6W13	902	107 28	2+47 FAN OUT TO MEMORY	3W18 902 H37 24
6W13	903	108 24	2.48 FAN OUT TO MEMORY	JW18 903 H39 2
6W13	904	108 7	2449 FAN OUT TO MEMORY	3W18 904 H39 19
6W13	905	109 28	2+50 FAN OUT TO MEMORY	3W18 905 H39 24
6W13	906	110 24	2+51 FAN OUT TO MEMORY	3W18 906 H40 2
6W13	907	110 7	2+52 FAN OUT TO MEMORY	3W18 907 H40 19
6W13	908		-	3W18 908

16W14	0.0			WRITE DISTRIBUTOR TO MEMORY	9₩06	00		
16W14	90	111	13	2+53 FAN OUT TO MEMORY	9W06	90	H40	24
16W14	91	112	3	2+54 FAN OUT TO MEMORY	9W06	91	H41	2
16W14	92	113	18	2+55 FAN OUT TO MEMORY	9W06	92	H41	19
16W14	93	113	13	2+56 FAN OUT TO MEMORY	9W06	93	H41	24
16W14	94	114	3	2+57 FAN OUT TO MEMORY	9W06	94	H42	2
16W14	95	115	18	2+58 FAN OUT TO MEMORY	9W06	95	H42	19
16W14	96	115	13	2+59 FAN OUT TO MEMORY	9W06	96	H42	24
16W14	97				9006	97		
16W14	98		· ·		9W06	98		
16W14	99				9W06	99		
16W14	900	106	3	2+45 FAN OUT TO MEMORY	9 4 0 6	900	H37	2
16W14	901		18	2+46 FAN OUT TO MEMORY		901	H37	19
16W14	902	107	13	2+47 FÃN OUT TO MEMORY	9406	902	H37	24
16W14	903	108	3	2+48 FAN OUT TO MEMORY	9406	903	H39	2
16W14	904	109	18	2+49 FAN OUT TO MEMORY	9W06	904	H39	19
16W14	905	109	13	2+50 FAN OUT TO MEMORY	9406	905	H39	24
16W14	906	110	3	2+51 FAN OUT TO MEMORY		906	H40	2
16W14	907	111	18	2+52 FAN OUT TO MEMORY		907		
16W14	908					908		

16W15	00	an area anno parte e sa papa ette area este e ce e ce area	WRITE DISTRIBUTOR TO MEMORY	10W06	00		
16W15	90	111 11	2+53 FAN OUT TO MEMORY	10006	90	H22	24
16W15	91	112 5	2+54 FAN OUT TO MEMORY	10006	91	H23	2
16W15	92	113 20	2+55 FAN OUT TO MEMORY	10W06	92	H23	19
16W15	93	113 11	2+56 FAN OUT TO MEMORY	10W06	93	H23	24
16W15	94	114 5	2+57 FAN OUT TO MEMORY	10006	94	H24	. 2
16W15	95	115 20	2+58 FAN OUT TO MEMORY	10W06	95	H24	19
16W15	96	115 11	2+59 FAN OUT TO MEMORY	10W06	96	H24	24
16W15	97			10006	97		
16W15	98			10W06	98		
16W15	99			10006	99		
16W15	900	106 5	2+45 FAN OUT TO MEMORY	10006	900	H19	2
16W15	901	107 20	2+46 FAN OUT TO MEMORY		901	H19	
16W15	902	107 11	2+47 FAN OUT TO MEMORY		902	H19	24
16W15	903	108 5	2+48 FAN DUT TO MEMORY		903	H21	2
16W15	904	109 20	2+49 FAN OUT TO MEMORY	· · · · · ·	904	H21	15
16W15	905	109 11	2+50 FAN OUT TO MEMORY	the state of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the same of the s	905	H21	24
16W15	906	110 5	2451 FAN OUT TO MEMORY		986	H22	2
16W15	907	111 20	2+52 FAN OUT TO MEMORY	1.01.00	907	H22	
16W15	908			77 1 1 2	908		

16W16	0 0			WRIT	E DI	STRI	BUŤ	OR TO MEMORY	14W05	00		
16W16	90	112	26	2+54	FÄN	OUT	TO	MEMORY	14W05	90	H23	2
16W16 16W16	91 92	113 113	22	2+55 2+56		DUT	TO	MEMORY MEMORY	14W05	91 92	H23 H23	
16W16	93	114	26	2+57	FAN	OUT	TO	MEMORY	14W05	93	H24	
16W16	94	115	55	2+58			TO	MEMORY MEMORY	14405	94	H24	
16W16 16W16	95 96	115	9	2+59	PAN	UUŢ	TO	MEMURY	14W05 14W05	95 96	H24	24
16W16	97							and the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of th	14W05	97		
16W16 16W16	98 99		,						14W05 14W05	98		
16W16	900	106	26	2+45	FAN	OUT	ŢO		14W05	900	H19	2
16W16 16W16	901 902	107	22	2+46 2+47	FAN	141	TO	MEMORY Memory	14W05 14W05	901 902	H19 H19	19 24
16W16	903	108	26	2+48		OUT	TO	MEMORY	14405	903	H21	2
16W16	904	109	22	2+49	FAN	OUT	TO	MEMORY Memory	14W05	904	H21 H21	19
16W16 16W16	905 906	I 0 9 I 1 0	9 26	2+50 2+51	FAN	OUT	TO	MEMORY	14W05	906	H22	2
16W16 16W16	907 908	111 111	22	2+52 2+53	FAN	OUT	TO	MEMORY MEMORY	14W05 14W05	907 908	H22	19

L6W17	0.0		WRITE DISTRIBUTOR TO MEMORY	15W05	0.0	
6W17	90	I12 28	2+54 FAN OUT TO MEMORY	15W05	90	H41 2
6W17	91	113 24	2+55 FAN OUT TO MEMORY	15W05	91	H41 19
6W17	92	113 7	2+56 FAN OUT TO MEMORY	15W05	92	H41 24
16W17	93	114 28	2+57 FAN OUT TO MEMORY	15W05	93	H42 2
6W17	94	115 24	2+58 FAN OUT TO MEMORY	15W05	94	H42 19
6W17	95	115 7	2+59 FAN OUT TO MEMORY	15W05	95	H42 24
6W17	96	111 7	2+53 FAN OUT TO MEMORY	15W05	96	H40 24
6W17	97			15W05	97	and the second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second s
6W17	98			15W05	98	
6W17	99			15W05	99	
6W17	900	106 28	2+45 FAN OUT TO MEMORY	15W05	900	H37 2
6W17	901	107 24	2+46 FAN OUT TO MEMORY	15W05	901	H37 19
6W17	902	107 7	2+47 FAN OUT TO MEMORY	15W05	902	H37 24
6W17	903	108 28	2+48 FAN OUT TO MEMORY	15W05	903	H39 2
6W17	904	109 24	2+49 FAN OUT TO MEMORY	15W05	904	H39 19
6W17	905	109 7	2+50 FAN OUT TO MEMORY	15W05	905	H39 24
6W17	906	110 28	2+51 FAN OUT TO MEMORY	15W05	906	H40 2
6W17	907	111 24	2+52 FAN OUT TO MEMORY	19W05	907	H40 19
6W17	908			15W05	908	

6W18	0.0		MEMORY ADDRESS FROM CONTROL	5W35	00	
6W18	90	G01 18	2+8 ADDRESS TO MEMORY	5W35	90	042 24
6W18	91	G02 11	2+9 ADDRESS TO MEMORY	5w35	91	R40 13
6W18	92	002 14	2+10 ADDRESS TO MEMORY	5W35	92	R40 28
6W18	93	G02 17	2+11 ADDRESS TO MEMORY	5W35	93	R40 24
5W18	94	G02 18	2+12 ADDRESS TO MEMORY	5w35	94	R41 13
5W18	95	G03 11	2+13 ADDRESS TO MEMORY	5W35	95	R41 2
5W18	96	G03 14	2+14 ADDRESS TO MEMORY	5W35	96	R41 24
W18	97	G03 17	2+15 ADDRESS TO MEMORY	5W35	97	R42 13
W18	98	G03 18	2+16 ADDRESS TO MEMORY	5W35	98	R42 2
W18	99		·····································	5w35	99	
W18	900	G13 12	CLOCK	5 W 3 5	900	M32 2
W18	901	G05 18	2+0 BANK SELECT	5W35	901	Q40 1
W18	902	G05 22	2+1 BANK SELECT	5W35	902	040 2
W18	903	G04 8	2+2 CHASSIS 16 SELECT (111)	9W35	903	Q40 2
W18	904	G04 5	2+3 CHASSIS 16 SELECT (111)	5W35	984	Q41 1
W18	905	G04 13	2.4 CHASSIS 16 SELECT (111)	5W35	905	041 2
W18	906	G01 11	2+5 ADDRESS TO MEMORY	5W35	906	Q41 2
W18	907	G01 14	2+6 ADDRESS TO MEMORY	5W35	907	042 1
W18	908	G01 17	2#7 ADDRESS TO MEMORY	5W35	908	042 2

16W24	0.0			DISK SŸNC PASSON (OUTPUT)	
16W24	90	K02	8	2+0 OUTPUT DATA	
16W24	91	K02	6	2+1 OUTPUT DATA	*
16W24	92	K02	_	2+2 OUTPUT DATA	
16W24	93	K02		2+3 QUTPUT DATA	
16W24	94	KO2		2+4 OUTPUT DATA	-1 1
16W24	95	K02		2+5 OUTPUT DATA	
16W24	96	K03		2+6 CUTPUT DATA	
16W24	97	K03	6	2+7 OUTPUT DATA	
16W24	98	K03	4	2+8 QUTPUT :DATA	
16W24	99	K03	27	2#9 OUTPUT DATA	
16W24	900	K03	25	2+10 OUTPUT DATA	
16W24	901	K03		2+11 OUTPUT DATA	
16W24	902	K01		ACTIVE	
16W24	903	K01		INACTIVE	
16W24	904	K01	4	FULL	
1988 10 10 10 10 10 10 10		* * *** *** *** *** ***			
16W24	905	K01		EMPTY	
16W24	906	K01	1	FUNCTION	
16W24	907	K01	25	MASTER CLEAR	
16W24	908				

16W25	00			DISK SŸNC	PASSON (INPUT)
16W25	90	L09	-5	2+0 INPUT	DATA
16W25 16W25	91 92	L09	7		DATA DATA
16W25	93	L09	21	2+3 INPUT	DATA
16W25 16W25	94 95	L09			DATA
16W25	96	LO8	5	2+6 INPUT	DATA
16W25	97	LOB	7		DAŢA
16W25	98	L08	10	2+8 INPUT	DATA
16W25	99	L08	21	2+9 INPUT	DATA
16W25	900	LO8	24	2+10 INPUT	
16W25	901	L08	26	2+11 INPUT	DATA
16W25	902			ACTIVE	CONTRACTOR OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF THE PROPERTY OF
16W25	903	L07	- 5	INACTIVE	
16W25	904	LO7	.7	FULL	
16W25	905			EMPTY	THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE COURT OF THE C
16W25	906	L26	1		
16W25	907	L26			
16W25	908				י מי אינורו או בי בי בי בי בי בי בי בי בי בי בי בי בי

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16W26	00			DISK SYNC TO CHANNEL (INPUT)
16W26	90	K05	8	BİT 10
16W26	91	K05	6	BIŤ 1
6W26	92	K05		8 Î Ŷ · 2
6W26	93	K05		Bit 3
6W26	94	KO5		817.4
6W26	95	K05		BIT 5
6W26	96	K06		
6W26	97	K06		0 t † 7
6W26	98	K06		Đị Triề
6W26	99	K06		017.0
6W26	900	K06		BIT 10
6W26	901	K06		B17: 11
6W26	902	KO4	- A	ACTIVE
6W26	903	K04		INACTIVE
6W26	904	K04		FULL
6W26	905	K04		EMPTY
6W26	906	L26		
6W26	907	L26		
6W26	908	L 2 0	T. G	
TAMER	740			

6W27	90	LO1	- 5	2 • 0
6W27	91	L01		2+1
6W27	92	101	1.0	2+2
6W27	93	L01		2.3
6W27	94	101	24	2+4
6W27	95	L01	26	2.5
6W27	96	LO2		2+6
6W27	97	L02		2+7
6W27	98	L02	46	2+8
6W27	99	L02	21	2.9
6W27	900	L02	24	2+10
6W27	901	L02	26	2+11
	902	L03	40	ACTIVE:
6W27	903			INACTIVE
		L03	21	
6W27	904	L03	10	PULL
6W27	905	L03		EMPTY
6W27	906	L03	5	FUNCTION
6W27	907	L03	24	MASTER CLEAR
6W27	908			

```
DISK SYNC TO DISK FILE (OUTPUT)
16W28
        00
         90
             J06
                      2+0 WRITE DATA
        91
                      2+1
16W28
             J06
                  - 6
        92
                      2+2
2+3
16W28
             J06
             J06 27
16W28
16W28
        94
             J06 25
                      2+4
16W28
        95
             J06 23
                      2+5
        96
             J05
                      4+6
16W28
        97
             J05
                      2+7
16W28
16W28
        98
             J05
                      2+8
        99
             J05 27
                      2.9
16W28
            J05
                25
16W2B
       900
                      2+10 WRITE DATA
                     2+11
POSITION CONTROL
GROUP CONTROL
WRITE CONTROL
CLOCK CONTROL
16W28
       901
       902
903
             J04
16W28
             J04
16W28
             J04
       904
             J04 27
       905
16W28
16W28
       906
       907
16W28
       908
16W28
```

16W29	0.0			DISK SYNC TO DISK FILE (INPUT)
16W29	90	J01	12	2+0 READ DATA ANALOG
16W29	91	J01	5	2+1
16W29	92	J01	26	2+2
16W29	93	J01		2 ≠ 3
16W29	94		12	2+4
16W29	95	J02	5	2+5
16W29	96		26	2*6
Application of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the				2+7
16W29	97		19	- <del> </del>
16W29	98	703	12	2+8
16W29	99	J03	5	2.9
16W29	900	J03	26	2+10 READ DATA ANALOG
16W29	901	J03	19	2+11
16W29	902	J04	12	REV MARK
16W29	903	J04	5	CLOCK
16W29	904	J04	26	SECTOR MARK
16W29	905	77.	= ==	A STANDARD CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE CONTRACTOR OF THE C
16W29	906			
16W29	907			
16W29	908	and the second second		

16W39	0.0		WRÎTE DÎSTRÎBUTOR TO MEMORY	16W39	00	<b>en an en an en an en an en an en en en en en en en en en en en en en</b>
16439	90	111 26	2#53 FAN OUT TO MEMORY	16W39	90	H22 24
16W39	91	112 22	2+54 FAN OUT TO MEMORY	16W39	91	H23 2
16W39	92	112 9	-2 W 5 5 FÄN OUT TO MEMORY	16W39	92	H23 19
16W39	93	113 26	2+56 FAN OUT TO MEMORY	16W39	93	H23 24
16W39	94	114 22	2+57 FAN OUT TO MEMORY	16W39	94	H24 2
16W39	95	114 9	2+58 FAN OUT TO MEMORY	16W39	95	H24 19
16W39	96		2.59 FAN OUT TO MEMORY	16W39	96	H24-24
16W39	97			16439	97	440 L 4 170 L
16W39	98			16W39	98	
16W39	99			16W39	99	
16W39	900	106 22	2+45 FAN OUT TO MEMORY	16W39	900	H19 2
16W39	901	106 9	2+46 FAN OUT TO MEMORY	16W39	901	H19 19
16W39	902	107 26	2+47 FAN OUT TO MEMORY	16W39	902	H19 24
16W39	903	108 22	2+48 FAN OUT TO MEMORY	16W39	903	H21 2
16W39	904	108 9	2449 FAN OUT TO MEMORY	16839	904	H21:19
16W39	905	109 26	2+50 FAN OUT TO MEMORY	16W39	905	H21 24
16W39	906	110 22	2451 FAN OUT TO MEMORY	16W39	906	H22 2
16W39	907	I10 9	2+52 FAN OUT TO MEMORY	16W39	907	H22 19
16W39	908	/	more and the second of the second	16W39	908	

16W39	0.0			MEMORY FROM WRITE DISTRIBUTOR	16W39	0.0		
16W39	90	H22	24	2+53 FAN OUT TO MEMORY &	16W39	90	111	26
16W39	91	H23	2	2+54 FAN OUT TO MEMORY	16W39	91	112	22
16W39	92	H23	-	2+55 FAN OUT TO MEMORY	16W39	92	112	9
16W39	93	H23		2+56 FAN OUT TO MEMORY	16W39	93	113	26
16W39	94	H24	2	2+57 FAN OUT TO MEMORY	16W39	94	114	22
16W39	95	H24	10	2+58 FAN OUT TO MEMORY	16W39	95		9
16W39	96	H24		2+59 FAN OUT TO MEMORY	16W39	96	115	
16W39	97			and a state of the second state of the second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second secon	16W39	97		The second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second secon
16W39	98				16W39	98		
16W39	99				16W39	99		THE REAL PROPERTY AND THE PARTY HAS BEEN THE REAL PROPERTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY AND THE PARTY
16W39	900	H19	: 2	2+45 FAN OUT TO MEMORY	16W39	900	106	22
16W39	901	H19	19	2-46 FAN OUT TO MEMORY	16W39	901	106	9
16W39	902	H19		2+47 FAN OUT TO MEMORY	16W39	902	107	26
16W39	903	H21		2+48 FAN OUT TO MEMORY	16W39	903	108	Contraction of the second section of the second section is a first second section of the second section is a second section of the second section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section sect
16W39	904	H21	-	2+49 FAN OUT TO MEMORY	16W39	904	108	9
16W39	905	H21		2+50 FAN OUT TO MEMORY	16W39	905	109	management of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of th
16W39	906	H22	2	2451 FAN OUT TO MEMORY	16W39	906	110	
16W39	907	H22		2+52 FAN OUT TO MEMORY	16W39	907		9
16W39	908			was the sail of the time.	16039	908	- 1 0	•

10W03	0,0			DISK FILE FROM DISK	SYNC
50W03	90	A18	5	2+0 WRITE DATA	
60W03	91	A18	7	2+1 WRITE DATA	TO DISK
50W03	92	A18	10	2+2 WRITE DATA	SYNCHRONIZER
50W03	93	A18	21	2+3 WRITE DATA	
50W03	94	A18	24	2+4 WRITE DATA	and the second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second s
50W03	95	A18	26	2+5 WRÍTE DAŤA	CHASSIS 4 AND/OR 10
10W03	96	A19	5	2+6 WRÎTE DATA	AND/OR 16
50W03	97	A19	7	2+7 WRITE DATA	
50W03	98	A19	10	2+8 WRITE DATA	All forms and the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the second section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the section of the sec
50W03	99	A19	21	2+9 WRÌTE DATA	
10W03	900	A19	24	2+10 WRITE DATA	
60W03	901	A19	26	2+11 WRITE DATA	
SOW03	902	A20	5	POSITION CONTROL	
IOW03	903	A 2 0	7	GROUP CONTROL	
50W03	904	A 2 0	10	WRITE CONTROL	
10W03	905	A17	12	CLOCK	
50W03	906				
50W03	907				
0W03	908				and the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of th

30W04	0 0			DISK FILE TO DISK SYNC	
30W04	90	A01	9	2+0 READ DATA ANALOG	
30W04	91	A 0 1	15	2+1 READ DATA ANALOG	TO DISK
30W04	92		25	2+2 READ DATA ANALOG	SYNCHRONIZER
30W04	93	A02	٠,	2+3 READ DATA ANALOG	
30W04	94	A 0 2	_	2+4 READ DATA ANALOG	
30W04	95	-	25	2+5 READ DATA ANALOG	
30W04	96	A03	9	2+6 READ DATA ANALOG	CHASSIS 4 AND/OR IO
30W04	97	A03		217 READ DATA ANALOG	AND/OR 16
30W04	98	A 0 3		2+8 READ DATA ANALOG	
	99	_	9	249 READ DATA ANALOG	
30W04			-		
30W04	900	A 0 4		2+10 READ DATA ANALOG	
30W04	901	_	25	2+11 READ DATA ANALOG	
30W04	902	B12	9	REV. MARK	V 16
30W04	903	B12	25	CLOCK	
30W04	905				
30W04	906				
30W04	907				
30W04	908				

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CONSOLE CABLE 1 FROM DISPLAY SYNC
50W01
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                                           90 A16 9 X+0
91 A16 6 X+1
92 A16 1 X+2
93 A20 9 X+3
50W01
                                                                                                                                                                                                                                                                                                                        TO DISPLAY CONSOLE SYNCHRONIZER
50W01
50W01
50W01
                                                                A20 6 X+4
A20 1 X+5
50W01
                                            94
                                                                                                                                                                                                                                                                                                                           CHASSIS 12
                                           95
50W01
                                                                                        9
50W01
                                           96
                                                                 A24
                                                                                                            X+6
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                                           97
50W01
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                                      901 A04 1
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50W01
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50W01 904 A08
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50W01 908 B22 9 FOCUS AND ASTIGMATISM
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CONSOLE CABLE 2 TO DISPLAY SYNC
50W02 00
50W02
        90 A26 1 KEYBOARD BIT 0 (TB2 1)
                                                               TO DISPLAY CONSOLE
        91 A26 2 KEYBOARD BIT 1(TB2 2)
50W02
       92 A26 3
93 A26 4
94 A26 5
                      KEYBOARD BIT 2 (TB2 3)
                                                                  SYNCHRONIZER
50W02
50W02
                      KEYBOARD BIT 3 (TB2 4)
                      KEYBOARD BIT 4 (TB2 5)
KEYBOARD BIT 5 (TB2 6)
50W02
        95 A26 6
                                                                  CHASSIS 12
50W02
                      KEY DOWN (TB2 8)
KEY UP (TB2 7)
        96
            A26 7
50W02
        97
            A26 B
50W02
                     UNBLANK LEFT CONSOLE OUNBLANK RIGHT CONSOLE O
                 1 6
        98
            B21
50W02
        99
50W02
            B21
                     SMALL SIZE
50W02 900 B22 6
       901 B22 1 MED. STZE
50W02
50W02
       902
                      VERT. PUSH ANALOG
50W02
       903
             B24
                     VERT. PULL ANALOG
HORIZ. PUSH ANALOG
            B24
50W02
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       905 B23 1
50W02
50W02 906 B23 9 HORIZ. PULL ANALOG
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81W00	90	D12	26	DATA	BĨ	7 2 * 0		•
81W00	91	D12	16	DATA	Bi	7 2+1	Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Company of the Compan	
81W00	92	D12	13	DATA	BŤ	2+2		
81W00	93	D12				2+3		and the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of the first of t
81W00	94	C12			- 5	2+4		
81W00	95					2 • 5		The state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the s
81W00	96	C12				2+6		
81W00	97	C12	3	DATA			the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the co	
A1WOO	98	011	_			0.40		
81W00	99	C11	16			2+9		
81W00	900	C11	13			2+10		
81W00	901	C11	3			2+11		Consider the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control
81W00	902	C10	16	ACTIV				
81W00	903	C10	26	INAC		-		The second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second secon
81W00	904	C10	3	FULL	• • •	•		
81W00	905	C10	13	EMPT	Ţ			
81W00	906	C09	3	FUNC		ı		
81W00	907	009	13			LEAR	÷	
81W00	908	507	- 0	**************************************	• • • •	/# E - N		

81W01	0.0		6681 SYNC PASSON (OUTPUT)
81W01	90	D12 28	DATA BIT 2+0
81W01	91	D12 14	DATA BÎT 2+1
81W01	92	D12 15	DATA BİT 2+2
81W01	93	D12 1	DATA BIT 2+3
81W01	94	C12 28	DATA BIT 2+4
81W01	95	C12 14	DATA BIT 2+5
81W01	96	C12 15	DATA BIT 2+6
81W01	97	C12 1	DATA BIT 2+7
81W01	98	C11 28	DATA BIT 2+8
81W01	99	C11 14	DATA BIT 2+9
81W01	900	C11 15	
81W01	901	C11 1	DATA BIT 2*11
81W01	902	C10 14	ACTIVE
81W01	903	C10 28	INACTIVE
81W01	904	C10 1	<u>FULL</u>
81W01	905	C10 15	EMPTY
81W01	906	E09 1	FUNÇTION
81W01	907	C09 15	MASTER CLEAR
81W01	908		
			Automotive Management of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control

81W02	0.0			6681 SYNC TO CHANNEL (INPUT)
81W02	90	D10	24	DATA BIT 2+0
81W02	91	D10	26	DATA BIT 2+1
81W02	92	D10	28	DATA BIT 2+2
81W02	93	D10	3	DATA BIT 2*3
81W02	94	D10	5	DATA BIT 2+4
81W02	95	D10	7	DATA BIT 2+5
81W02	96	D09	24	DATA BIT 2+6
81W02	97	D09	26	DATA BIT 2+7
81W02	98	009	28	DATA BIT 2+8
81W02	99	D09	3	DATA BIT 2*9
81W02	900	009	5	DATA BİT 2+18
81W02	901	009	7	DATA BIT 2+11
81W02	902	•	·	ACTIVE
81W02	903	D11	3	INACTIVE
81W02	904	011	-5	PULL
81W02	905	D11	7	EMPTY
81W02	906	E12	12	GLOCK (10 MC)
81W02	907	Cn9		CLOCK (1 MC)
81W02	908	-0-		

81W03	0.0		6681 8	SANO	PASS	ON	(INPUT)	The state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the s	
81W03	90	D10 8	DATA E	ÎT	2*0			•	
81W03	91	D10 9	DATA	ŤŤ	2*1			Expense and additional mathematical size defined as on the few and an expension and order.	
81W03	92	D10 10	DATA	***					
81W03	93	D10 21	DATA E				NO. 1.17 NO. 100 NO. 100 NO. 100 NO. 100 NO. 100 NO. 100 NO. 1 (100 NO. 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	The state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the s	
81W03	94	D10 22	DATA E		2*4				
81W03	95	D10 23	DATA E		2*5				
81W03	96	D09 8	DATA		2+6				
81W03	97	D09 9	DATA	ÎŤ	2+7		-	as an electrical value of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the contract of the	
81W03	98	D09 10	DATA	•	-				
81W03	99	D09 21	DATA E	ŤT	2+9				
81W03	900	D09 22	DATA E	ÎŤ	2+10				
81W03	901	D09 23	DATA E					The second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second second secon	
81W03	902		ACTIVE						
81W03	903	D11 21	INACT	VE				E ye a serial conflict angular displace displace of a large black of a state of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the serial field of the se	
81W03	904	D11 22	FULL						
81W03	905	D11 23	EMPTY						
81W03	906	D11 28	CLOCK	(10	MC)				
81W03	907	D11 26	CLOCK	(1	MC)			Annu Sales have been been dead and their sales and their sales have been been dead to the sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sales and their sale	
81W03	908								
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82W00	0.0	THE PERSON NAME AND ADDRESS OF THE PERSON NAME AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED AND ADDRESS OF THE PERSON NAMED ADDRESS OF THE PERSON NAMED ADDRESS OF THE PERSON NAMED ADDRESS OF THE PERSON NAMED ADDRESS OF THE PERSON NAMED ADDRESS OF THE PERSON NAMED ADDRESS OF THE PERSON NAMED ADDRESS	6682 SŸNC TO CHANNEL (OUTPUT)
82W00	90	D12 26	DATA BIT 2+0
82W00	91	D12 16	DATA BIT 2+1
82W00	92	D12 13	DATA BIT 2+2
82W00	93	D12 3	DATA BIT 2*3
		C12 26	
82W00	94.		PATA BIT 244
85M00	95	C12 16	DAȚA BIT 2+5
82W00	96	C12 13	DATA BIT 2:6
82W00	97	C12 3	DATA BIT 2+7
82W.0.0.	9.8	C11 26	DATA BÎT 2*8
82W00	99	C11 16	DATA BIT 2+9
82W00	900	C11 13	DATA BIT 2*10
82W00	901	C11 3	DATA BIT 2+11
82W00	902	C10 16	
		C10 26	INACTIVE
82W00	903		
82W00	904	C10 3	<u> Pulli-</u>
82W00	905	C10 13	EMPTY
82W00	906	C09 3	FUNCTION
82W00	907	C09 13	MASTER CLEAR
82W00	908		
	- =	1,845 (1,000 (1,000))	The state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the s

82W01	0.0			6682	SÝNC	PASS	ON	OUTPU	T)	 The second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of th
82W01	90	D12	28	DATA	BİT	2*0				
82W01	91	D12	14	DATA	BIT	2+1				 For a contract of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second of the second o
82W01	92	D12	15	DATA		2+2				
82W01	93	D12	1			2+3			*	 
82W01	94	C12	28	DATA		2+4				•
82W01	95	C12	14		BİT	2+5				
82W01	96	C12	15		BIT	2*6				
82W01	97	C12	1	DATA	BÎT	2*7	•			Transa Landelli Annie de la description de distribuir de distribuir de la description de la la la companyon de la la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon de la companyon
82W01	98	C11	28	_ ,	BİT	2*8				
82W01	99	C11	1.4		BÎT	2+9				·
82W01										
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82W01	902	C10	14	ACTIV						A process of the Communication and the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state of the state
82W01	903	C10	28	INACT	LIVE					
82W01	904	C10	1	FULL						
82W01	905	C10	15	EMPTY						
82W01	906	009	1	FUNCT		_				
82W01	907	C 0 9	15	MASTE	ERICL	EAR				
82W01	908			and the second of the second						
				•						AND AND AND AND AND AND AND AND AND AND

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6682 SYNC TO CHANNEL (INPUT)
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                           DATA BIT 2+0
82M05
                D10 24
                           DATA BIT 2+1
DATA BIT 2+2
DATA BIT 2+3
DATA BIT 2+3
           91
82W02
                D10 26
           92
93
82W02
                D10 28
82W02
                D10
           94
82W02
                010
                           DATA BIT 2+5
DATA BIT 2+6
DATA BIT 2+7
DATA BIT 2+8
DATA BIT 2+9
DATA BIT 2+10
DATA BIT 2+11
82W02
           95
                D10
82W02
           96
                D09
                      24
           97
                D09
82W02
                      26
82W02
          98
                D09
                      28
          99
82W02
                D09
                       3
                       5
82W02
         900
                D09
                D09
82W02
         901
                           ACTIVE
INACTIVE
82W02
         902
82W02
         903
                D11
82W02
                D11
         904
                       5
                           FULL
                D11
                           EMPTY
82W02
         905
                       7
                           CLOCK (10 MC)
82W02
                E12
         906
                      12
         907
82W02
                C09
                     16
82W02
         908
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82W03	0.0			6682	SYN	C PASS	ON	(INPUT)	 		
82W03	90	D10	8	DATA	BÎT	2 + 0					
82W03	91	D10	9	DATA	BIT	2+1		*** ** * * * * * * * * * * * * * * * * *		properties and a properties of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the control of the c	Annual Control of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of the State of
82W03	92	D10	10	DATA	BÍT	2*2					
82W03	93	D10	21	DATA	BIT	2*3			 		
82W03	94	D10	22	DATA	BÎT	2+4				·	
82W03	95	D10	23	DATA	BIT	2*5				I not use the ten ten yet and the past of the past of the past of the past of the ten ten ten ten ten ten ten ten ten te	
82W03	96	D09	8	DATA	BİT	2*6			 		
82W03	97	D09	9	DATA	BIT	2+7					
82W03	98	D09	10	DATA	BIT	2 * 8					
82W03	99	D09	21	DATA	BÎT	2+9				The second section and the second section of the second section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section section sectio	
82W03	900	D09	22	DATA	BİT	2*10					
82W03	901	D09	23	DATA	BÎT	2+11					
82W03	902			ACTI							
82W03	903	D11	24	INAC						* English of the Anthony of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Control of the Cont	
82W03	904	D11	22	FULL							
82W03	905	D11	23	EMPT	Ü						
-	906		28	CLOC		0 MC)					
82W03		D11			3.77						
82W03	907	D11	26	CLOC	× (1	MC)					
82W03	908		Name of the last							- contain make however an experience of the set of the contains of the set	and the second second
•	END F	ILE		•							

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        91
            F37
                     2+1 CENTRAL PROGRAM ADDRESS
        92
            F37
                     2+2 CENTRAL PROGRAM ADDRESS
1W02
1402
        93
            F37
                     2+3 CENTRAL PROGRAM ADDRESS
                21
1W02
        94
            F37
                24
                     2+4 CENTRAL PROGRAM ADDRESS
                     2+5 CENTRAL PROGRAM ADDRESS
        95
            F37
1W02
                26
        96
1W02
            F38
                     2+6 CENTRAL PROGRAM ADDRESS
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        97
            F38
                     2+7 CENTRAL PROGRAM ADDRESS
1W02
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                     2+8 CENTRAL PROGRAM ADDRESS
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                     2+12 CENTRAL PROGRAM ADDRESS
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            F39
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                     2+13 CENTRAL PROGRAM ADDRESS
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            F39
                     2+14 CENTRAL PROGRAM ADDRESS
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      904
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                     2+15 CENTRAL PROGRAM ADDRESS
                     2+16 CENTRAL PROGRAM ADDRESS
            F39
                24
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      906
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                     2+17 CENTRAL PROGRAM ADDRESS
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            F39
                26
                     BIT 2
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            F40
                     Bit 1
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                     BIT 6
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            F41 23
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            F41 25
                     BIT 10
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                     BIT 9
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            F42
                     BIT 13
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        94
            F42
                     BIT 12
            F42 23
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                     2+3 CENTRAL TO PERIPHERAL DATA
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                     2+4 CENTRAL TO PERIPHERAL DATA
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            G33 24
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                     2+5 CENTRAL TO PERIPHERAL DATA
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                     2+7 CENTRAL TO PERIPHERAL DATA
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                     2+19 CENTRAL TO PERIPHERAL DATA
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                     2+20 CENTRAL TO PERIPHERAL DATA
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                     2+21 CENTRAL TO PERIPHERAL DATA
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                     2+23 CENTRAL TO PERIPHERAL DATA
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                     2+24 CENTRAL TO PERIPHERAL DATA
1W05
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2+25 CENTRAL TO PERIPHERAL DATA
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      900
            G37
1W05
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            G37 10
                     2+26 CENTRAL TO PERIPHERAL DATA
            G37
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      902
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            G37
                     2+28 CENTRAL TO PERIPHERAL DATA
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                24
                     2+29 CENTRAL TO PERIPHERAL DATA
            G37
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            G38
                     2+30 CENTRAL TO PERIPHERAL DATA
      900
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                     2+31 CENTRAL TO PERIPHERAL DATA
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            G42
                10
            G42 21
                     2+57 CENTRAL TO PERIPHERAL DATA
1W07
        94
                     2+58 CENTRAL TO PERIPHERAL DATA
1W07
        95
            G42
                24
        96
                     2+59 CENTRAL TO PERIPHERAL DATA
            G42 26
1W07
1W01
      900
            H02
                12
                      CLOCK
1W07
        97
            H29
                16
                     MEMORY MARGIN
1W06
        97
            H29
                     MEMORY MARGIN
                18
      905
            H29
                     MEMORY MARGIN
1W04
                20
            H29 22
1W05
      907
                     STORAGE MARGIN
1W05
      905
            125
                11
                     RESUME CENTRAL READ
            129
                     RESUME CENTRAL READ
1W05
      906
                  1
            129
                     EXCHANGE RESUME OUT
1W12
      900
                  5
            129
                21
                     RESUME CENTRAL WRITE
1W08
      905
1W12
      902
            130
                     CLOCK
                      EXCH GO
      903
            130
1W01
                  8
1W12
      901
            130
                23
                     MC
                      WR GO
                 25
1W01
      902
            130
1 W 0 1
      901
            130
                 27
                      RD GO
                     2+50 PERIPHERAL TO CENTRAL DATA
        92
1W08
            J33
            J33
                     2+49 PERIPHERAL TO CENTRAL DATA
1W08
        91
                     2+48 PERIPHERAL TO CENTRAL DATA
1W08
        90
            J33
                  8
                     2+53 PERIPHERAL TO CENTRAL DATA
        95
            J33
                23
1W08
        94
                     2+52 PERIPHERAL TO CENTRAL DATA
1W08
            J33
                25
        93
                     2+51 PERIPHERAL TO CENTRAL DATA
1W08
            J33 27
        98
                     2+56 PERIPHERAL TO CENTRAL DATA
1W08
            J34
        97
                     2+55 PERIPHERAL TO
                                          CENTRAL DATA
1W08
            J34
                  6
        96
                                           CENTRAL DATA
1W08
            J34
                  8
                     2+54 PERIPHERAL TO
      901
            J34
1W08
                     2+59 PERIPHERAL TO CENTRAL DATA
                23
1W08
      900
            J34 25
                     2+58 PERIPHERAL TO CENTRAL DATA
        99
                     2+57 PERIPHERAL TO CENTRAL DATA
1W08
            J34 27
```

```
904
             J35
                      2+38 PERIPHERAL TO CENTRAL DATA
1W08
                  4
                      2+37 PERIPHERAL TO CENTRAL DATA
1W08
       903
             J35
                       2+36 PERIPHERAL TO CENTRAL DATA
1W08
       902
             J35
        92
                       2+41 PERIPHERAL TO CENTRAL DATA
1W09
             J35
                 23
                      2+40 PERIPHERAL TO
1W09
        91
             J35
                 25
                                             CENTRAL DATA
                       2+39 PERIPHERAL TO CENTRAL DATA
        90
             J35
1W09
                 27
                                             CENTRAL DATA
1W09
        95
             J36
                       2+44 PERIPHERAL TO
                       2+43 PERIPHERAL TO
                                             CENTRAL DATA
1W09
        94
             J36
                   6
        93
                       2+42 PERIPHERAL TO CENTRAL DATA
1W09
             J36
                   8
                                             CENTRAL DATA
        98
                 23
                       2+47 PERIPHERAL TO
1W09
             J36
        97
                       2+46 PERIPHERAL TO CENTRAL DATA
1W09
             J36
                  25
        96
                       2+45 PERIPHERAL TO CENTRAL DATA
1W09
             J36
                 27
                      2+26 PERIPHERAL TO
2+25 PERIPHERAL TO
                                             CENTRAL DATA
1W09
       901
             J37
                                             CENTRAL DATA
             J37
1W09
       900
                   6
                       2+24 PERIPHERAL TO CENTRAL DATA
1W09
        99
             J37
                   8
             J37
                       2+29 PERIPHERAL TO CENTRAL DATA
1W09
       904
                  23
                      2+26 PERIPHERAL TO CENTRAL DATA
2+27 PERIPHERAL TO CENTRAL DATA
                 25
1W09
       903
             J37
1W09
       902
             J37
                  27
                      2+32 PERIPHERAL TO CENTRAL DATA
1W10
        92
             J38
                  4
                      2+31 PERIPHERAL TO CENTRAL DATA
1W10
        91
             J38
                   6
        90
                      2+30 PERIPHERAL TO CENTRAL DATA
             J38
1W10
                   8
        95
                       2+35 PERIPHERAL TO CENTRAL DATA
1W10
             J38 23
                      2+34 PERIPHERAL TO CENTRAL DATA
2+33 PERIPHERAL TO CENTRAL DATA
1W10
        94
             J38
                  25
        93
1W10
                  27
             J38
1W10
                      2+14 PERIPHERAL TO CENTRAL DATA
        98
             J39
                   4
                      2+13 PERIPHERAL TO CENTRAL DATA
1W10
        97
             J39
                   6
1W10
             J39
                       2+12 PERIPHERAL TO CENTRAL DATA
        96
                   8
             J39
                       2+17 PERIPHERAL TO CENTRAL DATA
1W10
       901
                  23
                      2+16 PERIPHERAL TO CENTRAL DATA
             J39
1W10
       900
                  25
        99
                      2415 PERIPHERAL TO CENTRAL DATA
             J39
                 27
1W10
                      2+20 PERIPHERAL TO CENTRAL DATA
1W10
       904
             J40
                      2+19 PERIPHERAL TO CENTRAL DATA
1W10
       903
             J40
                   6
                      2+18 PERIPHERAL TO CENTRAL DATA
2+23 PERIPHERAL TO CENTRAL DATA
1W10
       902
             J40
                   8
                 23
        92
             J40
1W11
1W11
        91
             J40
                 25
                       2+22 PERIPHERAL TO CENTRAL DATA
                      2+21 PERIPHERAL TO CENTRAL DATA
        90
             J40
                 27
1W11
                      2+2 PERIPHERAL TO CENTRAL DATA
1W11
        95
             J41
                      2+1 PERIPHERAL TO CENTRAL DATA
2+0 PERIPHERAL TO CENTRAL DATA
1W11
        94
             J41
                   6
        93
             J41
1W11
             J41 23
1W11
                       2+5 PERIPHERAL TO CENTRAL DATA
        98
                       2+4 PERIPHERAL TO CENTRAL DATA
        97
1W11
             J41 25
                       2+3 PERIPHERAL TO CENTRAL DATA
        96
             J41 27
1W11
                       2+8 PERIPHERAL TO CENTRAL DATA
2+7 PERIPHERAL TO CENTRAL DATA
             J42
1W11
       901
1W11
       900
             J42
                   6
                       2+6 PERIPHERAL TO CENTRAL DATA
        99
             J42
1W11
                   8
                      2+11 PERIPHERAL TO CENTRAL DATA
2+10 PERIPHERAL TO CENTRAL DATA
1W11
       904
             J42 23
1W11
       903
             J42 25
1W11
       902
                       2+9 PERIPHERAL TO CENTRAL DATA
             J42 27
1W14
       906
             M35
                  3
                       FUNCTION
             M35
1W13
       905
                   8
                       EMPTY
             M35
1W14
       905
                   0
                       EMPTY
             M35 13
                      FULL
1W14
       904
             M35
       903
                       INACTIVE
1W13
                 14
             M35
1W13
       904
                 15
                       FULL
1W14
       903
             M35
                  18
                       INACTIVE
1W14
             M35
                 22
                       ACTIVE
       902
1W13
       902
             M35 25
                       ACTIVE
             M36
                       CLOCK (100N SEC)
1W13
       906
        91
                       2+1 INPUT DATA
1W13
             M36
        90
                       2+0 OUTPUT DATA
1W14
             M36
                   5
        90
                       2+0 INPUT DATA
1W13
             M36
                   6
        91
                       2+1 OUTPUT DATA
1W14
             M36 11
```

```
92
1W13
             M36 23
                      2+2 INPUT DATA
1W14
                      2+3 OUTPUT DATA
        93
             M36
                 24
1W13
        93
             M36
                 27
                      2+3 INPUT DATA
1W14
        92
             M36
                 28
                      2+2 OUTPUT DATA
1W13
                      CLOCK (1 US)
2+5 INPUT DATA
       907
             M37
                   1
1W13
        95
             M37
        94
             M37
                      2+4 OUTPUT DATA
1W14
                   5
1W13
        94
             M37
                      2+4 INPUT DATA
                   6
        95
                      2+5 OUTPUT DATA
             M37
1W14
                 11
                      2+6 INPUT DATA
1W13
        96
             M37
                 23
        97
             M37
                 24
1W14
                       2±7 OUTPUT DATA
             M37
                       2+7 INPUT DATA
1W13
        97
                  27
1W14
        96
             M37
                 28
                       2+6 OUTPUT DATA
       907
                      MASTER CLEAR
             M38
1W14
                      2+9 INPUT DATA
        99
1W13
             M38
        98
                      2+8 OUTPUT DATA
1W14
             M38
                   5
1W13
        98
             M38
                      2+8 INPUT DATA
                   6
1W14
        99
                      2+9 OUTPUT DATA
             M38
                 11
1W13
       900
             M38
                 23
                      2+10 INPUT DATA
                      2+11 OUTPUT DATA
2+11 INPUT DATA
1W14
             M38 24
       901
1W13
       901
             M38
                 27
1W14
       900
             M38
                 28
                      2+10 OUTPUT DATA
             M39
                      FUNCTION
1W16
       906
                   3
1W15
       905
             M39
                      EMPTY
                      EMPTY
       905
             M39
1W16
                   9
1W16
                      FULL
       904
             M39
                 13
1W15
       903
             M39
                      INACTIVE
                 14
             M39
1W15
       904
                 15
                      FULL
1W16
       903
             M39
                       INACTIVE
                 18
             M39
1W16
       902
                 22
                       ACTIVE
             M39
1W15
       902
                 25
                       ACTIVE
1W15
             M40
                      CLOCK (100NSEC)
       906
                   1
                      2+1 INPUT DATA
        91
             M40
1W15
1W16
        90
             M40
                   5
                      2+0 OUTPUT DATA
                      2+0 INPUT DATA
1W15
        90
             M40
                   6
        91
             M40
                      2+1 OUTPUT DATA
1W16
                 11
                      2+2 INPUT DATA
1W15
        92
             M40
                 23
                       2+3 OUTPUT DATA
1W16
        93
             M40
                 24
1W15
                       2+3 INPUT DATA
        93
             M40
                 27
                      2+2 OUTPUT DATA
        92
1W16
             M40
                 28
                      CLOCK (1US)
2+5 INPUT DATA
       907
1W15
             M41
1W15
        95
             M41
        94
             M41
                      2+4 OUTPUT DATA
1W16
                   5
        94
                       2+4 INPUT DATA
1W15
             M41
                   6
                      2+5 OUTPUT DATA
1W16
        95
             M41
             M41 23
                      2+6 INPUT DATA
1W15
        96
1W16
        97
             M41
                       2+7 OUTPUT DATA
                 24
        97
                       2#7 INPUT DATA
1W15
             M41
                 27
1W16
        96
             M41
                 28
                       2+6 OUTPUT DATA
                      MASTER CLEAR
2+9 INPUT DATA
1W16
       907
             M42
                   1
1W15
        99
             M42
                       2+8 OUTPUT DATA
1W16
        98
             M42
                   5
                       2+8 INPUT DATA
1W15
        98
             M42
                   6
1W16
             M42 11
        99
                       2.9 OUTPUT DATA
1W15
       900
             M42 23
                       2+10 INPUT DATA
1W16
                       2+11 OUTPUT DATA
       901
             M42 24
1W15
       901
             M42
                 27
                       2+11 INPUT DATA
                       2+10 OUTPUT DATA
             M42.
       900
                 28
1W16
1W18
                       FUNCTION
             N35
       906
                   3
1W17
       905
             N35
                      EMPTY
       905
             N35
                      EMPTY
1W18
                   9
1W18
       904
             N35 13
                      FULL
```

```
1W17
       903
            N35 14
                     INACTIVE
1W17
       904
            N35 15
                     FULL
            N35
                     INACTIVE
       903
1W18
                18
1W18
       902
            N35 22
                     ACTIVE
1W17
            N35 25
                     ACTIVE
       902
1W17
       906
            N36
                     CLOCK (100NSEC)
                  1
1W17
       91
            N36
                     2+1 INPUT DATA
                     2+0 QUTPUT DATA
        90
            N36
1W18
                  5
        90
                     2+0 INPUT DATA
1W17
            N36
                  6
                     2+1 OUTPUT DATA
1W18
        91
            N36 11
1W17
        92
            N36 23
                     2+2 INPUT DATA
        93
                     2+3 OUTPUT DATA
1W18
            N36 24
                     2+3 INPUT DATA
2+2 OUTPUT DATA
        93
1W17
            N36
                 27
        92
            N36 28
1W18
1W17
       907
            N37
                     CLOCK (1US)
                  1
        95
            N37
                     2+5 INPUT DATA
1W17
        94
            N37
                     2+4 OUTPUT DATA
1W18
                  5
            N37
                     2+4 INPUT DATA
        94
1W17
                  6
        95
            N37
                     2+5 OUTPUT DATA
1W18
                 11
                     2+6 INPUT DATA
1W17
        96
            N37
                23
        97
            N37 24
                     2+7 OUTPUT DATA
1W18
        97
                     2+7 INPUT DATA
1W17
            N37
                 27
       96
            N37
                     2+6 OUTPUT DATA
1W18
                 28
            N38
                     MASTER CLEAR
1W18
       907
                  1
                     2+9 INPUT DATA
       99
1W17
            N38
1W18
                     2+8 OUTPUT DATA
       98
            N38
                     2+8 INPUT DATA
       98
            N38
1W17
                  6
        99
            N38
                     2+9 OUTPUT DATA
1W18
                11
1W17
       900
            N38
                 23
                     2+10 INPUT DATA
      901
                24
                     2+11 OUTPUT DATA
            N38
1W18
1W17
      901
            N38 27
                     2+11 INPUT DATA
            N38 28
                     2+10 OUTPUT DATA
      900
1W18
1W20
      906
            N39
                  3
                     FUNCTION
      905
            N39
                     EMPTY
1W19
                  8
      905
            N39
                     EMPTY
1W20
       904
            N39 13
                     FULL
1W20
            N39 14
1W19
      903
                     INACTIVE
            N39
                 15
                     FULL
1W19
      904
            N39
                     INACTIVE
      903
                 18
1W20
            N39 22
                     ACTIVE
1W20
      902
1W19
       902
            N39 25
                     ACTIVE
1W19
                     CLOCK (100NSEC)
       906
            N40
                  1
        91
            N40
                     2+1 INPUT DATA
1W19
                  4
        90
                     2+0 OUTPUT DATA
            N40
1W20
                  5
1W19
        90
            N40
                     2+0 INPUT DATA
                  6
                     2+1 OUTPUT DATA
1W20
        91
            N40 11
                     2+2 INPUT DATA
        92
            N40 23
1W19
1W20
        93
            N40
                 24
                     2+3 OUTPUT DATA
                     2+3 INPUT DATA
        93
            N40 27
1W19
            N40 28
1W20
        92
                     2+2 OUTPUT DATA
       907
                     CLOCK (1USEC)
1W19
            N41
1W19
        95
            N41
                     2+5 INPUT DATA
        94
                     2+4 OUTPUT DATA
1W20
            N41
                 -5
                     2+4 INPUT DATA
1W19
        94
            N41
            N41 11
                     2+5 OUTPUT DATA
        95
1W20
1W19
                     2+6 INPUT DATA
        96
            N41 23
        97
                     2+7 OUTPUT DATA
1W20
            N41 24
        97
            N41 27
                     2+7 INPUT DATA
1W19
        96
1W20
            N41 28
                     2+6 OUTPUT DATA
                     MASTER CLEAR
       907
            N42
1W20
                 1
1W19
        99
            N42
                     2+9 INPUT DATA
                     2+8 OUTPUT DATA
        98
1W20
            N42
                  5
```

```
98
1W19
             N42
                  6
                      2+8 INPUT DATA
             N42 11
        99
                      2+9 OUTPUT DATA
1W20
                      2+10 INPUT DATA
1W19
       900
             N42 23
                      2+11 OUTPUT DATA
1W20
             N42 24
       901
1W19
       901
             N42
                 27
                      2+11 INPUT DATA
2+10 OUTPUT DATA
1W20
       900
             N42
                 28
             035
                      FUNCTION
1W22
       906
                  3
1W21
       905
             035
                      EMPTY
                   8
       905
             035
                      EMPTY
1W22
                   9
1W22
       904
             035
                 13
                      FULL
1W21
       903
             035
                      INACTIVE
                 14
             035
1W21
       904
                      FULL
                 15
             035
                      INACTIVE
1W22
       903
                 18
             035
1W22
       902
                 22
                      ACTIVE
       902
                 25
1W21
             035
                      ACTIVE
1W21
       906
             036
                      CLOCK (100NSEC)
                   1
1W21
        91
             036
                      2+1 INPUT DATA
                      2+0 OUTPUT DATA
1W22
        90
             036
1W21
        90
             036
                      2+0 INPUT DATA
                   6
        91
                      2+1 OUTPUT DATA
             036 11
1W22
1W21
        92
             036
                 23
                      2+2 INPUT DATA
                      2+3 OUTPUT DÂTA
        93
1W22
             036
                 24
                      2+3 INPUT DATA
2+2 OUTPUT DATA
1W21
        93
             036
                 27
        92
1W22
             036
                 28
       907
             037
                      CLOCK (1USEC)
1W21
        95
             037
                      2+5 INPUT DATA
1W21
                      2+4 OUTPUT DATA
        94
             037
1W22
                   5
        94
                      2+4 INPUT DATA
1W21
             037
                   6
1W22
        95
             037
                      2+5 OUTPUT DATA
        96
1W21
             037
                 23
                      2+6 INPUT DATA
1W22
        97
             037
                 24
                      2+7 OUTPUT DATA
        97
             037
                      2+7 INPUT DATA
1W21
                 27
1W22
                 28
        96
             037
                      2+6 OUTPUT DATA
1W22
       907
             038
                      MASTER CLEAR
                      2+9 INPUT DATA
2+8 OUTPUT DATA
        99
             038
1W21
1W22
        98
             038
                   5
        98
1W21
             038
                   6
                      2+8 INPUT DATA
                      2+9 OUTPUT DATA
        99
             038
1W22
                 11
                      2+10 INPUT DATA
1W21
       900
             038
                 23
                      2+11 OUTPUT DATA
1W22
       901
             038
                 24
                      2+11 INPUT DATA
                 27
1W21
       901
             038
                      2+10 OUTPUT DATA
             038 28
1W22
       900
                      FUNCTION
             039
1W24
       906
                   3
1W23
       905
             039
                      EMPTY
       905
             039
                      EMPTY
1W24
                   9
1W24
       904
             039
                      FULL
                 13
       903
             039
                      INACTIVE
1W23
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                 15
1W23
       904
             039
                      FULL
1W24
       903
             039
                 18
                      INACTIVE
             039
       902
1W24
                      ACTIVE
                 22
1W23
       902
             039
                 25
                      ACTIVE
                      CLOCK (100NSEC)
1W23
       906
             040
                      2+1 INPUT DATA
1W23
        91
             040
1W24
        90
             040
                      2+0 OUTPUT DATA
                   5
        90
                      2+0 INPUT DATA
1W23
             040
                      2+1 OUTPUT DATA
2+2 INPUT DATA
        91
1W24
             040
        92
1W23
             040
                  23
        93
                      2+3 OUTPUT DATA
1W24
             040
                 24
1W23
        93
             040
                 27
                      2+3 INPUT DATA
        92
1W24
             040
                 28
                      2+2 OUTPUT DATA
1W23
       907
                      CLOCK (1US)
             041
                   1
1W23
        95
             041
                      2+5 INPUT DATA
```

```
1W24
        94
             041
                      2+4 OUTPUT DATA
                 5
                      2+4 INPUT DATA
1W23
        94
             041
                   6
             041 11
1W24
        95
                       2+5 OUTPUT DATA
                      2+6 INPUT DATA
        96
1W23
             041 23
                      2+7 QUTPUT DATA
2+7 INPUT DATA
1W24
        97
             041 24
1W23
        97
             041 27
1W24
        96
             041 28
                      2+6 OUTPUT DATA
                       MASTER CLEAR
1W24
       907
             042
                  1
                      2+9 INPUT DATA
        99
             042
1W23
        98
             042
                      2+8 OUTPUT DATA
1W24
        98
                      2+8 INPUT DATA
1W23
             042
             042 11
1W24
        99
                      2+9 OUTPUT DATA
                      2+10 INPUT DATA
1W23
       900
             042
                 23
                      2+11 OUTPUT DATA
1W24
       901
             042 24
                      2+11 INPUT DATA
2+10 OUTPUT DATA
1W23
       901
             042 27
1W24
             042 28
       900
             P35
                      FUNCTION
1W26
       906
                  3
             P35
                      EMPTY
1W25
       905
                   8
                      EMPTY
             P35
1W26
       905
                   9
             P35 13
1W26
       904
                      FULL
                      INACTIVE
             P35 14
1W25
       903
                      FULL
             P35
1W25
       904
                 15
1W26
       903
             P35
                 18
                      INACTIVE
                 22
       902
             P35
                      ACTIVE
1W26
             P35 25
                      ACTIVE
1W25
       902
             P36
                      CLOCK (100NSEC)
1W25
       906
                  1
        91
                      2+1 INPUT DATA
1W25
             P36
1W26
        90
             P36
                      2+0 OUTPUT DATA
                  - 5
                      2+0 INPUT DATA
        90
             P36
1W25
                   6
        91
             P36 11
                      2+1 OUTPUT DATA
1W26
             P36
                      2+2 INPUT DATA
1W25
        92
                 23
                      2+3 OUTPUT DATA
2+3 INPUT DATA
        93
1W26
             P36
                 24
        93
1W25
             P36
                 27
                      2+2 OUTPUT DATA
1W26
        92
             P36
                 28
       907
1W25
             P37
                      CLOCK (1USEC)
        95
             P37
                      2+5 INPUT DATA
1W25
        94
             P37
                      2+4 OUTPUT DATA
1W26
                   5
        94
                      2+4 INPUT DATA
1W25
             P37
                   6
        95
                      2+5 OUTPUT DATA
1W26
             P37
                  11
                      2+6 INPUT DATA
2+7 OUTPUT DATA
        96
1W25
             P37
                  23
        97
             P37
1W26
                  24
             P37
                      2+7 INPUT DATA
        97
1W25
                  27
1W26
        96
             P37
                  28
                      2+6 OUTPUT DATA
                      MASTER CLEAR
       907
1W26
             P38
        99
                      2+9 INPUT DATA
1W25
             P38
1W26
        98
             P38
                      2+8 OUTPUT DATA
        98
                      2+8 INPUT DATA
1W25
             P38
1W26
        99
             P38
                 11
                      2+9 OUTPUT DATA
1W25
       900
             P38
                 23
                      2+10 INPUT DATA
                      2+11 OUTPUT DATA
2+11 INPUT DATA
             P38
1W26
       901
                 24
             P38
1W25
       901
                 27
             P38 28
1W26
       900
                      2+10 OUTPUT DATA
1W28
       906
             P39
                      FUNCTION
                  3
1W27
       905
             P39
                      EMPTY
                   8
1W28
       905
             P39
                   9
                      EMPTY
                      FULL
       904
             P39
1W28
                 13
             P39
                      INACTIVE
1W27
       903
1W27
       904
             P39
                 15
                      FULL
       903
             P39
                      INACTIVE
1W28
                 18
             P39
                 22
                      ACTIVE
1W28
       902
1W27
       902
             P39
                      ACTIVE
                 25
1W27
       906
             P40
                      CLOCK (100NSEC)
                  1
```

```
91
1W27
             P40
                      2+1 INPUT DATA
        90
1W28
             P40
                      2+0 OUTPUT DATA
                  5
1W27
             P40
                      2+0 INPUT DATA
1W28
             P40
                      2+1 OUTPUT DATA
                      2+2 INPUT DATA
1W27
        92
             P40
                 23
1W28
        93
             P40
                 24
                      2+3 OUTPUT DATA
             P40
1W27
        93
                 27
                      2+3 INPUT DATA
1W28
        92
             P40
                 28
                      2+2 OUTPUT DATA
             P41
                      CLOCK (1US)
1W27
       907
                      2+5 INPUT DATA
1W27
        95
             P41
1W28
        94
             P41
                  5
                      2+4 OUTPUT DATA
                      2+4 INPUT DATA
1W27
        94
            P41
1W28
        95
             P41
                      2+5 OUTPUT DATA
                 11
                          INPUT DATA
1W27
        96
             P41
                 23
                      2+6
                      2+7 OUTPUT DATA
1W28
        97
             P41
                 24
        97
                      2+7
                          INPUT DATA
1W27
             P41
                 27
        96
             P41
                 28
                      2+6 OUTPUT DATA
1W28
1W28
             P42
                      MASTER CLEAR
       907
                  1
                      2+9 INPUT DATA
             P42
        99
1W27
        98
             P42
                      2+8 OUTPUT DATA
1W28
                  5
                      2+8 INPUT DATA
2+9 OUTPUT DATA
1W27
        98
             P42
        99
1W28
             P42
1W27
       900
             P42 23
                      2+10 INPUT DATA
             P42 24
                      2+11 OUTPUT DATA
1W28
       901
1W27
       901
             P42 27
                      2+11 INPUT DATA
                      2+10 OUTPUT DATA
FUNCTION
             P42 28
1W28
       900
1W30
             Q35
       906
                  3
1W29
       905
             Q35
                      EMPTY
                  8
1W30
       905
             Q35
                      EMPTY
             035 13
1W30
       904
                      FULL
             Q35
1W29
       903
                      INACTIVE
                 14
1W29
       904
             035
                 15
                      FULL
                      INACTIVE
            Q35
1W30
       903
                 18
1W30
             Q35
                      ACTIVE
       902
                 22
             035
1W29
       902
                 25
                      ACTIVE
1W29
       906
             Q36
                      CLOCK (100NSEC)
1W29
        91
             036
                      2+1 INPUT DATA
        90
                      2+0 OUTPUT DATA
1W30
             036
                  5
                      2+0 INPUT DATA
1W29
        90
             Q36
                      2+1 OUTPUT DATA
1W30
        91
             Q36
                 11
                      2+2 INPUT DATA
1W29
        92
             Q36
                 23
1W30
        93
             Q36
                 24
                      2+3 OUTPUT DATA
                      2+3 INPUT DATA
        93
1W29
             036
                 27
1W30
        92
                      2+2 OUTPUT DATA
             036 28
1W29
       907
             Q37
                      CLOCK (1US)
             Q37
1W29
        95
                      2+5 INPUT DATA
                      2+4 OUTPUT DATA
2+4 INPUT DATA
1W30
        94
             037
                  5
        94
             Q37
1W29
1W30
        95
             Q37
                      2+5 OUTPUT DATA
                 11
                      2+6 INPUT DATA
1W29
        96
             Q37
                 23
1W30
        97
            Q37
                      2+7 OUTPUT DATA
                 24
1W29
        97
             037
                 27
                      2+7
                          INPUT DATA
        96
             Q37
                      2+6 OUTPUT DATA
1W30
                 28
1W30
                      MASTER CLEAR
       907
             Q38
                  1
1W29
        99
             038
                      2+9 INPUT DATA
1W30
        98
                      2+8 OUTPUT DATA
             038
1W29
        98
             038
                      2+8
                          INPUT DATA
                      2+9 OUTPUT DATA
1W30
        99
             938
                 23
1W29
       900
             Q38
                      2+10 INPUT DATA
1W30
       901
             038
                 24
                      2+11 OUTPUT DATA
       901
             038 27
                      2+11 INPUT DATA
1W29
1W30
       900
             038 28
                      2+10 OUTPUT DATA
```

```
1W32
      906
            039
                  3
                      FUNCTION
1W31
       905
            039
                      EMPTY
                  8
      905
            Q39
1W32
                      EMPTY
                  9
1W32
       904
            039
                 13
                      FULL
                      INACTIVE
            Q39
1W31
       903
                 14
1W31
       904
            039
                 15
                      FULL
1W32
       903
            039
                 18
                      INACTIVE
1W32
       902
            039
                 22
                      ACTIVE
            039
                 25
                      ACTIVE
1W31
       902
1W31
       906
            Q40
                      CLOCK (100US)
                  1
1W31
        91
            Q40
                      2+1 INPUT DATA
        90
                      2+0 OUTPUT DATA
1W32
            Q40
                  5
                      2+0 INPUT DATA
1W31
        90
            040
                  6
                      2+1 OUTPUT DATA
       91
            040
1W32
                      2+2 INPUT DATA
1W31
        92
            Q40
                 23
                      2+3 OUTPUT DATA
1W32
        93
            040
                 24
                      2+3 INPUT DATA
2+2 OUTPUT DATA
1W31
        93
            040
                 27
        92
1W32
            Q40
                 28
      907
95
            Q41
                      CLOCK (1US)
1W31
1W31
            041
                      2+5 INPUT DATA
                      2+4 OUTPUT DATA
        94
1W32
            Q41
                  5
        94
                      2+4 INPUT DATA
1W31
            041
                  6
            Q41 11
                      2+5 OUTPUT DATA
1W32
        95
                      2+6 INPUT DATA
        96
            Q41 23
1W31
                      2+7 OUTPUT DATA
1W32
        97
            Q41 24
                      2+7 INPUT DATA
2+6 OUTPUT DATA
1W31
        97
            041 27
        96
1W32
            Q41 28
      907
            Q42
                      MASTER CLEAR
1W32
        99
            Q42
                      2+9 INPUT DATA
1W31
                      2+8 OUTPUT DATA
        98
            Q42
1W32
                  5
        98
            Q42
                      2+8 INPUT DATA
1W31
                  6
                      2+9 OUTPUT DATA
        99
1W32
            Q42
      900
            042 23
                      2+10 INPUT DATA
1W31
      901
                      2+11 OUTPUT DATA
1W32
            042
                 24
                      2+11 INPUT DATA
2+10 OUTPUT DATA
            042
                 27
1W31
      901
            042 28
1W32
      900
                      FUNCTION
1W34
            R35
      906
                  3
            R35
1W33
      905
                      EMPTY
                  8
            R35
1W34
      905
                  9
                      EMPTY
            R35
      904
                      FULL
1W34
                13
      903
            R35
                      INACTIVE
1W33
                 14
            R35
      904
                      FULL
1W33
                 15
            R35
1W34
      903
                 18
                      INACTIVE
1W34
       902
            R35
                 22
                      ACTIVE
1W33
            R35
                      ACTIVE
       902
                 25
1W33
       906
            R36
                  1
                      CLOCK (100 NSEC)
       91
                      2+1 INPUT DATA
1W33
            R36
1W34
        90
            R36
                  5
                      2+0 OUTPUT DATA
                      2+0 INPUT DATA
1W33
        90
            R36
                  6
        91
            R36
                      2+1 OUTPUT DATA
1W34
                 11
        92
                 23
                      2+2 INPUT DATA
1W33
            R36
                      2+3 OUTPUT DATA
        93
1W34
            R36
                 24
                      2+3 INPUT DATA
        93
            R36 27
1W33
                      2+2 OUTPUT DATA
1W34
        92
            R36
                 28
1W33
       907
            R37
                      CLOCK (1US)
1W33
        95
            R37
                      2+5 INPUT DATA
        94
                      2+4 OUTPUT DATA
1W34
            R37
                  5
                      2+4 INPUT DATA
            R37
        94
1W33
                  6
        95
            R37
                      2+5 OUTPUT DATA
1W34
                 11
            R37
        96
                 23
                      2+6 INPUT DATA
1W33
            R37
        97
                24
                      2+7 OUTPUT DATA
1W34
1W33
        97
            R37 27
                      2+7 INPUT DATA
```

```
96
             R37
1W34
                 28
                       2+6 OUTPUT DATA
1W34
       907
             R38
                       MASTER CLEAR
                   1
1W33
        99
             R38
                       2+9 INPUT DATA
        98
                       2+8 OUTPUT DATA
1W34
             R38
                   5
        98
                       2+8 INPUT DATA
2+9 OUTPUT DATA
1W33
             R38
                   6
1W34
        99
             R38
                  11
                       2+10 INPUT DATA
2+11 OUTPUT DATA
1W33
       900
             R38
                  23
1W34
       901
             R38
                  24
                       2+11 INPUT DATA
       901
                  27
1W33
             R38
       900
             R38
                  28
                       2+10 OUTPUT DATA
1W34
                       FUNCTION
EMPTY
1W36
       906
             R39
                   3
       905
             R39
1W35
                   8
                       EMPTY
       905
1W36
             R39
                   9
                       FULL
1W36
       904
             R39
                  13
             R39
1W35
       903
                       INACTIVE
                  14
             R39
                       FULL
1W35
       904
                  15
                       INACTIVE
1W36
       903
             R39
                  18
             R39
                       ACTIVE
1W36
       902
                  22
1W35
       902
             R39
                  25
                       ACTIVE
                       CLOCK (100NS)
2+1 INPUT DATA
1W35
       906
             R40
                   1
1W35
        91
             R40
1W36
        90
             R40
                   5
                       2+0 OUTPUT DATA
                       2+0 INPUT DATA
        90
             R40
1W35
                   6
1W36
        91
             R40
                       2+1 OUTPUT DATA
                  11
                       2+2 INPUT DATA
        92
1W35
             R40
                  23
                       2+3 OUTPUT DATA
2+3 INPUT DATA
2+2 OUTPUT DATA
1W36
        93
             R40
                  24
1W35
        93
             R40
                  27
        92
1W36
             R40
                  28
       907
                       CLOCK (1US)
1W35
             R41
                   1
                       2+5 INPUT DATA
        95
1W35
             R41
                       2+4 OUTPUT DATA
        94
             R41
                   5
1W36
                       2+4 INPUT DATA
1 W35
        94
             R41
                   6
                       2+5 OUTPUT DATA
1W36
        95
             R41
                  11
        96
             R41 23
                       2+6 INPUT DATA
1W35
                       2+7 OUTPUT DATA
        97
1W36
             R41 24
                       2+7 INPUT DATA
1W35
        97
             R41 27
1W36
        96
             R41 28
                       2+6 OUTPUT DATA
                       MASTER CLEAR
2+9 INPUT DATA
       907
1W36
             R42
1W35
        99
             R42
                       2+8 OUTPUT DATA
        98
1W36
             R42
                       2+8 INPUT DATA
        98
             R42
1W35
                   6
1W36
        99
             R42
                       2+9 OUTPUT DATA
                  11
                       2+10 INPUT DATA
       900
             R42
                  23
1W35
             R42 24
                       2+11 OUTPUT DATA
1W36
       901
                       2+11 INPUT DATA
1W35
       901
             R42 27
                       2+10 OUTPUT DATA
             R42 28
1W36
       900
```

```
2W14
          90
                A01
                       -5
                            2+30
                        7
2W14
          91
                            2+31
                A01
          92
                A01 10
                            2+32
2W14
2W14
          93
                A01 21
                            2+33
          94
                            2+34
2W14
                A01 24
          95
2W14
                A01 26
                            2+35
                            STORE CONTROL BIT 2+36
STORE CONTROL BIT 2+37
2W13
         901
                A02
                      - 5
                       7
2W13
        902
                A02
                            STORE CONTROL BIT 2+38
        903
                A02 10
2W13
                            STORE CONTROL BIT 2+39
STORE CONTROL BIT 2+40
STORE CONTROL BIT 2+41
2W13
        904
                A02 21
        905
                A02 24
2W13
2W13
        906
                A02 26
2W13
         907
                A03
                      - 5
                            STORE CONTROL BIT 2+42
                            STORE CONTROL BIT 2+43
2W13
         900
                A03
                      7
                           STORE CONTROL BIT 2*44
STORE CONTROL BIT 2*45
STORE CONTROL BIT 2*46
STORE CONTROL BIT 2*47
STORE CONTROL BIT 2*48
          90
                A03 10
2W13
                A03 21
2W13
          91
2W13
          92
                A03 24
          93
                A03 26
2W13
2W13
          94
                A 0 4
                      - 5
          95
                A04
                      7
                            STORE CONTROL BIT 2+49
2W13
                            STORE CONTROL BIT 2+50
STORE CONTROL BIT 2+51
          96
                A04 10
2W13
          97
                A04 21
2W13
          98
                A04 24
                            STORE CONTROL BIT 2+52
2W13
                A04 26
                            STORE CONTROL BIT 2+53
2W13
          99
                A05 10
          96
2W14
                            2+56
                A07
2W14
        904
                       5
                            STORE PERIPHERAL
2W14
        902
                A07
                       7
                            STORE CONTROL
                            STORE REGISTERS
                A07 10
2W14
        903
                            DATA 2+0 REG. TO MEM.
DATA 2+1 REG. TO MEM.
2W09
        900
                A08
                      5
                A 0 8
2W09
        901
                            DATA 2+2 REG. TO MEM.
DATA 2+3 REG. TO MEM.
2W09
        902
                A08 10
2W09
        903
                A08 21
2W09
        904
                A08 24
                            DATA 2+4 REG. TO MEM.
         905
                            DATA 2+5 REG. TO MEM.
2W09
                A08 26
                            DATA 2+6 REG. TO MEM.
2W09
         906
                A09
                       5
                            DATA 2+7 REG. TO MEM.
DATA 2+8 REG. TO MEM.
2W09
         907
                A 0 9
                       7
                A 0 9
          90
2W09
                     10
          91
                     21
                A09
                            DATA 2+9 REG. TO MEM.
2W09
                            DATA 2+10 REG. TO MEM.
DATA 2+11 REG. TO MEM.
DATA 2+12 REG. TO MEM.
DATA 2+13 REG. TO MEM.
          92
                A09 24
2W09
2W09
          93
                A09 26
          94
2W09
                A10
                       5
          95
2W09
                A10
                       7
                            DATA 2+14 REG. TO MEM.
          96
2W09
                A10 10
                           DATA 2+15 REG. TO MEM.
DATA 2+16 REG. TO MEM.
DATA 2+17 REG. TO MEM.
DATA 2+16 REG. TO MEM.
DATA 2+19 REG. TO MEM.
2409
          97
                A10 21
          98
2W09
                A10 24
          99
                A10 26
2W09
2W10
        900
                A11
                       5
                       7
2W10
         901
                A11
                            DATA 2+20 REG. TO MEM.
2W10
        902
                A11 10
        903
                A11 21
                            DATA 2+21 REG. TO MEM.
2W10
                            DATA 2+22 REG. TO MEM.
DATA 2+23 REG. TO MEM.
DATA 2+24 REG. TO MEM.
DATA 2+25 REG. TO MEM.
        904
                A11 24
2W10
        905
                A11 26
2W10
2W10
         906
                A12
                       5
         907
                A12
2W10
2W10
          90
                A12 10
                            DATA 2#26 REG. TO MEM.
                            DATA 2+27 REG. TO MEM.
          91
2W10
                A12 21
                            DATA 2+28 REG. TO MEM.
DATA 2+29 REG. TO MEM.
DATA 2+30 REG. TO MEM.
                A12 24
2W10
          92
2W10
          93
                A12 26
          94
2W10
                A13
                      5
                            DATA 2+31 REG. TO MEM.
2W10
          95
                A13
```

```
DATA 2+32 REG. TO MEM.
2W10
        96
            A13 10
2W10
        97
             A13
                 21
                      DATA 2+33 REG.
                                       TO
2W10
        98
                      DATA 2+34 REG.
             A13
                                       TO
                 24
2W10
                      DATA 2+35 REG. TO MEM.
2+36 REGISTER TO DISTRIBUTOR
        99
             A13 26
2W11
       906
             A14
2W11
       907
             A14
                  7
                      2+37
                           REGISTER TO DISTRIBUTOR
2W11
        90
             A14
                 10
                      2+38
                           REGISTER TO
                                         DISTRIBUTOR
                                      TO DISTRIBUTOR
        91
            A14
                           REGISTER
2W11
                      2+39
                 21
2W11
        92
            A14
                           REGISTER
                                     TO DISTRIBUTOR
                 24
                      2+40
2W11
                           REGISTER TO DISTRIBUTOR
        93
                      2+41
            A14 26
                           REGISTER TO DISTRIBUTOR
REGISTER TO DISTRIBUTOR
2W11
            A15
                      2+42
        94
        95
2W11
             A15
                      2 + 43
       96
            A15
                           REGISTER TO
2W11
                      2+44
                                         DISTRIBUTOR
                 10
                                         DISTRIBUTOR
            A15
2W12
       900
                      2+45
                           REGISTER
                                     ŢO
                 21
            A15
2W12
       901
                           REGISTER TO DISTRIBUTOR
                24
                      2+46
                           REGISTER TO DISTRIBUTOR REGISTER TO DISTRIBUTOR
2W12
            A15
      902
                 26
                      2+47
2W12
       903
             A16
                      2+48
2W12
      904
                      2+49
                                      TO DISTRIBUTOR
                  7
                           REGISTER
            A16
                                     TO DISTRIBUTOR
2W12
            A16
       905
                      2+50
                           REGISTER
                 10
                      2+51 REGISTER TO DISTRIBUTOR
2W12
       906
             A16
                21
2W12
       907
            A16
                      2+52 REGISTER TO DISTRIBUTOR
                24
                                     TO DISTRIBUTOR
2W12
        90
            A16
                      2+53 REGISTER
                 26
       91
2W12
             A17
                      2+54 REGISTER
                  5
            A17
        92
                  7
                           REGISTER TO DISTRIBUTOR
2W12
                      2+55
            A17
        93
                           REGISTER TO DISTRIBUTOR
2W12
                      2+56
                 10
2W12
        94
            A17
                      2+57 REGISTER TO DISTRIBUTOR
                 21
            A17
                           REGISTER TO DISTRIBUTOR REGISTER TO DISTRIBUTOR
2W12
        95
                 24
                      2+58
       96
            A17
2W12
                      2+59
                 26
        93
                      2+0 PERIPHERAL TO CENTRAL DATA
2W04
            801
2W04
        94
            B01
                  7
                      2+1 PERIPHERAL TO CENTRAL DATA
2W04
        95
                      2+2 PERIPHERAL TO CENTRAL DATA
            801
2W04
        96
                 21
                          PERIPHERAL TO CENTRAL DATA
            B01
                      2+3
        97
                          PERIPHERAL
                                           CENTRAL DATA
2W04
            B01
                 24
                      2+4
                                       TO
2W04
            B01
                      2+5 PERIPHERAL TO
                                          CENTRAL DATA
        98
                 26
                      2+6 PERIPHERAL TO CENTRAL DATA
2W04
        99
            B02
2W04
                          PERIPHERAL TO CENTRAL DATA
       900
            B02
                  7
                      2+7
                      2+8 PERIPHERAL TO CENTRAL DATA
2+9 PERIPHERAL TO CENTRAL DATA
      901
2W04
            B02
                 10
2W04
       902
            B02
                 21
2W04
      903
            B02
                      2+10 PERIPHERAL TO CENTRAL DATA
                 24
2W04
            B02
                      2+11 PERIPHERAL TO CENTRAL DATA
       904
                 26
2W03
        96
                      2+12 PERIPHERAL TO CENTRAL DATA
            B03
2W03
        97
                                            CENTRAL DATA
            803
                  7
                      2+13 PERIPHERAL TO
2W03
        98
            803
                      2+14 PERIPHERAL TO
                                            CENTRAL DATA
                 10
        99
                      2+15 PERIPHERAL
                                            CENTRAL DATA
2W03
            B03
                 21
                                        TO
2W03
                 24
                           PERIPHERAL
                                            CENTRAL DATA
       900
            B03
                      2+16
                                        TO
2W03
       901
            B03
                 26
                      2+17 PERIPHERAL TO
                                            CENTRAL DATA
2W03
                      2+18 PERIPHERAL TO
                                            CENTRAL DATA
       902
            B04
2W03
       903
            B04
                  7
                      2+19
                           PERIPHERAL TO
                                            CENTRAL DATA
                                            CENTRAL DATA
2W03
      904
            B 0 4
                           PERIPHERAL
                 10
                      2+20
                                         TO
2W04
       90
            B 0 4
                      2+21 PERIPHERAL
                                        ŤΟ
                                            CENTRAL DATA
                 21
                                            CENTRAL DATA
        91
                      2+22 PERIPHERAL TO
2W04
            804
                 24
        92
2W04
            B04
                 26
                      2+23 PERIPHERAL TO
                                            CENTRAL DATA
        99
            B05
                      2+24 PERIPHERAL TO
                                            CENTRAL
2W02
                  5
2W02
            805
                                            CENTRAL DATA
       900
                      2+25 PERIPHERAL
                                        TO
2W02
            805
                      2+26 PERIPHERAL TO
                                            CENTRAL DATA
      901
                 10
            805
2W02
      902
                 21
                      2+27 PERIPHERAL TO
                                            CENTRAL DATA
            B05
                      2+28 PERIPHERAL
                                            CENTRAL DATA
2W02
      903
                 24
                                        TO
2W02
       904
            B05
                 26
                           PER PHERAL
                                            CENTRAL DATA
                      2 * 29
                                        TO
       90
                                            CENTRAL DATA
                           PERIPHERAL
2W03
            B06
                      2+30
                                         TO
       91
2W03
            B06
                      2+31 PERIPHERAL
                                         ŤO
                                            CENTRAL DATA
2W03
       92
            B06
                 10
                      2+32 PERIPHERAL
                                        TO
                                            CENTRAL DATA
                      2+33 PERIPHERAL TO CENTRAL DATA
       93
2W03
            B06
                 21
```

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94
                    2+34 PERIPHERAL TO CENTRAL DATA
2W03
            806 24
       95
                     2+35 PERIPHERAL TO CENTRAL DATA
2W03
            806
                26
                                          CENTRAL DATA
2W01
      902
            B07
                     2+36 PERIPHERAL
                                      TO
2W01
            B07
                     2+37 PERIPHERAL
                                      ŤO
                                          CENTRAL DATA
      903
                     2+38 PERIPHERAL
2W01
      904
            B07
                                      TO
                                          CENTRAL DATA
                10
                     2+39 PERIPHERAL TO
                                          CENTRAL DATA
       90
            B07 21
2W02
                     2+40 PERIPHERAL
2W02
       91
            B07
                24
                                      TO
                                          CENTRAL DATA
                                          CENTRAL DATA
2W02
       92
            B07
                26
                     2+41 PERIPHERAL
                                      TO
                                          CENTRAL DATA
       93
                     2+42 PERIPHERAL
2M05
            808
                                      TO
                 5
                                          CENTRAL DATA
        94
            808
                     2+43 PERIPHERAL
                                      TO
2W02
                                         CENTRAL DATA
2W02
        95
                     2+44 PERIPHERAL
                                      TO
            B08
                10
                21
                     2+45 PERIPHERAL
                                          CENTRAL DATA
2W02
        96
            808
                                      TO
       97
                     2+46 PERIPHERAL
                                      TO
                                          CENTRAL DATA
2W02
            808
                24
2W02
       98
            808
                26
                     2-47 PERIPHERAL
                                      ŤO
                                          CENTRAL DATA
                     2+48 PERIPHERAL TO
                                          CENTRAL DATA
2W01
       90
            B09
                 5
       91
                                          CENTRAL DATA
                     2+49 PERIPHERAL TO
            B09
                 7
2W01
       92
            809
                     2+50 PERIPHERAL TO
                                          CENTRAL DATA
2W01
                10
                     2+51 PERIPHERAL TO
                                         CENTRAL DATA
       93
2W01
            B09
                21
            B09
                     2+52 PERIPHERAL TO CENTRAL DATA
2W01
       94
                24
2W01
       95
            B 0 9
                26
                     2+53 PERIPHERAL TO CENTRAL DATA
       96
                     2+54 PERIPHERAL TO CENTRAL DATA
2W01
            B10
                 5
2W01
       97
            810
                 7
                     2+55 PERIPHERAL TO CENTRAL DATA
2W01
       98
            810
                10
                     2+56 PERIPHERAL TO
                                          CENTRAL DATA
       99
                     2-57 PERIPHERAL TO CENTRAL DATA
                21
2W01
            B10
      900
                     2+58 PERIPHERAL TO CENTRAL DATA
2W01
            B10 24
                     2+59 PERIPHERAL TO CENTRAL DATA
2W01
      901
            B10 26
       92
                     2+2 DATA TO MEMORY FAN OUT
2W05
            812
                 4
                     2+1 DATA TO MEMORY FAN OUT 2+0 DATA TO MEMORY FAN OUT
       91
2W05
            B12
                 6
       90
2W05
            B12
                 8
       95
                     2+5 DATA TO MEMORY FAN OUT
2W05
            812
                23
2W05
        94
            812 25
                     2+4 DATA TO MEMORY FAN OUT
       93
                     2+3 DATA TO MEMORY FAN OUT
2W05
            B12 27
                     2+8 DATA TO MEMORY FAN OUT
2W05
       98
            813
                 4
                     2+7 DATA TO MEMORY FAN OUT
       97
2W05
            B13
                 6
       96
                     2+6 DATA TO MEMORY FAN OUT
2W05
            B13
                 8
                     2+11 DATA TO MEMORY FAN OUT
2W05
      901
            B13 23
                     2+10 DATA TO MEMORY FAN OUT
2W05
      900
            B13 25
       99
                     2+9 DATA TO MEMORY FAN OUT
2W05
            B13 27
                     2414 DATA TO MEMORY FAN OUT
2W05
      904
            B14
                 - 4
            B14
                     2+13 DÃTA TO MEMORY FAN OUT
2W05
      903
                     2+12 DATA TO MEMORY FAN OUT
            B14
2W05
      902
                 8
       92
            B14 23
                     2+17 DATA TO MEMORY FAN OUT
2W06
                     2+16 DATA TO MEMORY FAN OUT
2W06
       91
            B14 25
                                   MEMORY FAN OUT
2W06
       90
            B14
                27
                     2+15 DATA TO
2W06
            B15
                     2+20 DATA TO
                                   MEMORY FAN OUT
       95
                 4
                                   MEMORY FAN OUT
2W06
       94
            815
                     2+19 DATA TO
                 6
       93
                     2+18 DATA TO
                                   MEMORY FAN OUT
            815
2W06
                 8
2W06
       98
            815
                23
                     2+23 DATA TO
                                   MEMORÝ FAN OUT
                                   MEMORY FAN OUT
       97
                     2+22 DATA TO
            815
2W06
                25
       96
            815
                     2+21 DATA TO MEMORY FAN OUT
2W06
                27
                     2+26 DATA TO MEMORY FAN OUT
      901
2W06
            B16
                - 4
                     2+25 DATA TO MEMORY FAN OUT
2W06
      900
            B16
                 - 6
       99
                     2+24 DATA TO MEMORY FAN OUT
2W06
            B16
                 8
2W06
      904
            B16
                23
                     2+29 DATA TO
                                   MEMORY FAN OUT
                                   MEMORY FAN OUT
2W06
      903
            B16
                25
                     2+28 DATA TO
      902
                     2+27 DATA TO
                                   MEMORY FAN DUT
2W06
            B16 27
                                   MEMORY FAN OUT
       92
            B17
                     2+32 DATA TO
2W07
       91
            B17
                     2+31 DATA TO
                                   MEMORY FAN OUT
2W07
       90
            B17
                     2+30 DATA TO
                                   MEMORY FAN OUT
2W07
                 A
            817
       95
                                   MEMORY FAN OUT
                23
2W07
                     2+35
                         DATA TO
       94
            B17
                     2+34 DATA TO MEMORY FAN OUT
2W07
                25
       93
            817 27
                     2+33 DATA TO MEMORY FAN OUT
2W07
```

```
2W07
                      2+38 DATA TO MEMORY FAN OUT
             818
        97
                      2+37 DATA TO MEMORY FAN OUT
2W07
             B18
                   6
        96
                      2+36 DATA TO MEMORY FAN OUT
2W07
             B18
                 23
                           DATA TO
                                     MEMORY FAN
                                                  OUT
2W07
       901
             818
                      2+41
2W07
       900
             B18
                 25
                      2+40
                           DATA TO
                                     MEMORY FAN
                                                  OUT
                                     MEMORY FAN
        99
                      2+39 DATA TO
                                                  OUT
2W07
                 27
             B18
                                     MEMORY FAN
                                                  OUT
2W07
       904
             B19
                      2+44
                           DATA TO
                      2+43 DATA TO
                                     MEMORY FAN
       903
             B19
                                                  OUT
2W07
2W07
       902
             B19
                      2+42 DATA TO
                                     MEMORY FAN
                                                  DUT
                  8
                                     MEMORY FAN
2W08
        92
             B19
                 23
                      2+47
                            DATA TO
                                                  OUT
             B19
                                     MEMORY FAN
        91
                      2+46 DATA TO
                                                  OUT
2W08
                 25
             819
                                     MEMORY FAN
2W08
        90
                 27
                      2+45 DATA TO
                      2+50 DATA TO
2W08
        95
             B20
                                     MEMORY FAN
                                                  OUT
                                     MEMORY FAN
        94
                      2+49 DATA TO
                                                  001
             820
2W08
        93
                                     MEMORY FAN OUT
2W08
             B20
                      2+48 DATA TO
                  8
                      2+53 DATA TO
                                     MEMORY FAN
2W08
        98
             B20
                                                  OUT
                 23
                      2+52 DATA TO
                                     MEMORY FAN
        97
                                                  DUT
2W08
             820
                 25
                                     MEMORY FAN
2W08
        96
                      2+51 DATA TO
                                                  OUT
             820
                 27
                                     MEMORY FAN
2W08
       901
             B21
                      2+56 DATA TO
                                                  OUT
             821
                      2+55 DATA TO
                                     MEMORY FAN
                                                  DUT
2W08
       900
                   6
2W08
        99
             B21
                  8
                      2+54 DATA TO
                                     MEMORY FAN
                                                  OUT
                      2+59 DATA TO MEMORY FAN
                                                  OUT
2W08
       904
             B21
                 23
                      2+58 DATA TO MEMORY FAN OUT
2+57 DATA TO MEMORY FAN OUT
2W08
       903
             821
                 25
2W08
       902
             B21 27
2W08
       905
             B22 16
                      MEMORY MARGIN
                      MEMORY MARGIN
             B22 18
2W07
       905
      905
                      MEMORY MARGIN
2W06
             B22 20
                      MEMORY MARGIN
2W05
       905
             B22 22
2W01
       905
             C03
                      RESUME CENTRAL WRITE
      907
                      MASTER CLEAR
2W20
             C16 12
2W20
       905
             C17
                      EMPTY
                 12
2W19
       903
             C32
                      INACTIVE
2W19
       904
             C32
                      FULL
                  8
2W19
        92
             C33
                      2+2 INPUT DATA
        91
                      2+1 INPUT DATA
             C33
2W19
                      2+0 INPUT DATA
2+5 INPUT DATA
2W19
        90
             C33
                  8
2W19
        95
             C33
                 23
2W19
        94
                      2+4 INPUT DATA
             C33
                 25
        93
2W19
             C33
                 27
                      2+3 INPUT DATA
        98
             C34
                      2+8 INPUT DATA
2W19
                      2+7 INPUT DATA
2+6 INPUT DATA
        97
2W19
             C34
        96
2W19
             C34
                  8
             C34 23
                      2+11 INPUT DATA
2W19
       901
2W19
       900
             C34
                      2+10 INPUT DATA
                 25
        99
             C34 27
                      2+9 INPUT DATA
2W19
                      BOOLEAN REQUEST RELEASE
2W21
       906
             E01
                  6
                      DIVIDE REQUEST RELEASE
2W21
       905
             E01
2W14
       906
             E01 23
                      EXP = 3777 ERROR
                      EXP = 1777 ERROR
2W14
       905
             E01.
                 25
                      ERROR (EXP = 1777 FROM CHASSIS 7)
ERROR (EXP = 3777 FROM CHASSIS 8)
2W22
      908
             E02
                  - 5
2W23
       908
             E02
2W14
       901
             122 12
                      CLOCK
2W21
        93
                      SUM
             J01
                  . 5
                      DIFFERENCE
        94
                  7
2W21
             J01
2W21
        95
                      PRODUCT
             J01 10
2W21
        98
             J01 21
                      TRANSFER
        97
                      COMPLEMENT
2W21
             J01
                 24
2W21
        91
             J02
                  .7
                      ROUND
        96
2W21
             J02 10
                      POP. COUNT
                      ERROR MULT.
        98
2W32
             J03
                  1
       900
                      ERROR MULT.
2W32
             J03
                   6
2W32
        99
             J03 23
                      ERROR MULT.
```

```
2W32
       901
             J03 28
                       ERROR MULT. 2
                       2+0 DIVIDE TO MULTIPLY RESULT
2W24
        90
             K01
                       2+1 DIVIDE TO MULTIPLY RESULT
2W24
        91
             K01
                       2+2 DIVIDE TO MULTIPLY RESULT
        92
2W24
             K01
                  23
        93
                                        MULTIPLY
2W24
             KO1
                  28
                       2+3 DIVIDE TO
                                                   RESULT
                                        MULTIPLY
                                                   RESULT
        94
                       2+4 DIVIDE TO
2W24
             K02
2W24
        95
             K02
                       2+5 DIVIDE TO MULTIPLY
                                                   RESULT
                       2+6 DIVIDE TO MULTIPLY RESULT 2+7 DIVIDE TO MULTIPLY RESULT
2W24
        96
             K02
                  23
        97
2W24
             K02
                  28
                       2+8 DIVIDE TO MULTIPLY RESULT
2W24
        98
             K03
                   1
                       2+9 DIVIDE TO MULTIPLY RESULT 2+10 DIVIDE TO MULTIPLY RESULT
2W24
        99
             K03
             K03
2W24
       900
                  23
                       2+11 DIVIDE TO MULTIPLY RESULT 2+12 DIVIDE TO MULTIPLY RESULT
             K03
2W24
       901
                  28
2W24
       902
             K04
                   1
2W24
                       2+13 DIVIDE TO MULTIPLY RESULT
       903
             KO4
                       2+14 DIVIDE TO MULTIPLY RESULT
2+15 DIVIDE TO MULTIPLY RESULT
2+16 DIVIDE TO MULTIPLY RESULT
2W24
       904
             K04 23
       905
             K04
2W24
                  28
2W24
       906
             K05
                   1
                                      TO MULTIPLY RESULT
2W24
       907
             K05
                       2+17 DIVIDE
                   6
2W25
        90
             K05
                  23
                       2+18 DIVIDE TO MULTIPLY RESULT
                       2+19 DIVIDE TO MULTIPLY RESULT
        91
             K05
2W25
                  28
        92
2W25
                       2+20 DIVIDE TO MULTIPLY RESULT
             K06
                   1
                       2+21 DIVIDE TO MULTIPLY RESULT
2+22 DIVIDE TO MULTIPLY RESULT
2W25
        93
             K06
                    6
        94
                  23
2W25
             K06
                       2+23 DIVIDE TO MULTIPLY RESULT
        95
             K06
2W25
                  28
                       2+24 DÍVIDE TO MULTIPLY RESULT
2+25 DÍVIDE TO MULTIPLY RESULT
2+26 DÍVIDE TO MULTIPLY RESULT
        96
             K<sub>0</sub>7
2W25
                   1
        97
             K07
2W25
        98
             K07
2W25
                  23
                                     TO MULTIPLY RESULT
                       2+27 DIVIDE
2+28 DIVIDE
2W25
        99
             KO7
                  28
2W25
             K08
       900
                   1
                                     TO MULTIPLY RESULT
2W25
       901
             KO8
                       2+29 DÍVIDE
                   6
                       2+30 DIVIDE TO MULTIPLY RESULT
2W25
                 23
       902
             K08
                                      TO MULTIPLY RESULT
2W25
       903
             KO8
                  28
                       2+31 DIVIDE
                                      TO MULTIPLY RESULT
             K09
                       2+32 DIVIDE
2W25
       904
                   1
       905
             K09
2W25
                       2+33 DIVIDE TO MULTIPLY RESULT
                   6
                       2+34 DIVIDE TO MULTIPLY RESULT
             K09
2W25
       906
                  23
                       2+35 DIVIDE TO MULTIPLY RESULT
2W25
       907
             K09
                  28
                                      TO MULTIPLY RESULT
                       2+36 DIVIDE
2+37 DIVIDE
        90
2W26
             K10
                   1
                                     TO MULTIPLY RESULT
        91
2W26
             K10
                       2+38 DÍVIDE TO MULTIPLY RESULT
        92
                  23
             K10
2W26
                       2+39 DIVIDE TO MULTIPLY RESULT
2W26
        93
             K10
                  28
                       2+40 DIVIDE TO MULTIPLY RESULT
        94
2W26
             K11
                   1
                                      TO MULTIPLY RESULT
        95
                       2+41 DIVIDE
2W26
             K11
                    4
                                      TO MULTIPLY RESULT
        96
2W26
             K11
                  23
                       2+42 DIVIDE
        97
                                     TO MULTIPLY RESULT
2W26
                       2+43 DIVIDE
             K11
                  28
                                     TO MULTIPLY RESULT
2W26
        98
             K12
                       2+44 DIVIDE
                   1
        99
                       2+45 DIVIDE TO MULTIPLY RESULT
2W26
             K12
                   6
                       2+46 DIVIDE TO MULTIPLY RESULT 2+47 DIVIDE TO MULTIPLY RESULT
2W26
       900
             K12 23
2W26
       901
             K12
                  28
                       2+48 EXPONENT RESULT-REG.
             K13
2W22
       902
                   1
                       2+49 EXPONENT RESULT-REG.
2W22
       903
             K13
                   6
2W22
       904
             K13 23
                       2+50 EXPONENT RESULT-REG.
                       2+51 EXPONENT RESULT-REG.
       905
             K13 28
2W22
                       2+52 EXPONENT RESULT-REG.
2W22
       906
             K14
                   1
2W22
       907
             K14
                       2+53 EXPONENT RESULT-REG.
             K14
                       2+54 EXPONENT RESULT-REG
2W23
       902
                  23
2W23
       903
             K14
                  28
                       2+55 EXPONENT RESULT=REG
             K15
                       2+56 EXPONENT RESULT-REG
       904
2W23
                   1
             K15
                       2+57 EXPONENT RESULT-REG
2W23
       905
                   . 6
             K15
2W23
       906
                  23
                       2-58 EXPONENT RESULT-REG
       907
             K15
                       2+59 EXPONENT RESULT-REG
                  28
2W23
                       2+40 MULTIPLY TO DIVIDE OPERAND X++J
       907
             L06
2W31
                 12
```

```
2W32
         90
                        2+41 MULTIPLY TO DIVIDE X++J
              L06 14
2W32
        91
              L06 16
                        2+42 MULTIPLY TO DIVIDE X++J
              L06
                        2+43 MULTIPLY TO DIVIDE X++J
2W32
         92
                  18
2W32
         93
              L07 12
                        2+44 MULTIPLY TO DIVIDE X++J
                        2+45 MÜLTIPLY TO DIVIDE X++J
2+46 MÜLTIPLY TO DIVIDE X++J
2+47 MÜLTIPLY TO DIVIDE X++J
         94
              L07 14
2W32
2W32
         95
              L07 16
              L07
2W32
         96
                   18
         90
              L08
                        2+48 X++J (EXPONENT)
2W23
                   12
                        2+49 X++J (EXPONENT)
2W23
         91
              L08
                  14
2W23
         92
              L08 16
                        2+50 X++J (EXPONENT)
                        2+51 X++J (EXPONENT)
2W23
         93
                  18
              L08
        94
             L09
2W23
                  12
                        2+52 X++J (EXPONENT)
             L09
        95
                        2+53 X++J (EXPONENT)
2W23
                  14
              L09 16
2W23
         96
                        2+54 X++J (EXPONENT)
        97
              L09 18
                        2+55 X++J (EXPONENT)
2W23
                        2+56 X++J (EXPONENT)
2+57 X++J (EXPONENT)
         98
2W23
              L10 12
2W23
        99
              L10
                  14
                        2+58 X++J (EXPONENT)
2W23
       900
             L10
                  16
2W23
       901
                        2+59 X++J (EXPONENT)
              L10 18
       903
                        Go DIVÍDE
2W21
              L11 12
2W21
       900
             L11 14
                        TRANSMIT DIVIDE
2W21
       902
              L11
                        GO BOOLEAN
                  16
                        TRANSMIT BOOLEAN
2+40 MULTIPLY TO DIVIDE (OPERAND) X++K
        99
2W21
             L11 18
2W29
        94
              M06 12
        95
                        2+41 MULTIPLY TO DIVIDE (OPERAND) X**K
2W29
              M06
                  14
                       2+42 MULTIPLY TO DIVIDE (OPERAND) X++K
2+43 MULTIPLY TO DIVIDE (OPERAND) X++K
2+44 MULTIPLY TO DIVIDE (OPERAND) X++K
2+45 MULTIPLY TO DIVIDE (OPERAND) X++K
2+46 MULTIPLY TO DIVIDE (OPERAND) X++K
             M06 16
2W29
        96
        97
2W29
              M06
                  18
        98
              M07 12
2W29
             M07 14
        99
2W29
2W29
       900
              M07 16
                        2+47 MULTIPLY TO DIVIDE (OPERAND) X++K
             M07 18
2W29
       901
2W22
        90
              M 0 8
                        2+48 X++K (EXPONENT)
                  12
        91
             80M
                        2+49 X++K (EXPONENT)
2W22
                  14
         92
              M08 16
                        2+50 X++K (EXPONENT)
2W22
                        2+51 X++K (EXPONENT)
        93
             M08 18
2W22
                        2+52 X++K (EXPONENT)
        94
              M09 12
2W22
2W22
        95
              M 0 9
                        2+53 X++K (EXPONENT)
                  14
             M09
2W22
        96
                  16
                        2+54 X++K (EXPONENT)
        97
              MD9
                        2+55 X++K (EXPONENT)
2W22
                  18
                        2+56 X++K (EXPONENT)
2W22
        98
              M10
                  12
2W22
        99
             M10 14
                        2+57 X++K (EXPONENT)
                        2+58 X++K (EXPONENT)
       900
2W22
              M10 16
                        2+59 X++K (EXPONENT)
2W22
       901
              M10
                  18
                        GO MULT 1
       905
              M11 14
2W26
2W26
       906
              M11 16
                        GO MULT 2
2W30
             N06 12
                        2+20 MULTIPLY TO DIVIDE OPERAND X++J
       905
                        2+21 MULTIPLY TO DIVIDE OPERAND X++J
2+22 MULTIPLY TO DIVIDE OPERAND X++J
2W30
       906
              N06 14
2W30
       907
              NO6 16
2W31
        90
              N06 18
                        2+23 MULTIPLY TO DIVIDE OPERAND X++J
                        2+24 MULTIPLY TO DIVIDE OPERAND X++J
2W31
        91
              No7 12
        92
                        2+25 MULTIPLY TO DIVIDE OPERAND X++J
              NO7 14
2W31
        93
              NO7 16
                        2+26 MULTIPLY TO DIVIDE OPERAND X++J
2W31
                        2+27 MULTIPLY TO DIVIDE OPERAND X++J
2+28 MULTIPLY TO DIVIDE OPERAND X++J
2W31
        94
              No7
                   18
             N 0 8
2W31
        95
                  12
                        2+29 MULTIPLY TO DIVIDE OPERAND X++J
        96
2W31
              NO8
                  14
                        2+30 MULTIPLY TO DIVIDE OPERAND X++J
        97
2W31
              NO8 16
                        2+31 MULTIPLY TO DIVIDE OPERAND X++J
2W31
        98
              N08 18
                        2+32 MULTIPLY TO DIVIDE OPERAND X++J
2W31
        99
              N 0 9
                  12
             N 0 9
                        2+33 MULTIPLY TO DIVIDE OPERAND X++J
       900
2W31
                   14
                        2+34 MULTIPLY TO DIVIDE OPERAND X++J
2W31
       901
             N09
                  16
                        2+35 MULTIPLY TO DIVIDE OPERAND X++J
2+36 MULTIPLY TO DIVIDE OPERAND X++J
2W31
       902
              N09
                  18
       903
             N10 12
2W31
```

```
2+37 MULTIPLY TO DIVIDE OPERAND X++J
2W31
       904
             N10 14
2W31
       905
             N10 16
                       2+38 MULTIPLY TO DIVIDE OPERAND X++J
                       2+39 MULTIPLY TO DIVIDE OPERAND X++J
2W31
       906
             N10 18
                       TRANSMIT MUIT
2W21
       907
             009
2W21
       908
             009 21
                       TRANSMIT MUIT
                       2+20 MULTIPLY TO DIVIDE (OPERAND) X++K
        92
             P06 12
2W28
2W28
        93
             P06 14
                       2+21 MULTIPLY TO DIVIDE (OPERAND) X++K
                       2+22 MULTIPLY TO DIVIDE (OPERAND) X++K
2+23 MULTIPLY TO DIVIDE (OPERAND) X++K
2W28
        94
             P06
                  16
        95
             P06 18
2W28
                       2+24 MULTIPLY TO DIVIDE (OPERAND) X++K
2W28
        96
             P07 12
                       2+25 MULTIPLY TO DIVIDE
2+26 MULTIPLY TO DIVIDE
2+27 MULTIPLY TO DIVIDE
             P07
2W28
        97
                                                   (OPERAND) X++K
                  14
             P07 16
        98
                                                    (OPERAND)
                                                                X**K
2W28
                                                    (OPERAND) X++K
        99
             P07
2W28
                  18
                                           DIVIDE
             P 0 8
                       2+28 MULTIPLY TO
                                                    (OPERAND) X++K
2W28
       900
                  12
                       2+29 MULTIPLY TO DIVIDE
                                                   (OPERAND) X++K
                 14
2W28
       901
             P08
                       2+30 MULTIPLY TO DIVIDE
2+31 MULTIPLY TO DIVIDE
                                                   (OPERAND) X++K
             P08 16
       902
2W28
             P08 18
2W28
       903
                       2+32 MULTIPLY TO DIVIDE (OPERAND) X++K
             P09
2W28
       904
                 12
                       2+33 MULTIPLY TO DIVIDE (OPERAND) X++K
2W28
             P09
       905
                  14
             P09 16
                       2+34 MULTIPLY TO DIVIDE (OPERAND) X++K
       906
2W28
       907
             P09 18
                       2+35 MULTIPLY TO DIVIDE
                                                   (OPERAND) X++K
2W28
2W29
        90
             P10 12
                       2+36 MULTIPLY TO DIVIDE (OPERAND) X++K
                       2+37 MULTIPLY TO DIVIDE (OPERAND) X++K
2W29
        91
             P10
                  14
                       2+38 MULTIPLY TO DIVIDE (OPERAND) X++K
2W29
        92
             P10
                 16
                       2+39 MULTIPLY TO DIVIDE (OPERAND) X++K
2W29
        93
             P10
                 18
2W29
       903
             006 12
                       2+0 MULTIPLY TO DIVIDE (OPERAND) X++J
                       2+1 MULTIPLY TO DIVIDE (OPERAND) X++J
2W29
       904
             006 14
                       2+2 MULTIPLY TO DIVIDE (OPERAND) X++J
             Q06 16
Q06 18
2W29
       905
                       2+3 MULTIPLY TO DIVIDE (OPERAND) X++J
2W29
       906
                       2+4 MULTIPLY TO DIVIDE (OPERAND) X++J
2W29
       907
             Q07 12
                       2+5 MULTIPLY TO DIVIDE OPERAND X++J
             Q07 14
2W30
        90
                       2+6 MULTIPLY TO DIVIDE OPERAND X++J
2+7 MULTIPLY TO DIVIDE OPERAND X++J
        91
             Q07 16
Q07 18
2W30
2W30
        92
        93
             008 12
                       2+8 MULTIPLY TO DIVIDE OPERAND X++J
2W30
                       2+9 MULTIPLY TO DIVIDE OPERAND X++J
2+10 MULTIPLY TO DIVIDE OPERAND X++J
2W30
        94
             008 14
        95
             Q08 16
2W30
                       2+11 MULTIPLY TO DIVIDE OPERAND X++J
2+12 MULTIPLY TO DIVIDE OPERAND X++J
2W30
        96
             008 18
        97
             009
2W30
                 12
                       2+13 MULTIPLY TO DIVIDE OPERAND X++J
        98
             009 14
2W30
             009 16
                       2+14 MULTIPLY TO DIVIDE OPERAND X++J
        99
2W30
                       2+15 MULTIPLY TO DIVIDE OPERAND X++J
2+16 MULTIPLY TO DIVIDE OPERAND X++J
2W30
       900
             009 18
2W30
       901
             Q10 12
                       2+17 MULTIPLY TO DIVIDE OPERAND X++J
2+18 MULTIPLY TO DIVIDE OPERAND X++J
2W30
       902
             Q10 14
2W30
       903
             Q10
                  16
                       2+19 MULTIPLY TO DIVIDE OPERAND X++J
2W30
       904
             Q10 18
2W26
       907
             011
                       D P MULT 1
                   7
                       REDUCE MULT. 1
2W25
       908
             Q11
2W26
       908
             Q12
                   5
                       D P MULT 2
                   7
                       REDUCE MULT 2
2W26
       902
             Q12
2W27
        90
             R06 12
                       2+0 MULTIPLY TO DIVIDE (OPERAND) X++K
                       2+1 MULTIPLY TO DIVIDE (OPERAND) X++K
2W27
        91
             R06 14
                       2+2 MULTIPLY TO DIVIDE (OPERAND) X++K
2W27
        92
             R06 16
                       2+3 MULTIPLY TO DIVIDE (OPERAND) X++K
2+4 MULTIPLY TO DIVIDE (OPERAND) X++K
             R06 18
2W27
        93
2W27
        94
             R07
                  12
             R07
2W27
        95
                       2+5 MULTIPLY TO DIVIDE (OPERAND) X++K
                  14
             R07 16
                       2+6 MULTIPLY TO DIVIDE (OPERAND) X++K
        96
2W27
                       2+7 MULTIPLY TO DIVIDE (OPERAND) X++K
        97
             R07 18
2W27
                       2+8 MULTIPLY TO DIVIDE (OPERAND) X++K
             R08 12
2W27
        98
                       2+9 MULTIPLY TO DIVIDE (OPERAND) X++K
2+10 MULTIPLY TO DIVIDE (OPERAND) X++K
        99
2W27
             R08
                 14
2W27
       900
             R08
                 16
                       2+11 MULTIPLY TO DIVIDE (OPERAND) X++K
2W27
       901
             R08 18
                       2+12 MULTIPLY TO DIVIDE (OPERAND) X++K
2W27
       902
             R09 12
```

```
2+13 MULTIPLY TO DIVIDE (OPERAND) X++K
2W27
           903
                    R09 14
                    R09 16
R09 18
R10 12
                                  2+14 MULTIPLY TO DIVIDE (OPERAND) X++K
2+15 MULTIPLY TO DIVIDE (OPERAND) X++K
2+16 MULTIPLY TO DIVIDE (OPERAND) X++K
2+17 MULTIPLY TO DIVIDE (OPERAND) X++K
           904
905
2W27
2W27
           906
2W27
2W27
           907
                    R10 14
2W28
            90
                    R10 16
            91
2W28
                    R10 18
```

## CHASSIS 3

```
3W03
      905
            C14 14
                     MEMORY MARGIN
                     CLOCK
            G35 12
3W20
      900
                     2+3 CHASSIS 3 SELECT (000)
3W20
      904
            G38
                     2+2 CHASSIS 3 SELECT (000)
3W20
      903
            G38
3W20
      905
            G38 14
                     2+4
3W05
      906
            038
                 20
                     WRITE
3W05
      905
            G38 21
                     GO
3W20
            G39 18
                     2+0 BANK SELECT
      901
            G39 22
                     2+1 BANK SELECT
3W20
      902
        97
3W39
            G39 28
                     ACCEPT
3W20
      906
            G40 11
                      2+5 ADDRESS TO MEMORY
            G40 14
                      2+6 ADDRESS TO MEMORY
3W20
       907
            G40 17
                     2+7 ADDRESS TO MEMORY
3W20
      908
                     2+8 ADDRESS TO MEMORY
3W20
        90
            G40 18
       91
            G41 11
                     2+9 ADDRESS TO MEMORY
3W20
3W20
        92
            G41 14
                     2+10 ADDRESS TO MEMORY
                     2+11 ADDRESS TO MEMORY
3W20
        93
            G41 17
                     2+12 ADDRESS TO MEMORY
        94
            G41 18
3W20
                     2+13 ADDRESS TO MEMORY
3W20
        95
            G42 11
        96
                     2+14 ADDRESS TO MEMORY
            G42 14
3W20
                     2+15 ADDRESS TO MEMORY
2+16 ADDRESS TO MEMORY
3W20
        97
            G42 17
        98
3W20
            G42 18
            H19
3W39
      900
                     2+0
                     2+0 FAN OUT TO MEMORY
            H19
3W15
      900
3W39
      901
            H19
                     2+1
3W15
                     2+1 FAN OUT TO MEMORY
      901
            H19
                19
3W15
      902
            H19
                 24
                     2+2 FAN OUT TO MEMORY
            H19
3W39
                     2+2
      902
                 28
3W39
      903
            H20
                     2+3
                 1
                     2+3 FAN OUT TO MEMORY
3W15
      903
            H20
3W39
      904
            H20
                     2+4
                 8
                     2+4 FAN OUT TO MEMORY
2+5 FAN OUT TO MEMORY
3W15
       904
            H20
                 19
      905
            H20
3W15
                 24
      905
3W39
            H20
                 28
                     2+5
                     2+6
      906
3W39
            H21
                     2+6 FAN OUT TO MEMORY
3W15
      906
            H21
                  2
3W39
      907
            H21
                     2+7
      907
                 19
                     2+7 FAN OUT TO MEMORY
3W15
            H21
        90
                     2+8 FAN OUT TO MEMORY
3W15
            H21 24
        90
3W39
            H21 28
                      2+8
3W39
        91
            H22
                      2+9
                  1
                     2+9 FAN OUT TO MEMORY
3W15
        91
            H22
                  2
                     2+10
3W39
        92
            H22
3W15
        92
            H22
                 19
                      2+10 FAN OUT TO MEMORY
3W15
        93
            H22 24
                     2+11 FAN OUT TO MEMORY
3W39
        93
            H22
                     2+11
                 28
3W39
        94
            H24
                      2+12
                  1
                      2+12 FAN OUT TO MEMORY
3W15
        94
            H24
                  '2
        95
            H24
3W39
                     2+13
        95
                     2+13 FAN OUT TO MEMORY
            H24
3W15
                 19
                     2+14 FAN OUT TO MEMORY
        96
            H24
3W15
                 24
3W39
        96
            H24
                 28
                     2+14
            H25
3W06
      900
                     2+15
            H25
                      2+15 FAN OUT TO MEMORY
3W16
      900
            H25
3W06
      901
                     2+16
            H25 19
3W16
      901
                     2+16 FAN OUT TO MEMORY
      902
            H25
                 24
                      2+17 FAN OUT TO MEMORY
3W16
            H25
                     2+17
3W06
      902
                 28
3W06
      903
            H26
                     2*18
                  1
3W16
      903
            H26
                     2+18 FAN OUT TO MEMORY
```

```
904
            H26
                      2+19
3W06
                 8
                      2+19 FAN OUT TO MEMORY
3W16
       904
            H26 19
                      2+20 FAN OUT TO MEMORY
3W16
       905
            H26 24
                      2+20
3W06
       905
            H26
                28
3W06
       906
            H27
                      2+21
                      2+21 FAN OUT TO MEMORY
3W16
       906
            H27
                  2
3W06
       907
            H27
                  8
                      2+22
       907
            H27
                      2+22 FAN OUT TO MEMORY
3W16
                 19
        90
            H27
                      2+23 FAN OUT TO MEMORY
3W16
        90
            H27
3W06
                      2+23
        91
            H29
3W06
                      2+24
                      2+24 FAN OUT TO MEMORY
3W16
        91
            H29
                  2
3W06
        92
            H29
                      2+25
                  8
        92
            H29
3W16
                      2+25 FAN OUT TO MEMORY
                 19
                      2+26 FAN OUT TO MEMORY
            H29
3W16
                 24
        93
                      2+26
            H29
3W06
                 28
3W06
        94
            H30
                      2+27
                  1
                      2+27 FAN OUT TO MEMORY
        94
3W16
            H30
        95
3W06
            H30
                      2+28
        95
                      2+28 FAN OUT TO MEMORY
3W16
            H30
                 19
3W16
        96
            H30
                      2+29 FAN OUT TO MEMORY
                 24
3W06
        96
            H30
                 28
                      2 + 29
       900
            H31
3W02
                      2 * 30
                      2+30 FAN OUT TO MEMORY
3W17
       900
            H31
3W02
       901
            H31
                      2+31
                      2+31 FAN OUT TO MEMORY
      901
3W17
            H31
                 19
3W17
       902
            H31
                 24
                      2+32 FAN OUT TO MEMORY
       902
            H31
                      2+32
3W02
                 28
3W02
       903
            H32
                      2+33
3W17
       903
            H32
                      2+33 FAN OUT TO MEMORY
3 W 0 2
       904
                      2+34
            H32
                  8
                      2+34 FAN OUT TO MEMORY
2+35 FAN OUT TO MEMORY
                 19
3W17
       904
            H32
      905
3W17
            H32
                 24
       905
3W02
            H32
                 28
                      2+35
3W02
       906
            H34
                      2*36
            H34
                      2+36 FAN OUT TO MEMORY
3W17
       906
                  2
            H34
3W02
       907
                  8
                      2+37
       907
            H34
                      2+37 FAN OUT TO MEMORY
3W17
                 19
                 24
3W17
        90
            H34
                      2+38 FAN OUT TO MEMORY
        90
            H34
3W02
                 28
                      2+38
3W02
        91
            H35
                      2+39
3W17
            H35
                      2+39 FAN OUT TO MEMORY
        91
                  2
        92
            H35
3W02
                      2+40
3W17
        92
            H35
                      2+40 FAN OUT TO MEMORY
                 19
3W17
        93
            H35
                      2+41 FAN OUT TO MEMORY
            H35
        93
                      2+41
3W02
                 28
        94
                      2+42
3W02
            H36
        94
                      2+42 FAN OUT TO MEMORY
3W17
            H36
        95
3W02
                      2+43
            H36
3W17
        95
            H36
                 19
                      2+43 FAN OUT TO MEMORY
        96
                      2+44 FAN OUT TO MEMORY
3W17
            H36
                 24
            H36 28
3W02
        96
                      2+44
3W04
       900
            H37
                      2+45
            H37
3W18
       900
                      2+45 FAN OUT TO MEMORY
                  2
                      2+46
            H37
3W04
       901
                  8
            H37
                      2+46 FAN OUT TO MEMORY
3W18
       901
                 19
3W18
       902
            H37
                      2+47 FAN OUT TO MEMORY
                 24
3W04
       902
            H37
                 28
                      2+47
            H39
3W04
       903
                      2+48
            H39
                      2+48 FAN OUT TO MEMORY
3W18
       903
                  2
3W04
       904
            H39
                      2+49
      904
            H39
3W18
                      2+49 FÄN OUT TO MEMORY
                 19
```

```
3W18
      905
            H39 24
                     2+50 FAN OUT TO MEMORY
       905
            H39
                      2+50
3W04
                 28
                      2+51
3W04
       906
             H40
3W18
             H40
                      2+51 FAN OUT TO MEMORY
       906
                  2
3W04
       907
             H40
                      2+52
            H40
                      2+52 FAN OUT TO MEMORY
       907
3W18
                 19
3W18
        90
             H40
                 24
                      2+53 FAN OUT TO MEMORY
3W04
        90
             H40
                 28
                      2+53
        91
3W04
            H41
                      2+54
        91
            H41
                  2
                      2+54 FAN OUT TO MEMORY
3W18
        92
             H41
                      2 + 55
3W04
                      2+55 FAN OUT TO MEMORY
2+56 FAN OUT TO MEMORY
3W18
        92
            H41
                19
3W18
        93
            H41
                 24
                      2+56
3W04
        93
            H41
                 28
3W04
        94
            H42
                      2+57
        94
                      2+57 FAN OUT TO MEMORY
3W18
            H42
                  2
3W04
        95
            H42
                  8
                      2+58
        95
                      2+58 FAN OUT TO MEMORY
            H42
3W18
                 19
        96
                 24
            H42
                      2459 FAN OUT TO MEMORY
3W18
3W04
        96
            H42
                 28
                      2+59
        98
            123
                      ACCEPT
SWOO
                  3
3W39
        97
             123
                  - 5
                      ACCEPT
        99
                      ACCEPT
3W07
             123
        97
                      ACCEPT FROM CHAS. 10
3W04
            123
                  9
        97
                      MEMORY ACCEPT
3W15
             123
                 19
                      ACCEPT
            123
3W16
        97
                21
        97
             123
                 23
                      ACCEPT
3W17
        97
             123
                      ACCEPT
3W18
                 25
        99
                      ACCEPT
3W20
            123
                 28
3W06
        97
             124
                      READ PERIPHERAL
                 12
            125
        90
                      2+0 CENTRAL TO PERIPHERAL DATA
3W03
        90
             125
3W05
                  3
                      2+0
             125
       900
                      2+0
3W39
3W07
            125
       900
                  7
                      2+0
             125
3W08
       900
                      2+0
             125
       900
                      2+0
3W09
                 11
       900
             125
                      2+0
3W10
                 19
             125
                      2 + 0
3W11
       900
                 21
             125
                 23
                      2+0
3W13
       900
             125
3W14
       904
                 26
                      2+0
3W12
       900
             125 28
                      2+0 MEMORY TO REGISTER
                      2+1 CENTRAL TO PERIPHERAL DATA
3W03
        91
             126
3W05
        91
             126
                  3
                      2+1
                      2+1
             126
3W39
       901
3W07
       901
             126
                      2+1
                      2+1
3W08
       901
             126
                  9
             126 11
                      2+1
3W09
       901
3W10
       901
             126
                 19
                      2+1
                      2+1
3W11
       901
             126
                 21
3W13
       901
             126
                 23
                      2+1
3W14
       905
             126
                 26
                      2+1
                      2+1 MEMORY TO REGISTER
3W12
             126 28
       901
        92
                      2+2 CENTRAL TO PERIPHERAL DATA
3W03
             127
             127
3W05
        92
                  3
                      2+2
             127
                      2+2
3W39
       902
                  5
3W07
       902
             127
                      2+2
3W08
       902
             127
                      2+2
                      2+2
3W09
       902
             127
3W10
       902
             127
                      2+2
                 19
             127
                      2+2
       902
3W11
                 21
            127
       902
                 23
                      2+2
3W13
3W14
       906
             127 26
                      2+2
```

```
3W12
     902
            127 28 2+2 MEMORY TO REGISTER
                1 2+3 CENTRAL TO PERIPHERAL DATA
3W03
      93
            128
                 3 2+3
5 2+3
7 2+3
3W05
       93
            128
3W39
      903
            128
3W07
      903
            128
3W08
            128
      903
                 9
                    2+3
3W09
      903
            128 11
                     2+3
3W10
      903
            128
                     2+3
                19
3W11
      903
            128
                21
                     2+3
                    2+3
2+3
2+3 MEMORY TO REGISTER
3W13
      903
            128
                23
3W14
      907
            128 26
3W12
      903
            128 28
       94
                     2+4 CENTRAL TO PERIPHERAL DATA
3W03
            129
       94
3W05
            129
                     2+4
3W39
      904
            129
                - 5
                     2+4
3W07
      904
            129
                     2+4
3 W D 8
      904
            129
                9
                     2+4
            129 11
      904
                     2+4
3W09
3W10
      904
            129 19
                     2+4
3W11
      904
            129 21
                     2+4
            129 23
3W13
      904
                     2 + 4
                    2+4 MEMORY TO REGISTER
2+5 CENTRAL TO BETTER
            129 26
3W14
      900
            129 28
3W12
      904
       95
            132
                    2+5 CENTRAL TO PERIPHERAL DATA
3W03
3W05
       95
            132
                     2+5
      905
3W39
            132
                 5
                     2+5
3W07
      905
            132
                 7
                     2+5
BOWE
      905
            132
                     2+5
      905
                    2+5
3W09
            132 11
3W10
      905
            132 19
                     2+5
      905
3W11
            132 21
                     2+5
      905
            132 23
3W13
                     2+5
           132 26
       90
3W14
                     2+5
                    2+5 MEMORY TO REGISTER
3W12
      905
            132 28
       96
                    2+6 CENTRAL TO PERIPHERAL DATA
3W03
           133
3W05
       96
           133
                3
                     2+6
3W39
      906
            133
                 5
                    2 * 6
                 7 2+6
3W07
      906
            133
3W08
      906
            133
                     2+6
3W09
      906
            133 11
                    2+6
            133 19
3 W 1 0
      906
                     2 + 6
3W11
      906
            133 21
                     2+6
                    2+6
3W13
      906
            133 23
3W14
       91
            133 26
                     2+6
                    2+6 MEMORY TO REGISTER
            133 28
3W12
      906
3W03
       97
            134
                     2+7 CENTRAL TO PERIPHERAL DATA
       97
3W05
            134
                     2+7
3W39
      907
            134
                     2+7
      907
3W07
            134
                     2+7
3W08
      907
            134
                     2+7
                 9
3W09
      907
            134 11
                     2+7
      907
                     2+7
3W10
            134
                19
            134 21
3W11
      907
                     2+7
      907
            134 23
                     2+7
3W13
3W14
      92
            134 26
                     2+7
                    2+7 MEMORY TO REGISTER
            134 28
3W12
      907
3W03
       98
            135
                     2+8 CENTRAL TO PERIPHERAL DATA
           135
                    2+8
3W05
3W39
       90
            135
                     2+8
           135
3W07
       90
                7
                     2+8
                9
3 W D 8
       90
            135
                     2+8
            135 11
3W09
       90
                     2+8
```

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3W11
         90
             135 21
                       2+8
             135 23
                      2+8
3W13
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3W14
         93
             135
                  26
                       2+8
             135
                      2+8 MEMORY TO REGISTER
3W12
         90
                  28
3W03
         99
             136
                      2+9 CENTRAL TO PERIPHERAL DATA
3W05
         99
             136
                   .3
                      2+9
3W39
         91
                       2+9
             136
3W07
         91
                       2+9
             136
        91
                      2+9
3W08
             136
                   9
3W09
         91
             136 11
                      2+9
3W10
         91
             136
                 19
                       2+9
3W11
         91
             136
                       2+9
                  21
        91
3W13
             136 23
                      2+9
         94
3W14
             136 26
                      2+9
                      2+9 MEMORY TO REGISTER
             136
3W12
        91
                  28
3W03
             138
                       2+10 CENTRAL TO PERIPHERAL DATA
       900
                   1
                      2+10
3W05
       900
             138
                   3
                      2+10
3W39
        92
             138
                   5
        92
                      2+10
3W07
             138
         92
             138
                  9
                      2+10
3W08
3W09
         92
             138 11
                      2+10
3W10
         92
             138
                 19
                      2+10
         92
3W11
             138
                  21
                       2 * 10
3W13
         92
             138 23
                      2+10
3W14
3W12
         95
             138
                      2+10
                  26
             138
                      2+10 MEMORY TO REGISTER
         92
                  28
                      2+11 CENTRAL TO PERIPHERAL DATA
3W03
       901
             139
                   1
3W05
       901
             139
                      2+11
        93
                      2+11
3W39
             139
                   5
3W07
         93
             139
                      2+11
             139
                      2+11
         93
3W08
                  . 9
             139
3W09
         93
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         93
             139
3W10
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                      2+11
             139
         93
                 21
                      2+11
3W11
         93
             139
3W13
                  23
                      2+11
             139
         96
                      2-11
3W14
                  26
3W12
         93
             139
                  28
                       2+11 MEMORY TO REGISTER
                      2+12 CENTRAL TO PERIPHERAL DATA
3W03
             140
       902
             140
                      2+12
3W05
       902
 3W39
        94
             140
                       2+12
                      2+12
         94
                   7
             140
3W07
                      2+12
         94
             140
                   9
3W08
3W09
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              140
                       2+12
                  11
         94
             140
                      2+12
3W10
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3W11
                 21
         94
             140
                       2+12
         94
             140 23
                      2+12
3W13
3W14
         97
             140
                  26
                      2+12
                      2+12 MEMORY TO REGISTER
3W12
        94
             140
                  28
                       2+13 CENTRAL TO PERIPHERAL DATA
3W03
       903
             141
3W05
       903
             141
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        95
             141
                      2+13
3W39
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-3W07
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                      2+13
         95
BOWE
             141
                   9
                      2+13
        95
3W09
             141
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                       2+13
         95
3W10
             141 19
                      2+13
         95
             141 21
3W11
                      2+13
                      2+13
3W13
         95
             141 23
         98
3W14
             141
                  26
                      2+13
         95
                      2+13 MEMORY TO REGISTER
             141 28
3W12
3W03
       904
             142
                       2+14 CENTRAL TO PERIPHERAL DATA
                  1
       904
             142
                       2+14
3W05
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3W39
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             142
                       2+14
        96
             142
3W07
        96
                       2+14
             142
                   9
3W08
3W09
        96
             142 11
                       2+14
3W10
        96
             142
                       2+14
        96
3W11
                       2+14
             142
                  21
        96
             142 23
3W13
                       2+14
3W14
        99
             142 26
                       2+14
                       2+14 MEMORY TO REGISTER
        96
             142 28
3W12
3W21
       900
             J37 27
                        CLOCK
             J39
                        EXCHANGE GO
3W21
       903
                   8
       902
             J39
                        WRITE GO
3W21
                   9
3W21
       901
             J39 10
                        READ GO
                        EXCHANGE GO (PERIPH)
WRITE GO (PERIPH)
             J39
3W24
       902
                  24
3W24
       901
             J39
                  26
                        READ GO (PERIPH)
             J39
3W24
       900
                  28
                        BIT 2
BIT 1
3W23
       903
             J40
                   3
             J40
3W23
       902
                   .5
                   7
3W23
             J40
                        BIT 0
       901
       906
             J40
                        BIT
                             5
3W22
                   8
                        BIT 4
BIT 3
       905
3W22
             J40
                   9
             J40 10
3W22
       904
             J40 21
3W22
       903
                        Bif 2
             J40 22
3W22
       902
                        BIT 1
3W22
       901
             J40 23
                        BIT 0
3W23
       906
             J40
                  24
                        BIT 5
       905
                        BIT 4
3W23
             J40
                  26
3W23
       904
             J40
                  28
                        Bif 3
                        BIT 8
BIT 7
3W23
       900
             J41
                   3
3W23
       908
             J41
                   Š
                        BIT 6
BIT 11
3W23
       907
             J41
                   7
        93
             J41
3M25
                   8
3W22
        92
             J41
                        BIT 10
        91
                        BIT 9
             J41 10
3W22
             J41 21
                        BIT 8
BIT 7
3W22
       900
             J41 22
3W22
       908
             J41 23
                        BIT 6
3W22
       907
3W23
        93
             J41 24
                        BIT 11
        92
             J41 26
3W23
                        BIT 10
3W23
        91
             J41 28
                        BIT 9
                        BIT 14
BIT 13
        96
3W23
             J42
                   3
        95
3W23
             J42
                   5
3W23
        94
             J42
                        BIT 12
3W22
        99
             J42
                   8
                        BIT 17
3W22
        98
             J42
                   9
                        BIT 16
        97
             J42 10
                        BIT 15
3W22
         96
             J42
                 21
3W22
                        BIT 14
         95
3W22
             J42 22
                        BIT 13
        94
             J42 23
3W22
                        BIT 12
        99
3W23
             J42 24
                        BIT 17
                        BIT 16
BIT 15
3W23
        98
             J42
                  26
        97
             J42 28
3W23
```

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4W07
       907
            C14 14
                      STORAGE MARGIN
            G01 11
4W20
       906
                      2+5 ADDRESS TO MEMORY
4W20
       907
             G01 14
                      2+6 ADDRESS TO MEMORY
                      2+7 ADDRESS TO MEMORY
4W20
       908
            G01 17
                      2+6 ADDRESS TO MEMORY
2+9 ADDRESS TO MEMORY
4W20
        90
            G01 18
4W20
        91
             G02
                 11
        92
                      2+10 ADDRESS TO MEMORY
4W20
            G02
                 14
                      2+11 ADDRESS TO MEMORY
4W20
        93
            002 17
        94
                      2+12 ADDRESS TO MEMORY
4W20
            G02 18
            603 11
        95
                      2+13 ADDRESS TO MEMORY
4W20
            G03 14
G03 17
                      2+14 ADDRESS TO MEMORY
2+15 ADDRESS TO MEMORY
4W20
        96
        97
4W20
                      2+16 ADDRESS TO MEMORY
4W20
            G03 18
        98
                      2+3 CHASSIS 4 SELECT (001)
4W20
            G04
       904
4W20
       903
            G04
            G04 14
                      2+4
4W20
       905
            G04 20
4W39
       906
                      WRITE
            G04 21
4W39
       905
                      GO
                      2+0 BANK SELECT
            G05 18
4W20
       901
4W20
       902
            005 22
                      2+1 BANK SELECT
                      ACCEPT
            G05 28
4W06
        98
       900
            G13 12
                      CLOCK
4W20
        90
4W05
            H01
                      2+0
                  1
                      2+0 FAN OUT TO MEMORY
4W03
       900
            H01
                  . 2
        91
                      2+1
4W05
            HO1
                  8
                      2+1 FAN OUT TO MEMORY
4W03
       901
            H01 19
                      2+2 FAN OUT TO MEMORY
4W03
       902
            H01 24
        92
4W05
            H01 28
                      2+2
4W05
        93
             H02
                  1 2
                      2+3
            H02
                      243 FAN OUT TO MEMORY
4W03
       903
4W05
        94
            H02
                  8
                      2+4
                      2+4 FAN OUT TO MEMORY
       904
            H02
                 19
4W03
                      2+5 FAN OUT TO MEMORY
4W03
       905
            H02 24
        95
                      2+5
4W05
            H02 28
                      2+6
4W05
        96
            H03
                  1
            H03
                      2+6 FAN OUT TO MEMORY
4W03
       906
                  2
        97
                      2+7
            H03
4W05
                  8
                      2+7 FAN OUT TO MEMORY
       907
            H03 19
4W03
                      2+8 FAN OUT TO MEMORY
4W03
        90
            H03 24
4W05
        98
            H03 28
                      2+8
4W05
        99
             H04
                      2+9
                  1
        91
            H04
                      2.9 FAN OUT TO MEMORY
4W03
                  2
             H04
4W05
       900
                      2+10
                  8
                      2+10 FAN OUT TO MEMORY
4W03
        92
             H04
                 19
4W03
        93
             H04
                 24
                      2+11 FAN OUT TO MEMORY
4W05
       901
             H04
                 28
                      2+11
4W05
       902
             H06
                      2+12
                      2+12 FAN OUT TO MEMORY
4W03
        94
            H06
                  2
       903
             H06
                      2+13
4W05
                      2+13 FAN OUT TO MEMORY
4W03
        95
             H06 19
        96
                      2+14 FAN OUT TO MEMORY
4W03
            H06 24
       904
            H06 28
4W05
                      2+14
4W39
        90
            H07
                      2+15
            H07
                      2+15 FAN OUT TO MEMORY
       900
4W04
                  2
4W39
        91
            H07
                      2+16
            H07
                      2+16 FAN OUT TO MEMORY
4W04
       901
                 19
            H07
                      2+17 FAN OUT TO MEMORY
4W04
       902
                 24
            H07
4W39
        92
                 28
                      2+17
        93
4W39
             H08
                      2+18
                  ž
                      2+18 FAN OUT TO MEMORY
4W04
       903
             H08
```

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4W39
        94
            H08
                     2+19
                  8
4W04
            H08 19
      904
                     2+19 FAN OUT TO MEMORY
4W04
       905
            H08 24
                     2+20 FAN OUT TO MEMORY
       95
4W39
            H08
                 28
                     2#20
4W39
       96
            H09
                     2+21
                  1
            H09
                     2+21 FAN OUT TO MEMORY
4W04
       906
                  2
            H09
       97
4W39
                     2 + 22
4W04
      907
            H09 19
                     2+22 FAN OUT TO MEMORY
4W04
       90
            H09 24
                     2+23 FAN OUT TO MEMORY
4W39
       98
            H09
                     2+23
                28
       99
4W39
            H11
                     2+24
                  1
       91
4W04
                     2+24 FAN OUT TO MEMORY
            H11
                  2
4W39
      900
            H11
                     2+25
                  8
4W04
       92
            H11
                 19
                     2+25 FAN OUT TO MEMORY
       93
4W04
            H11 24
                     2+26 FAN OUT TO MEMORY
4W39
      901
            H11
                     2+26
                 28
4W39
      902
            H12
                     2+27
4W04
       94
            H12
                     2+27 FAN OUT TO MEMORY
                  2
4W39
      903
            H12
                     2+28
4404
       95
            H12 19
                     2+28 FAN OUT TO MEMORY
4W04
       96
            H12
                 24
                     2+29 FAN OUT TO MEMORY
4W39
      904
            H12 28
                     2+29
4W08
      900
            H13
                     2+30
                  1
                     2+30 FAN OUT TO MEMORY
4W09
      900
            H13
                  2
4 W 0 8
      901
            H13
                     2+31
                  8
4W09
      901
            H13
                 19
                     2+31 FAN OUT TO MEMORY
                24
4W09
            H13
                     2+32 FAN OUT TO MEMORY
      902
                     2+32
4W08
      902
            H13 28
4 W 0 8
      903
            H14
                     2+33
                  1
4W09
      903
            H14
                     2+33 FAN OUT TO MEMORY
                  2
      904
            H14
4W08
                  8
                     2+34
            H14 19
4W09
      904
                     2+34 FAN OUT TO MEMORY
      905
            H14 24
                     2+35 FAN OUT TO MEMORY
4W09
4W08
      905
            H14 28
                     2+35
4W08
      906
            H16
                     2+36
4W09
      906
            H16
                  2
                     2+36 FAN OUT TO MEMORY
4W08
      907
            H16
                     2+37
                     2+37 FAN OUT TO MEMORY
4W09
      907
            H16 19
       90
4W09
            H16
                24
                     2+38 FAN OUT TO MEMORY
       90
4W08
            H16
                 28
                     2+38
       91
            H17
4W08
                     2+39
            H17
4W09
       91
                  2
                     2+39 FAN OUT TO MEMORY
            H17
       92
4W08
                  8
                     2+40
            H17
4W09
       92
                     2+40 FAN OUT TO MEMORY
                 19
            H17
                     2+41 FAN OUT TO MEMORY
4W09
       93
                 24
4W08
            H17
       93
                     2+41
                 28
       94
            H18
4W08
                     2+42
       94
                     2+42 FAN OUT TO MEMORY
4W09
            H18
                  2
4W08
        95
            H18
                     2+43
                  8
       95
4W09
            H18
                 19
                     2+43 FAN OUT TO MEMORY
4W09
       96
            H18
                 24
                     2+44 FAN OUT TO MEMORY
4W08
       96
            H18
                     2+44
                 28
            H19
      900
                     2+45
4W11
4W10
      900
            H19
                     2+45 FAN OUT TO MEMORY
                  2
            H19
4W11
      901
                     2+46
            H19
4W10
      901
                     2+46 FAN OUT TO MEMORY
                19
4W10
      902
            H19
                 24
                     2+47 FAN OUT TO MEMORY
            H19
                     2+47
4W11
      902
                 28
4W11
      903
            H21
                     2+48
                  1
4W10
      903
            H21
                     2+48 FAN OUT TO MEMORY
                  2
4W11
      904
            H21
                     2+49
                  8
      904
4W10
            H21 19
                     2+49 FAN OUT TO MEMORY
```

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2+50 FAN OUT TO MEMORY
4W10
      905
            H21 24
4W11
      905
            H21 28
                     2+50
                     2+51
4W11
            H22
      906
                     2+51 FAN OUT TO MEMORY
4W10
      906
            H22
                 2
4W11
      907
            H22
                  8
                     2+52
4W10
      907
            H22 19
                     2+52 FAN OUT TO MEMORY
4W10
        90
            H22 24
                     2+53 FAN OUT TO MEMORY
4W11
            H22 28
        90
                     2+53
4W11
        91
            H23
                     2+54
                 1
       91
            H23
                     2+54 FAN OUT TO MEMORY
4W10
                  2
        92
4W11
            H23
                     2+55
                 8
4W10
        92
            H23 19
                     2+55 FAN OUT TO MEMORY
4W10
        93
            H23 24
                     2+56 FAN OUT TO MEMORY
4W11
        93
            H23 28
                     2+56
        94
                     2+57
4W11
            H24
4W10
        94
            H24
                     2+57 FAN OUT TO MEMORY
                 2
        95
                     2+58
4W11
            H24
        95
            H24 19
                     2+58 FAN OUT TO MEMORY
4W10
            H24 24
        96
4W10
                     2+59 FAN OUT TO MEMORY
4W11
        96
            H24
                28
                     2+59
                     2+15 CENTRAL TO PERIPHERAL DATA
        90
4W07
            101
                 1
        90
4W39
                     2+15
            101
                 3
4W06
      900
            101
                     2+15
                 7
                     2+15
4W12
      900
            101
4W13
                 9
      900
            101
                     2+15
4W15
      900
            101 11
                     2+15
                     2+15
      900
4W16
            101 19
4W17
      900
            101 21
                     2+15
4W21
      900
            101 23
                     2+15
4W14
      904
            101 26
                     2+15
                     2+15 MEMORY TO REGISTER
4W18
      900
            101
                28
       91
                     2+16 CENTRAL TO PERIPHERAL DATA
4W07
            102
                 1
4W39
       91
            102
                     2 + 16
                 3
                     2+16
4W06
      901
            102
                  5
                  7
4W12
      901
            102
                     2+16
                     2+16
4W13
      901
            102
                 9
                     2+16
4W15
            102 11
      901
4W16
      901
            102
                     2+16
                19
                     2+16
            102 21
4W17
      901
4W21
      901
            102 23
                     2+16
4W14
      905
            102
                26
                     2+16
                     2+16 MEMORY TO REGISTER
            102 28
4W18
      901
                           CENTRAL TO PERIPHERAL DATA
4W07
       92
            103
                     2+17
                  1
       92
4W39
            103
                     2+17
4W06
      902
            103
                  5
                     2+17
4W12
      902
            103
                  7
                     2+17
                     2+17
      902
            103
                  9
4W13
            103 11
4W15
      902
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4W16
      902
            103
                19
                     2+17
      902
            103 21
                     2+17
4W17
4W21
      902
            103
                23
                     2+17
                     2+17
4W14
      906
            103
                26
            103 28
                     2+17 MEMORY TO REGISTER
4W18
      902
                     2+18 CENTRAL TO PERIPHERAL DATA
4W07
       93
            104
            104
       93
4W39
                     2+18
                  3
4W06
      903
            104
                  5
                     2+18
            104
                     2+18
      903
4W12
            104
                     2+18
4W13
      903
                 9
            104 11
4W15
      903
                     2+18
      903
            104 19
                     2+18
4W16
4W17
      903
            104
                21
                     2+18
            104 23
4W21
      903
                     2+18
```

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4W14
      907
            104 26 2+18
                     2+18 MEMORY TO REGISTER
4W18
      903
            104 28
                     2+19 CENTRAL TO PERIPHERAL DATA
4W07
       94
            105
                 1
4W39
        94
                     2+19
            105
4W06
            105
      904
                     2+19
      904
            105
4W12
                     2+19
4W13
            105
      904
                 9
                     2+19
      904
            105
                     2+19
4W15
                11
            105
      904
                     2+19
4W16
                19
            105
4W17
      904
                21
                     2+19
4W21
            105
      904
                23
                     2+19
      900
            105 26
                     2+19
4W14
            105 28
                     2+19 MEMORY TO REGISTER
4W18
      904
                     2+20 CENTRAL TO PERIPHERAL DATA
       95
4W07
            108
4W39
        95
            108
                     2+20
                 3
4W06
      905
            108
                     2+20
      905
            108
                     2#20
4W12
4W13
      905
            108
                     2+20
4W15
      905
            108 11
                     2+20
      905
4W16
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                19
                     2+20
4W17
      905
            108
                21
                     2 * 20
            108 23
4W21
      905
                     2+20
            108
                26
4W14
       90
                     2+20
                     2+20 MEMORY TO REGISTER
4W18
      905
            108 28
                     2+21 CENTRAL TO PERIPHERAL DATA
            109
       96
4W07
        96
            109
4W39
                     2+21
            109
4W06
      906
                     2+21
                     2+21
4W12
      906
            109
                 7
4W13
      906
            109
                     2+21
            109 11
4W15
      906
                     2+21
4W16
      906
            109
                     2+21
                19
4W17
      906
            109 21
                     2 * 21
            109
4W21
      906
                23
                     2+21
4W14
       91
            109
                26
                     2+21
            109
                     2+21 MEMORY TO REGISTER
4W18
      906
                28
            110
                     2+22 CENTRAL TO PERIPHERAL DATA
4W07
        97
                  1
        97
4W39
            110
                     2+22
      907
            110
4W06
                     2+22
4W12
      907
            110
                     2 + 22
      907
4W13
            110
                  9
                     2+22
4W15
      907
            110 11
                     2+22
4W16
      907
            I10 19
                     2+22
4W17
      907
            110 21
                     2+22
4W21
      907
            110
                23
                     2+22
       92
4W14
            110
                26
                     2*22
4W18
      907
            110
                28
                     2+22 MEMORY TO REGISTER
                     2+23 CENTRAL TO PERIPHERAL DATA
        98
4W07
            111
4W39
        98
            111
                     2*23
        90
                     2+23
4W06
            111
4W12
        90
                     2+23
            111
                     2+23
4W13
        90
            111
        90
4W15
            111 11
                     2+23
            111 19
                     2+23
4W16
        90
4W17
        90
                     2+23
            111
                21
        90
4W21
            111
                23
                     2+23
4W14
        93
            111 26
                     2+23
                     2+23 MEMORY TO REGISTER
4W18
        90
            111
                28
4W07
        99
            112
                     2+24 CENTRAL TO PERIPHERAL DATA
4W39
        99
            112
                     2+24
        91
            112
                  5
                     2+24
4W06
       91
            112
                     2+24
4W12
4W13
        91
            112
                  9
                     2 + 24
```

```
112 11
4W15
        91
                     2+24
4W16
        91
            112 19
                     2+24
4W17
        91
            112 21
                     2+24
-4W21
        91
            112 23
                     2+24
4W14
        94
            112 26
                     2+24
                     2+24 MEMORY TO REGISTER
        91
4W18
            112 28
                     2+25 CENTRAL TO PERIPHERAL DATA
4W07
       900
            114
4W39
       900
            114
                  3
                     2+25
       92
                     2+25
4406
            114
                  5
4W12
        92
                  7
            114
                     2+25
4W13
        92
            114
                 9
                     2+25
                     2+25
4W15
        92
            114 11
        92
            I14 19
                     2+25
4W16
        92
4W17
            114 21
                     2+25
4W21
        92
            114 23
                     2+25
        95
4W14
            114 26
                     2*25
            114 28
                     2+25 MEMORY TO REGISTER
4W18
        92
                     2+26 CENTRAL TO PERIPHERAL DATA
4W07
       901
            115
                 1
            115
                     2+26
4W39
       901
                  3
            115
                     2+26
4W06
        93
            115
        93
4W12
                     2+26
4W13
        93
            115
                 9
                     2+26
            115 11
4W15
        93
                     2+26
            115 19
4W16
        93
                     2+26
            115 21
        93
                     2+26
4W17
            115 23
        93
                     2+26
4W21
        96
            115 26
                     2+26
4W14
            115 28
        93
                     2+26 MEMORY TO REGISTER
4W18
4W07
       902
            116
                 13
                     2+27 CENTRAL TO PERIPHERAL DATA
4W39
                     2+27
       902
            116
4W06
        94
            116
                     2+27
                  Ź
        94
4W12
            116
                     2+27
4W13
        94
            116
                  9
                     2+27
        94
4W15
            116 11
                     2+27
        94
                     2+27
4W16
            116 19
        94
4W17
            116 21
                     2+27
        94
            116 23
                     2+27
4W21
        97
            116 26
                     2+27
4W14
                     2+27 MEMORY TO REGISTER
        94
4W18
            116 28
                           CENTRAL TO PERIPHERAL DATA
4W07
       903
            117
                     2+28
            117
4W39
       903
                     2+28
                     2+28
4W06
        95
            117
                  5
            117
        95
                     2+28
4W12
            117
        95
4W13
                  9
                     2 * 28
        95
            117 11
4W15
                     2+28
            117 19
4W16
        95
                     2+28
        95
            117 21
                     2+28
4W17
            117
4W21
        95
                23
                     2+28
            117
        98
4W14
                26
                     2+28
4W18
        95
            117 28
                     2+28 MEMORY TO REGISTER
                     2+29 CENTRAL TO PERIPHERAL DATA
            118
4W07
       904
                 1
            118
                     2+29
       904
4W39
            118
4406
        96
                     2+29
                  5
4W12
        96
            118
                  7
                     2+29
        96
            118
4W13
                  9
                     2+29
        96
            118 11
4W15
                     2+29
4W16
        96
            118 19
                     2+29
        96
4W17
            118 21
                     2+29
        96
            118 23
                     2+29
4W21
        99
4W14
            118 26
                     2+29
        96
            118 28
                     2+29 MEMORY TO REGISTER
4W18
4W39
      907
            119 12
                     READ PERIPHERAL
```

```
4W19
      901
            120
                 5
                     MEMORY GO
4W19
      902
            120
                     MEMORY WRITE
4W19
            120 24
                     READ TO PERIPHERAL
      904
4W10
       99
                     Q O
            121
                  3
       97
                     GO
4W17
            121
                  5
4W11
       99
            121
                     READ PERIPHERAL
4W08
       99
            121
                  9
                     READ PERIPHERAL
       97
                     READ PERIPHERAL
4W06
            121 11
4W39
      907
                     READ PERIPHERAL
            121 13
       97
                     GO
4W11
            121 18
       97
4W08
            121 20
                     GO
4W05
      905
            121 22
                     GO
4W39
      905
            121 24
                     GO
            121 26
4W16
       97
                     GO MEMORY
       97
                     MEMORY GO
4W15
            121 28
4W11
       98
                     WRITE
            124
                 3
4W08
       98
            124
                     WRITE
                 5
            124
                     RESUME CENTRAL READ
      905
4W07
            124
      906
                     RESUME CENTRAL READ
4W07
                 9
4W15
       98
            124
                18
                     MEMORY WRITE
       98
            124 20
                     WRITE MEMORY
4W16
       98
            124 22
                     WRITE
4W17
       98
            124
4W10
                24
                     WRITE
            124 26
4W05
      906
                     WRITE
4W39
      906
            124
                28
                     WRITE
       91
                     2+1 READ DATA ANALOG
4W29
            J01
                 73
4W29
       90
                     2+0 READ DATA ANALOG
            J01
                12
       93
                     2+3 READ DATA ANALOG
4W29
            J01
                19
        92
            J01 26
                     2+2 READ DATA ANALOG
4W29
4W29
        95
            J02
                     2+5 READ DATA ANALOG
                 - 5
                     2+4 READ DATA ANALOG
4W29
        94
            J02
                12
4W29
       97
                     2+7 READ DATA ANALOG
            J02
                19
       96
                     2+6 READ DATA ANALOG
4W29
            J02
                26
       99
                     2+9 READ DATA ANALOG
4W29
            103
                 - 5
4W29
        98
            J03
                12
                     2+8 READ DATA ANALOG
                     2+11 READ DATA ANALOG
4W29
      901
            J03
                19
                     2+10 READ DATA ANALOG
                26
4W29
      900
            J03
4W29
      903
            J04
                 5
                     CLOCK
            J04
                     REV. MARK
4W29
      902
                12
            J05
                     2+8 WRITE DATA
4W28
       98
       97
            J05
                     2+7 WRITE DATA
4W28
            J05
                     2+6 WRITE DATA
4W28
       96
                  8
            J05
                     2+11 WRITE DATA
4W28
      901
                23
                     2+10 WRITE DATA
4W28
      900
            J05
                 25
            J05
                     2+9 WRITE DATA
       99
                27
4W28
                     2+2 WRITE DATA
4W28
       92
            J06
       91
                     2+1 WRITE DATA
4W28
            J06
                  6
4W28
       90
            J06
                  8
                     2+0 WRÎTE DATA
       95
                     2+5 WRÎTE DATA
4W28
            J06
                 23
                25
4W28
       94
            J06
                     2+4 WRITE DATA
                     2+3 WRITE DATA
       93
4W28
            J06
                27
4W28
      904
            J07
4W28
      903
            J07
                  6
            J07
      902
4W28
      905
            J07 27
4W28
                     FUNCTION
4W24
      906
            K01
4W24
      904
            K01
                     FULL
4W24
      903
            K01
                     INACTIVE
                  8
4W24
      902
            K01 23
                     ACTIVE
4W24
      907
            K01 25
                     MASTER CLEAR
4W24
      905
            K01 27
                     EMPTY
                     2+2 OUTPUT DATA
4W24
       92
            K02
                  4
```

```
OUTPUT DATA
4W24
        91
            K02
                      2+1
                  6
4W24
        90
             K<sub>0</sub>2
                      2+0
                            OUTPUT DATA
                  8
                            OUTPUT DATA
        95
4W24
             K02
                      2+5
                            OUTPUT DATA
4W24
        94
             K02 25
                      2+4
4W24
        93
             K02
                 27
                      2+3
                            OUTPUT DATA
                            OUTPUT DATA
        98
                      2 * 8
4W24
             K03
        97
                            OUTPUT DATA
4W24
             K03
                      2+7
                  6
        96
                            OUTPUT DATA
4W24
             K03
                      2+6
                      2+11 OUTPUT DATA
4W24
       901
             K03
                 23
                      2+10 OUTPUT DATA
2+9 OUTPUT DATA
4W24
       900
             K03
                  25
        99
                 27
             K03
4W24
       902
             K04
                      ACTIVE
4W26
             K04
                      FULL
4W26
       904
                   6
       903
             K04
                      INACTIVE
4W26
                   8
       905
             K 0 4
                 27
                      EMPTY
4W26
            K05
                      2+2 INPUT DATA
4W26
        92
        91
4W26
             K05
                  6
                      2+1 INPUT DATA
        90
            K05
                      2+0 INPUT DATA
4W26
                  8
                      2+5 INPUT DATA
2+4 INPUT DATA
        95
4W26
            K05
                 23
4W26
        94
            K<sub>0</sub>5
                 25
        93
                      2+3 INPUT DATA
            K05
                 27
4W26
                      2+8 INPUT DATA
        98
            K06
4W26
4W26
        97
                      2+7 INPUT DATA
             K06
                  6
        96
                      2+6 INPUT DATA
4W26
             K06
                  8
                      2+11 INPUT DATA
4W26
       901
             K06 23
4W26
       900
             K06
                 25
                      2+10 INPUT DATA
        99
             K06 27
                      2+9 INPUT DATA
4W26
                      2+0 OUTPUT DATA
4W27
        90
            L01
                  5
        91
                      2+1 OUTPUT DATA
                  7
4W27
            L01
                      2+2 OUTPUT DATA
2+3 OUTPUT DATA
4W27
        92
            L01 10
        93
4W27
            L01
                 21
4W27
        94
            L01 24
                      2+4 OUTPUT DATA
        95
                      2+5 OUTPUT DATA
4W27
            L01 26
        96
4W27
                      2+6 OUTPUT DATA
                  5
            L02
                      2+7 OUTPUT DATA
2+8 OUTPUT DATA
4W27
        97
            L02
        98
4W27
            L02 10
        99
                      2+9 DUTPUT DATA
4W27
            L02 21
                      2+10 OUTPUT DATA
4W27
       900
            L02 24
                      2+11 OUTPUT DATA
             L02 26
4W27
       901
4W27
       906
            L03
                  - 5
                      FUNCTION
4W27
       902
             L03
                      ACTIVE
                      FULL
       904
4W27
            L03 10
4W27
       903
            L03 21
                      INACTIVE
             L03 24
                      MASTER CLEAR
4W27
       907
       905
4W27
            L03 26
                      EMPTY
4W25
       903
             LO7
                       INACTIVE
            L07
                      FULL
4W25
       904
            L08
4W25
        96
                  5
                      2+6 INPUT DATA
        97
            L08
                            INPUT DATA
                  7
4W25
                      2 * 7
                            INPUT DATA
        98
            L08 10
4W25
                      2+8
                      2+9 INPUT DATA
2+10 INPUT DATA
        99
            L08
4W25
                 21
4W25
       900
            L08
                 24
                      2+11 INPUT DATA
4W25
            L08
       901
                 26
4W25
        90
             L09
                      2+0
                            INPUT DATA
        91
            L09
                  7
                            INPUT DATA
4W25
                      2+1
4W25
        92
            L09
                      2+2
                            INPUT DATA
                 10
        93
            L09
                            INPUT DATA
4W25
                 21
                      2+3
4W25
        94
            L09
                      2 + 4
                            INPUT DATA
                 24
        95
4W25
            L09 26
                            INPUT DATA
4W25
       906
            L26
4W26
       906
            L26
                  3
4W26
      907
            L26 13
```

4W25 907 L26 15

254

## CHASSIS 5

```
5W03
       900
             A41
                      2+45
5W03
                      2+46
       901
             A41
                   6
5W03
       902
             A41
                      2+47
5W03
       903
             A41 22
                      2+48
5W03
       904
             A41 25
                      2+49
5W03
       905
             A41 27
                      2+50
5W03
       906
             A42
                  . 4
                      2+51
5W03
       907
             A42
                      2+52
SWO3
        90
             A42
                      2+53
        91
5W03
             A42 22
                      2+54
        92
5W03
             A42 25
                      2+55
5w03
        93
             A42 27
                      2+56
        94
             B41
5W03
                      2+57
5W03
        95
             841
                      2+58
                   6
        96
5W03
             B41
                      2+59
5W04
       900
             841
                 22
                      2+30 BUFFER TO CONTROL
                      2+31 BUFFER TO CONTROL
5W04
       901
             B41
                 25
                      2+32 BUFFER TO CONTROL
             B41
5W04
       902
                 27
                      2+33 BUFFER TO CONTROL
5W04
       903
             B42
                  4
                      2+34 BUFFER TO CONTROL
5W04
             B42
       904
                   6
5W04
       905
             B42
                  7
                      2+35 BUFFER TO CONTROL
                      2+36 BUFFER TO CONTROL
5W04
       906
             B42 22
                      2+37 BUFFER TO CONTROL
5W04
       907
             B42 25
                      2+38 BUFFER TO CONTROL
5W04
        90
             B42 27
        91
                      2+39 BUFFER TO CONTROL
5W04
             C41
                  4
        92
                      2+40 BUFFER TO CONTROL
5W04
             C41
                   6
                      2+41 BUFFER TO CONTROL
2+42 BUFFER TO CONTROL
5W04
        93
             C41
        94
             C41 22
5W04
                      2+43 BUFFER TO CONTROL
2+44 BUFFER TO CONTROL
5W04
        95
             C41 25
        96
             C41 27
5W04
5W01
       904
             C42
                      2+15
                  4
5W01
       905
             C42
                      2+16
                  . 6
                      2+17
5W01
       906
             C42
5W01
       907
             C42
                 22
                      2+18
       900
             C42 25
                      2+19
5W01
5W01
        90
             C42 27
                      2+20
5w01
        91
             D41
                      2+21
        92
5W01
             D41
                      2+22
5w01
        93
             D41
                      2+23
        94
5W01
             D41 22
                      2+24
5w01
        95
             D41 25
                      2+25
        96
5w01
             D41 27
                      2 + 26
        97
             D42
                      2+27
5W01
5W01
        98
             D42
                      2+28
        99
5w01
             D42
                      2+29
             D42 22
5W02
       904
                      2+0
5W02
       905
             D42 25
                      2+1
5W02
             D42 27
                      2 * 2
       906
        98
5W13
             E38
                  4
                      TRANSFER
5W13
        97
             E38
                      COMPLEMENT
                   6
        93
5W13
             E38
                      SUM
                  8
        94
             E38
                      DIFFERENCE
5W13
                 10
5W13
        95
             E38
                 12
                      PRODUCT
        97
                      SHIFT (JK)
             E38
5W12
                 19
        99
             E38 21
                      SHIFT LEFT
5W12
                      SHIFT NOMINAL
        98
5W12
             E38 23
       908
             E39
5W12
                      NORM. (SHIFT)
                   4
5W12
                      ROUND SHIFT
       900
             E39
                   6
        95
             E39
                      PACK
5W12
                   8
        96
             E39 10
                      UN-PACK
5W12
```

```
5W12
        92
             E39 12
                      MASK
5W12
        94
             E39
                       ADD
                 19
             E39
5W12
        93
                       ROUND ADD
                  21
5W11
        92
             E39
                 23
                       ADD PLUS
             E39 27
5W12
                      LONG ADD - PLUS
       905
5W14
        91
             E40
                      ROUND NO. 1
5W14
       901
             E40
                   6
                      ROUND
             E40
5W14
                      DOUBLE NO. 1
                   8
             E40 10
5W14
       902
                      DOUBLE
5W13
        91
             E40
                      ROUND
                 12
        96
                      POP. COUNT
GO BOOLEAN
             E40
5W13
                 19
                 21
5W13
       902
             E40
       907
             E40
                      SHIFT IN
5W12
                 23
        91
             E40
                      ADD IN
5W12
                 25
        90
5W12
             E40 27
                      LONG ADD IN
5W02
       907
             E41
                       2+3
5W02
             E41
       900
                      2+4
                   6
        90
5W02
             E41
                   7
                      2+5
                 22
5W02
        91
             E41
                      2+6
        92
5W02
             E41 25
                      2+7
        93
5W02
             E41
                 27
                      2+8
5W02
        94
             E42
                      2+9
                  67
        95
5W02
             E42
                      2+10
5W02
        96
             E42
                      2+11
                 22
        97
             E42
                      2+12
5W02
        98
             E42 25
5W02
                      2+13
                      2+14
        99
             E42 27
5W02
5W08
       900
             F41
                       BİT
             F41
5W08
       901
                       BIT
                   6
       902
             F41
5WOB
                   8
                       BIT
             F41 10
5W08
       903
                        BIT
       904
             F41 12
                        BIT
SWO8
                       BIT 5
BIT 6
5W08
       905
             F41
                 19
             F41 21
5W08
       906
5W08
       907
             F41 23
                       BIT
       908
             F41 25
                       BIT 8
5W08
5W08
        90
             F41 27
                       BIT 9
5 W 0 8
        91
             F42
                       BIT 10
5W08
        92
             F42
                   6
                       BIT 11
        93
             F42
5W08
                       BIT 12
                   8
5W08
        94
             F42 10
                       BIT 13
        95
             F42 12
5W08
                       BIT 14
        96
97
             F42
                       BIT 15
BIT 16
5W08
                 19
5W08
             F42
                  21
5W08
        98
             F42 23
                       BIT 17
5W11
             F42 25
        90
                       INCR SIGN
             F42 27
                      MEMORY GO
       901
5W18
                       2+0 CENTRAL PROGRAM ADDRESS
5W09
        90
             G41
                       2+1 CENTRAL PROGRAM ADDRESS
        91
5W09
             G41
                   6
        92
5W09
             G41
                      2+2 CENTRAL PROGRAM ADDRESS
                   8
                       2+3 CENTRAL PROGRAM ADDRESS
5W09
        93
             G41 10
                      2+4 DENTRAL PROGRAM ADDRESS
5W09
        94
             G41 12
                      2+5 CENTRAL PROGRAM ADDRESS
2+6 CENTRAL PROGRAM ADDRESS
        95
             G41
5W09
                 19
        96
5W09
                 21
             G41
        97
             G41 23
                      2+7 CENTRAL PROGRAM ADDRESS
5W09
                       2+8 CENTRAL PROGRAM ADDRESS
5W09
        98
             G41 25
        99
                       2+9 CENTRAL PROGRAM ADDRESS
5W09
             G41 27
             G42
                      2+10 CENTRAL PROGRAM ADDRESS
5W09
       900
                      2+11 CENTRAL PROGRAM ADDRESS
5W09
       901
             G42
                   6
                      2+12 CENTRAL PROGRAM ADDRESS
5W09
       902
             G42
                   -8
                       2+13 CENTRAL PROGRAM ADDRESS
5W09
       903
             G42 10
                      2+14 CENTRAL PROGRAM ADDRESS
5W09
       904
             G42 12
```

```
5W09
       905
            G42 19
                      2+15 CENTRAL PROGRAM ADDRESS
             G42 21
                      2+16 CENTRAL PROGRAM ADDRESS
5W09
       906
                      2+17 CENTRAL PROGRAM ADDRESS
5W09
       907
             G42 23
                      MULTIPLY TO X
5W22
       903
            H38
                      MULTIPLY TO X MULTIPLY TO X
5W15
       902
             H38
                  3
5W22
            H38
                                            GO
       902
                  5
            H38
                      MULTIPLY TO X GO
5W15
       901
                  7
                      MULTIPLY TO X
       905
            H38 22
                                            2
5W22
                      MULTIPLY TO X
                 24
5W15
       904
            H38
                                TO X 2
5W22
       904
            H38
                 26
                                            1
                      MULTIPLY TO X
5W15
       903
            H38
                 28
5W22
       907
             H39
                      ADD TO X
                                            0
            H39
5W15
       906
                      ADD TO X
5W22
            H39
                  5
                                            GO
       906
                      ADD TO X
            H39
                  7
5W15
       905
                      ADD
                          TO X
                                      G0
        90
            H39 22
                                            2
5W22
                      ADD TO X
            H39 24
5W15
        98
                      ADD TO X
        93
             H39 26
                      ADD TO X
                                            1
5W20
            H39
5W15
       907
                 28
                      ADD TO X
                      INCREMENT TO X
5W22
        92
            H40
                                            n
                  1
                      INCR TO X 0 INCREMENT TO X
        95
            H40
5W17
                  3
        91
                                            GO
5W22
            H40
        94
            H40
                  7
                      INCR TO X
5W17
                                  GO
            H40 22
5W22
        94
                      INCREMENT TO X
                                            2
5W17
             H40 24
        97
                      INCR TO X
        93
                      INCREMENT TO X
            H40 26
5W22
                                            1
                      INCR TO X
        96
            H40 28
5W17
                                   1
                      INCREMENT TO B
5W22
        96
            H41
                 1
                      INCREMENT TO B
        95
            H41
                                            GO
5W22
                  5
5W22
        98
            H41 22
                      INCREMENT TO B
                                            2
        97
                      INCREMENT
                                 TO B
5W22
            H41 26
                                            1
5W16
       901
            H42
                      INCREMENT TO A
                                            0
                      INCREMENT TO A
                                            GO
5W16
       900
             H42
                      INCREMENT TO A
5W16
       903
            H42 22
                                            2
5W16
            H42 26
                      INCREMENT TO A
       902
5w23
            122 12
                      CLOCK
       902
        94
            138
5W27
                      2+34
        93
5W27
            138
                      2+33
            138
5W27
        92
                      2+32
                 8
             138 10
5W27
        91
                      2+31
        90
5W27
             138 12
                      2 * 30
            138
                      STORE CONTROL BIT 2+39
       904
5W10
                 19
5W10
       903
             138 21
                      STORE CONTROL BIT 2+38
                      STORE CONTROL BIT 2+37
5W10
       902
             138 23
5W10
       901
             138 25
                      STORE CONTROL BIT 2+36
        95
5W27
             138
                 27
                      2+35
        90
             139
5W10
                      STORE CONTROL BIT 2+44
                      STORE CONTROL BIT 2+43
5W10
       900
             139
                  6
                      STORE CONTROL BIT 2+42
             139
5W10
       907
                  8
             139 10
                      STORE CONTROL BIT 2+41
5W10
       906
                      STORE CONTROL BIT 2+40
STORE CONTROL BIT 2+49
       905
             139
5W10
                 12
            139
5W10
        95
                 19
                 21
             139
                      STORE CONTROL BIT 2+48
5W10
        93
5W10
             139 23
                      STORE CONTROL BIT 2+47
            139
        92
                      STORE CONTROL BIT 2+46
5W10
                 25
                      STORE CONTROL BIT 2+45
STORE CONTROL BIT 2+50
5W10
        91
             139 27
        96
             140
5W10
5W27
        96
             140 19
                      2+56
        98
5W10
             140 23
                      STORE CONTROL BIT 2+52
        99
             140 23
                      STORE CONTROL BIT 2+53
5W10
                      STORE CONTROL BIT 2+51
SHIFT TO B 0
        97
             140 27
5W10
5W16
       905
             141
```

```
5W16
       904
             141
                  : 5
                      SHIFT TO B
                                             GO
5W16
       907
             141 22
                      SHIFT TO B
                                             2
5W16
       906
             141
                  26
                       SHIFT TO B
                                             1
5W16
        90
             142
                       MEMORY TO D
                   1
                                       0
5W15
        91
                       MEMORY TO
             142
                   3
                                  D
                                       0
5W20
        94
                       MEM. TO D
                                             GO
             142
                   5
        90
                       MEMORY TO
5W15
             142
                                       g0
5W16
        92
             142
                  22
                       MEMORY TO D
                                       2
5W15
        93
             142
                       MEMORY TO
                                  D
                  24
                                       2
        91
                       MEMORY TO
5W16
             142 26
                      MEMORY TO D
        92
             142 28
5W15
                      REQUEST RELEASE SHIFT
5W12
       904
             J22 19
5W13
       906
             J23 19
                      BOOLEAN REQUEST RELEASE
                      DIVIDE REQUEST RELEASE
REQUEST RELEASE
5W13
       905
             J24
                  19
             J25
        95
5W14
                 19
       901
5W22
             J39
                      CLOCK
             J39
5W28
       900
                      CLOCK
             J39
                      CLOCK
5W27
       901
                   8
             139
5W29
       900
                  23
                      CLOCK
             139
5W15
       900
                  25
                      CLOCK
5W14
             J39
        96
                  27
                      CLOCK
        90
             J40
                      GO UNIT NO. 1.
5W14
                      GO UNIT NO. 2
5W14
       900
             J40
5W13
       903
             J40
                      GO DIVIDE
                   8
5W13
        ,99
             J40
                       TRANSMIT BCOLEAN
                 10
                       TRANSMIT SHIFT
5W12
       902
             J40
                  12
                       TRANSMIT ADD
5W12
       903
             J40 19
5W12
       901
             J40 21
                       TRANSMIT LONG ADD
                      XMIT RESULT NO. 1
5W14
        93
             J40 23
5W14
        94
             J40
                 25
                      XMIT RESULT NO. 2
5W13
       900
             J40
                  27
                      TRANSMIT DIVIDE
                      J TO MULTIPLY (2)
5W16
        97
             J41
                   1
                      J TO MULTIPLY O
5W17
       901
             J41
5W22
        99
                      XJ TO MULTIPLY (2)
                                            GO
             J41
                   5
                      J TO MULTIPLY GO J TO MULTIPLY (2)
                   7
5W17
       900
             J41
        99
5W16
             J41
                  22
       903
                      J TO MULTIPLY 2
5W17
             J41
                  24
5W16
        98
             J41 26
                            MULTIPLY (2)
                      J TO
                            MULTIPLY 1
5W17
       902
             J41
                  28
                       J TO
                            MULTIPLÝ (1)
MULTIPLÝ 0
5W16
        94
             J42
                         TO
                   1
        95
5W15
             J42
                   3
                         TO
        93
                         TO MULTIPLY (1)
5W16
             J42
                   5
                                             GO
5W15
        94
             J42
                         TO MULTIPLY GO
        96
                      K TO MULTIPLY (1)
5W16
             J42 22
                      K TO MULTIPLY 2
K TO MULTIPLY (1)
5W15
        97
             J42 24
        95
5W16
             J42 26
                       K TO MULTIPLY 1
5W15
        96
             J42 28
5W14
       905
             K21 19
                       REQUEST RELEASE
                      REQUEST RELEASE LONG ADD
5W12
       906
             K22 19
        98
             K23 19
                      REQUEST RELEASE ADD
5W26
                      READ TO PERIPHERAL
5W18
       904
             K38
5W18
                      MEMORY WRITE
       902
             K38
                 23
                      STORE CONTROL
5W27
       902
             K38
                      STORE REGISTERS
5W27
             K38 25
       903
5W27
       904
             K38
                  27
                      STORE PERIPHERAL
5W19
        90
             K40
                   1
                      X TO INCREMENT
                                             0
5W20
        95
                      X TO INCR.
                                             GO
             K40
5W19
        92
             K40
                  22
                      X TO INCREMENT
        91
                      X TO INCREMENT
5W19
             K40
                  26
5W19
       901
             K41
                      XJ TO ADD (1)
                                             0
                   1
5W17
       905
                      X TO ADD
             K41
                   3
5W19
       900
             K41
                      XJ TO ADD (1)
                                             GO
```

```
5W17
       904
             K41
                 7
                      X TO ADD
                                      g0
5W19
       903
             K41 22
                      MIN OF LX
                                            2
5W17
                      X TO ADD
       907
             K41
                 24
             K41 26
5W19
       902
                      ADD OF LX
                                            1
5W17
       906
             K41 28
                      X TO ADD
5W19
       905
                      XL TO ADD
             K42
                                            0
5W17
        90
             K42
                  3
                      X TO ADD
                                      n
                      XL TO ADD
X TO ADD
5W19
       904
             K42
                  5
                                            GO
                  7
5W17
        93
             K42
                                      GΟ
                      XL TO ADD (2)
5W19
       907
             K42 22
                                            2
5W17
        92
             K42 24
                      X TO ADD
             K42 26
       906
5W19
                      XL TO ADD (2)
                                            1
5W17
        91
             K42 28
                      X TO ADD
                      BJ TO INCREMENT
5W20
       901
            L41
                  1
                                            0
5W20
       900
            L41
                  5
                      BJ TO
                             INCREMENT
                                            GO
5W20
       903
            L41 22
                      BJ TO INCREMENT
                                            2
5W20
            L41 26
                      BJ TO INCREMENT
       902
                                            1
5W20
       905
            L42
                      BK TO
                             INCREMENT
                                            0
                  1
                                            GO
5W20
       904
                      BK TO
                            INCREMENT
            L42
                  5
            L42 22
5W20
       907
                      BK TO
                            INCREMENT
                                            2
                      BK TO INCREMENT
            L42 26
5W20
       906
5W32
       900
            M32
                      CLOCK
                  4
5W31
       900
             M32
                  6
                      CLOCK
             M32
5W30
                      CLOCK
       900
                  8
5W35
       900
             M32 23
                      CLOCK
5W34
       900
             M32 25
                      CLOCK
                      CLOCK
5W33
       900
            M32 27
        94
5W19
             M41
                      В
                        TO ADD (1)
                                            GO
5W19
        93
             M41
                  5
                      ₿
                        TO ADD (1)
        96
             M41 22
5W19
                      B TO ADD (1)
5W19
        95
             M41 26
                      B TO ADD (1)
        90
                           INCREMENT
                      A TO
5W20
             M42
                  1
5W20
        96
             M42
                  5
                      A TO INCR.
                                            GO
5W20
        92
             M42
                 22
                      A TO
                           INCREMENT
                                            2
                      A TO INCREMENT
EXP = 3777 ERROR
        91
             M42 26
5W20
5W27
       906
             N23
5W27
       905
            N23 10
                      EXP = 1777 ERROR
5w24
                      BIT 0
       901
            N40
                  5
5W24
                  7
                      BIT
       902
            N40
            N40 10
5w24
       903
                      BIT 2
                          3
5W24
       904
            N40
                 21
                      BIT
      905
                      BIT
5W24
            N40 24
                          5
5W24
       906
            N40 26
                      BIT
                      BIT
                          6
5W24
       907
             N41
                  5
5W24
             N41
                      BIT 7
       908
                      91T 8
5W24
       900
             N41 10
       91
            N41 21
                      BIT 9
5W24
5W24
        92
             N41 24
                      BIT 10
        93
5W24
             N41 26
                      BIT 11
5W24
        94
             N42
                      BIT 12
        95
5W24
            N42
                  7
                      BIT 13
5W24
        96
            N42 10
                      BIT 14
5W24
        97
            N42 21
                      81T 15
                      BIT 16
5W24
        98
            N42
                 24
        99
                      BIT 17
            N42 26
5W24
5W37
                      INCREMENT OPERAND K BIT 2+1
       901
            022
                  3
5W36
       901
            022
                  7
                      INCREMENT OPERAND
                                          J BIT 2+1
5w36
       900
            022 23
                      INCREMENT
                                 OPERAND
                                           J BIT 2+0
5W37
       900
            022
                 27
                      INCREMENT
                                 OPERAND
                                           K BIT
                                                 2 * 0
                                           K BIT 2+3
                      INCREMENT
5W37
       903
            023
                  3
                                 OPERAND
                                          J BIT 2+3
5W36
       903
                  7
                      INCREMENT OPERAND
            023
5W36
       902
            023 23
                      INCREMENT OPERAND J BIT 2+2
```

```
5W37
      902
            023 27
                     INCREMENT OPERAND K BIT 2+2
5W37
      905
            024
                     INCREMENT OPERAND K BIT 2+5
                  3
                                            BIT
                                                2+5
                     INCREMENT OPERAND
5W36
      905
            024
            024
                23
5W36
      904
                     INCREMENT OPERAND
                                            BIT 2*4
5W37
      904
            024
                27
                     INCREMENT OPERAND
                                         K BIT 2*4
5W37
      907
            025
                     INCREMENT OPERAND
                                         K BIT 2+7
                  3
                                            BIT 2+7
5W36
      907
            025
                     INCREMENT
                                OPERAND
                                            BIT
5W36
      906
            025
                     INCREMENT OPERAND
                                                2+6
                23
            025
5W37
      906
                27
                     INCREMENT OPERAND
                                         K BIT 2*6
       90
                     INCREMENT OPERAND
                                         K BIT 2+9
5W37
            026
                 3
       90
                     INCREMENT OPERAND
                                         J BIT 2*9
5W36
            026
                  7
5W36
                     INCREMENT OPERAND
                                         J BIT 2+8
      908
            026 23
                                            BIT
5W37
      908
            026
                      INCREMENT OPERAND
                                                2+8
                27
                                            BIT
                                                2+11
                     INCREMENT OPERAND K
5W37
       92
            027
                  3
                                            BIT 2*11
5W36
        92
            027
                     INCREMENT OPERAND J
       91
5W36
            027
                23
                     INCREMENT OPERAND
                                         J BIT 2*10
       91
            027
5w37
                     INCREMENT OPERAND K BIT 2*10
                27
                                         K BIT 2+13
                     INCREMENT OPERAND
5W37
       94
            028
                     INCREMENT OPERAND J BIT 2+13
        94
5W36
            028
                  7
        93
            028
                23
                     INCREMENT OPERAND J BIT 2*12
5W36
                     INCREMENT OPERAND K BIT 2+12
       93
5437
            028
                27
       96
            029
                     INCREMENT OPERAND K BIT 2+15
5W37
                  3
                                         J BIT
5W36
        96
            029
                      INCREMENT OPERAND
                                                2*15
                                                2+14
        95
            029
                23
                     INCREMENT OPERAND J BIT
5W36
            029
        95
                     INCREMENT OPERAND K BIT 2*14
5W37
                27
5W37
       97
            030
                 3
       97
5W36
            030
5w36
       98
            030
                24
       98
5W37
            030
                25
5W26
      905
                 3
                     X TO MEMORY
            031
                                     n
5W25
      905
            031
      904
            031
5w25
                  6
5W26
      904
            031
                  8
      907
            031 23
5W25
5W26
      907
            031
                25
                     X TO MEMORY
5W25
      906
            031 26
                                           1
5W26
                     X TO MEMORY
       906
            031
                28
5W25
        93
            033
                  6
       91
5W26
            033
                  8
            034
       98
5W25
5W25
      903
            034
                  6
5W25
      902
            034
                  8
            034 10
5W25
      901
5w25
      900
            034
                12
            035
                     ENTER (JK) 2+2
       94
5W26
                  4
5W26
       93
            035
                     ENTER (JK) 2+1
                  6
            035
                     ENTER (JK) 2+0
       92
5W26
5W13
      907
            035
                     TRANSMIT MUIT 1
                10
5W13
      908
            035
                     TRANSMIT MUIT 2
                12
            035
5W26
       97
                     ENTER (JK) 2+5
                23
       96
            035
                     ENTER (JK) 2+4
5W26
                25
       95
                     ENTER (JK) 2+3
5W26
            035
                27
                     EXCHANGE RESUME OUT EXCHANGE GO (PERIPH
5W23
      900
            040
                 2
5W21
      902
            040
                15
                     ACCEPT
5W29
       99
            040
                17
5W21
      900
            041 18
                      READ/GO (PERIPH)
                      WRITE GO (PERIPH)
5W21
       901
            041 20
                     LONG ADD SER CONTROL
       99
5W26
            042
                  5
5W17
       99
            042
                     LONG ADD SIGN CH 5
       98
5W17
            042 10
                     LONG ADD ZERO
                                      CH
5W11
      901
            042 21
                     LONG ADD
                               INFÍNITE
5W11
      900
            042
                24
                     LONG ADD INDEFINITE
```

```
5w23
            042 26
      901
                      MC
5W28
      902
            Q34
                      2+1 BANK SELECT
                  3
5W29
            Q34
                          BANK SELECT
       902
                      2+1
            Q34
5W28
      901
                  7
                      2+0 BANK SELECT
5W29
      901
            Q34
                  9
                      2+0 BANK SELECT
                     2+0 BANK SELECT
5W30
            034 11
      901
5W31
      901
            034 13
                      2+0 BANK SELECT
                      2+2 CHASSIS 4 SELECT (001)
5W28
      903
            Q34
                 18
            Q34 20
                      2+2 CHASSIS 3 SELECT(000)
5W29
      903
5W30
      903
            Q34 22
                      2+2 CHASSIS 9 SELECT (010)
            034 24
                     2+2 CHASSIS 10 SELECT (011)
5w31
      903
                     2+1 BANK SELECT
2+1 BANK SELECT
5W30
      902
            034
                26
            Q34 28
5W31
      902
                     2+4 CHASSIS 4 SELECT (001)
5W28
      905
            035
                  - 3
5W29
      905
            035
                      2+4 CHASSIS 3 SELECT (000)
                  -5
            Q35
5w28
      904
                  7
                     2+3 CHASSIS 4 SELECT (001)
                     2+3 CHASSIS 3 SELECT (000)
2+3 CHASSIS 9 SELECT (010)
5W29
      904
            035
                  9
            035 11
5W30
      904
            035 13
                      2+3 CHASSIS 10 SELECT (011)
5W31
      904
            Q35 18
5W28
      906
                      2+5 ADDRESS TO MEMORY
5W29
            Q35 20
                      2+5 ADDRESS TO MEMORY
      906
5W30
      906
            035 22
                      2+5 ADDRESS TO MEMORY
                      2+5 ADDRESS TO MEMORY
            Q35 24
5W31
      906
                     2+4 CHASSIS 9 SELECT (010)
5W30
      905
            Q35 26
                     2+4 CHASSIS 10 SELECT (011)
2+7 ADDRESS TO MEMORY
5W31
            Q35 28
      905
      908
            Q36
                  3
                     2+7 ADDRESS TO MEMORY
2+6 ADDRESS TO MEMORY
2+6 ADDRESS TO MEMORY
5W29
      908
            Q36
                  5
5W28
      907
            Q36
                  7
5W29
      907
                  9
            Q36
5W30
      907
            036 11
                      2+6 ADDRESS TO MEMORY
      907
                     2+6 ADDRESS TO MEMORY
5W31
            036 13
5W28
       90
                      2+8 ADDRESS TO MEMORY
            Q36 18
       90
                     2+8 ADDRESS TO MEMORY
5W29
            036 20
       90
                     2+8 ADDRESS TO MEMORY
5W30
            036 22
                      2+8 ADDRESS TO MEMORY
5W31
       90
            Q36 24
                     2+7 ADDRESS TO MEMORY
5W30
       908
            036 26
                     2+7 ADDRESS TO MEMORY
5w31
       908
            036 28
       92
                      2+10 ADDRESS TO MEMORY
5W28
            Q37
                  3
        92
                      2+10 ADDRESS TO MEMORY
5W29
            Q37
                  5
                      2+9 ADDRESS TO MEMORY
5W28
        91
            Q37
                  7
        91
            Q37
                     2+9 ADDRESS TO MEMORY
5w29
                  9
            037 11
5W30
        91
                      2±9 ADDRESS TO MEMORY
                      2+9 ADDRESS TO MEMORY
        91
            Q37
5W31
                13
            Q37
                     2+11 ADDRESS TO MEMORY
        93
5W28
                 18
        93
            Q37 20
                     2+11 ADDRESS TO MEMORY
5W29
                     2+11 ADDRESS TO MEMORY
5W30
        93
            Q37 22
            Q37 24
                     2+11 ADDRESS TO MEMORY
        93
5W31
                     2+10 ADDRESS TO MEMORY
5W30
        92
            037
                26
        92
            Q37
5w31
                 28
                      2+10 ADDRESS TO MEMORY
5W28
        95
            038
                      2+13 ADDRESS TO MEMORY
                  3
                     2+13 ADDRESS TO MEMORY
5W29
        95
            038
        94
                     2+12 ADDRESS TO MEMORY
5W28
                  7
            Q38
        94
                     2+12 ADDRESS TO MEMORY
5W29
            Q38
                  9
5W30
        94
            038
                      2+12 ADDRESS TO MEMORY
                11
        94
                     2+12 ADDRESS TO MEMORY
5W31
            038 13
                     2+14 ADDRESS TO MEMORY
        96
            Q38 18
5W28
        96
                      2+14 ADDRESS TO MEMORY
5W29
            Q38 20
            Q38 22
5W30
        96
                     2+14 ADDRESS TO MEMORY
5W31
        96
            038
                24
                      2+14 ADDRESS TO MEMORY
        95
                     2+13 ADDRESS TO MEMORY
5W30
            Q38 26
            Q38 28
5W31
        95
                      2+13 ADDRESS TO MEMORY
        98
5W28
            Q39
                      2+16 ADDRESS TO MEMORY
```

```
5W29
        98
             039
                   5
                       2+16
5W28
        97
             039
                       2+15 ADDRESS TO MEMORY
             Q39
                       2+15 ADDRESS TO MEMORY
        97
5W29
                   0
                       2+15 ADDRESS TO MEMORY
             Q39
5W30
        97
                  11
        97
             Q39
                       2+15 ADDRESS TO
5W31
                  13
                                          MEMORY
5W30
        98
             039
                      2+16 ADDRESS TO MEMORY
                 26
             Q39
                       2+16 ADDRESS TO
5W31
        98
                  28
                       2+1 BANK SELECT
5W32
       902
             Q40
                   3
5w33
       902
             040
                       2+1 BANK SELECT
                   7
                           BANK SELECT
5W32
       901
             Q40
                       2+0
5W33
       901
             Q40
                   9
                       2+0 BANK SELECT
5W34
       901
             040
                           BANK SELECT
                  11
                       2+0
5W35
       901
903
             040
                       2+0
                            BANK SELECT
                  13
                           CHASSIS 13 SELECT (100)
                       2+2
5W32
             040
                  18
                      2+2 CHASSIS 14 SELECT (101)
2+2 CHASSIS 15 SELECT (110)
2+2 CHASSIS 16 SELECT (111)
5W33
       903
             Q40 20
5W34
       903
             040 22
5w35
       903
             Q40
                  24
5W34
       902
             Q40
                  26
                       2+1 BANK SELECT
       902
                  28
             040
                       2+1 BANK SELECT
5W35
                      2+4 CHASSIS 13 SELECT (100)
2+4 CHASSIS 14 SELECT (101)
5W32
       905
             041
                   3
       905
             Q41
5W33
                   5
                       2+3 CHASSIS 13 SELECT (100)
2+3 CHASSIS 14 SELECT (101)
5W32
       904
             041
                   7
5W33
       904
             041
                   9
       904
             041 11
                       2+3 CHASSIS 15 SELECT (110)
5W34
       904
             041 13
                       2+3 CHASSIS 16 SELECT (111)
5W35
                       2+5 ADDRESS TO MEMORY
5W32
       906
             Q41 18
             041 20
5W33
       906
                       2+5 ADDRESS TO MEMORY
                           ADDRESS TO MEMORY
ADDRESS TO MEMORY
5W34
       906
             Q41 22
                       2+5
5W35
       906
             Q41 24
                       2+5
       905
             041 26
                       2+4 CHASSIS 15 SELECT (110)
5w34
                       2+4 CHASSIS 16 SELECT (111)
       905
5W35
             Q41 28
       908
             Q42
                       2+7 ADDRESS TO MEMORY
5W32
                   3
5w33
       907
             Q42
                   5
                       2+6 ADDRESS TO MEMORY
       907
                       2+6 ADDRESS TO MEMORY
5W32
             042
                   7
5W33
             042
                       2+7 ADDRESS TO MEMORY
       908
                   9
             Q42 11
                       2+6 ADDRESS TO MEMORY
       907
5W34
       907
                       2+6 ADDRESS TO MEMORY
5W35
             Q42 13
        90
             042
                      2+8 ADDRESS TO MEMORY
5W32
                 18
        90
                 20
                      2+8 ADDRESS TO MEMORY
             Q42
5W33
             042 22
5W34
                       2+8 ADDRESS TO MEMORY
        90
                       2+8 ADDRESS TO MEMORY
5W35
        90
             Q42
                 24
             042 26
                       2+7 ADDRESS TO MEMORY
5W34
       908
       908
             042 28
                       2+7 ADDRESS TO MEMORY
5W35
5W32
        92
             R40
                   3
                       2+10 ADDRESS TO MEMORY
        92
             R40
                       2+10 ADDRESS TO MEMORY
5W33
5W32
        91
             R40
                       2+9 ADDRESS TO MEMORY
        91
             R40
                       2+9 ADDRESS TO MEMORY
5W33
                   9
                       2+9 ADDRESS TO MEMORY
2+9 ADDRESS TO MEMORY
             R40 11
5W34
        91
        91
5W35
             R40
                  13
                       2+11 ADDRESS TO MEMORY
2+11 ADDRESS TO MEMORY
5W32
        93
             R40
                  18
        93
             R40 20
5W33
                       2+11 ADDRESS TO MEMORY
        93
5W34
             R40 22
                       2+11 ADDRESS TO MEMORY
        93
             R40
                  24
5W35
        92
                       2+10 ADDRESS TO MEMORY
             R40
5W34
                  26
5W35
        92
             R40 28
                       2+10 ADDRESS TO MEMORY
                       2+13 ADDRESS TO
5W32
        95
             R41
        95
                       2+13 ADDRESS TO MEMORY
5W33
             R41
                   5
                                      TO MEMORY
        94
                       2+12 ADDRESS
5W32
             R41
                   7
        94
5W33
             R41
                   9
                       2+12 ADDRESS TO MEMORY
        94
5W34
             R41 11
                       2+12 ADDRESS TO MEMORY
5W35
        94
             R41 13
                       2+12 ADDRESS TO MEMORY
5W32
                       2+14 ADDRESS TO MEMORY
        96
             R41
                  18
```

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2+14 ADDRESS TO MEMORY
5W33
           96
                 R41 20
                               2+14 ADDRESS TO MEMORY
2+14 ADDRESS TO MEMORY
5W34
           96
                  R41 22
5W35
                  R41 24
                               2+13 ADDRESS TO MEMORY
2+13 ADDRESS TO MEMORY
2+16 ADDRESS TO MEMORY
           95
                  R41 26
5W34
5W35
           95
                  R41 28
5W32
           98
                  R42 3
                               2+16 ADDRESS TO MEMORY
2+15 ADDRESS TO MEMORY
           98
                  R42 5
5W33
5W32
           97
                  R42 7
                               2+15 ADDRESS TO MEMORY
           97
5W33
                  R42 9
                 R42 11 2+15 ADDRESS TO MEMORY
R42 13 2+15 ADDRESS TO MEMORY
R42 26 2+16 ADDRESS TO MEMORY
R42 28 2+16 ADDRESS TO MEMORY
5W34
           97
5W35
           97
           98
5W34
5W35
           98
```

```
6W10
        90
            E01
                     2+0 MULTIPLY TO REGISTER
                 1
                      2+1 MULTIPLY TO REGISTER
6W10
        91
            E01
                  5
6W10
        92
            E01
                 25
                      2+2 MULTIPLY TO REGISTER
6W10
        93
            E01 27
                      2+3 MULTIPLY TO REGISTER
        94
                      2+4 MULTIPLY TO REGISTER
6W10
            E02
                  1
        95
                     2+5 MULTIPLY TO REGISTER
6W10
            E02
        96
            E02 25
6W10
                     2+6 MULTIPLY TO REGISTER
                     2+7 MULTIPLY TO REGISTER
2+8 MULTIPLY TO REGISTER
        97
            E02
6W10
                27
        98
6W10
            E03
        99
                      2+9 MULTIPLY TO REGISTER
6W10
            E03
                  5
                     2+10 MULTIPLY TO REGISTER
6W10
       900
            E03 25
                     2+11 MULTIPLY TO REGISTER
       901
            E03 27
6W10
                     2+12 MULTIPLY TO
6W10
       902
            E04
                                         REGISTER
                  1
                      2+13 MULTIPLY TO REGISTER
6W10
       903
            E04
                  5
            E04 25
6W10
       904
                     2+14 MULTIPLY TO
                                        REGISTER
       905
            E04 27
                     2+15 MULTIPLY TO REGISTER
6W10
            E 0 5
                     2+16 MULTIPLY TO REGISTER
6W10
       906
                 1
                     2+17 MULTIPLY TO
2+18 MULTIPLY TO
       907
            E 0 5
                                        REGISTER
6W10
                  15
6W11
        90
            E05 25
                                         REGISTER
       91
            E05
                                        REGISTER
                27
                     2+19 MULTIPLY TO
6W11
        92
                      2+20 MULTIPLY TO REGISTER
6W11
            E06
                 1
        93
                     2+21 MULTIPLY TO REGISTER
6W11
            E06
                 5
6W11
        94
            E06 25
                     2+22 MULTIPLY TO REGISTER
                     2+23 MULTIPLY TO
2+24 MULTIPLY TO
6W11
        95
            E06
                27
                                         REGISTER
                      2+24 MULTIPLY
        96
            EO7
                                        REGISTER
6W11
                  1
6W11
        97
            E 07
                      2+25 MULTIPLY TO REGISTER
                     2+26 MULTIPLY TO REGISTER
        98
            E07 25
6W11
6W11
       99
            E07 27
                     2+27 MULTIPLY TO REGISTER
                      2+28 MULTIPLY TO
       900
            F01
                                        REGISTER
6W11
                  1
            F01
                     2+29 MULTIPLY TO REGISTER
       901
6W11
6W11
                      2+30 MULTIPLY TO REGISTER
       902
            F01 25
                     2+31 MULTIPLY TO REGISTER
            F01 27
6W11
       903
       904
            F02
                      2+32 MULTIPLY TO REGISTER
6W11
                 1
                      2+33 MULTIPLY TO REGISTER
6W11
       905
            F02
                  5
                      2+34 MULTIPLY TO REGISTER
            F02 25
6W11
       906
                     2+35 MULTIPLY TO REGISTER
            F02 27
6W11
       907
                      2+36 MULTIPLY TO REGISTER
        90
6W08
            F03
                 1
        91
            F03
                     2+37 MULTIPLY TO REGISTER
6W08
                 5
                     2+38 MULTIPLY TO REGISTER 2+39 MULTIPLY TO REGISTER
        92
            F03 25
6W08
        93
            F03
                27
6W08
6W08
        94
            F 0 4
                     2+40 MULTIPLY TO REGISTER
                  1
        95
            F04
                     2+41 MULTIPLY TO REGISTER
6W08
                 5
6W08
        96
            F04 25
                      2+42 MULTIPLY TO REGISTER
        97
                      2+43 MULTIPLY TO REGISTER
            F 0 4
6W08
                 27
6W08
        98
            F05
                      2+44 MULTIPLY TO REGISTER
                  1
6W08
        99
            F05
                      2+45 MULTIPLY TO REGISTER
            F05 25
                     2+46 MULTIPLY TO REGISTER
6W08
       900
            F05
                      2+47 MULTIPLY TO REGISTER
6W08
       901
                27
                     REDUCE MULT. 2
            F06
6W22
       902
6W09
            F06 15
                     2+59 REGISTER TO MULTIPLY, OPERAND (XJ)
       902
            F07
       908
                      REDUCE MULT. 1
6W21
                     2+59 REGISTER TO MULTIPLY, OPERAND 1(XK)
6W07
            F07 15
       902
                     2+0 REGISTER TO MULTIPLY, OPERAND 1(XK)
2+1 REGISTER TO MULTIPLY, OPERAND 1(XK)
6W12
        90
            G01 12
       91
6W12
            G01 14
6W12
        92
                      2+2 REGISTER TO MULTIPLY, OPERAND 1(XK)
            G01 16
                     2+3 REGISTER TO MULTIPLY, OPERAND 1(XK)
        93
6W12
            G01 18
       94
                     2+4 REGISTER TO MULTIPLY, OPERAND 1(XK)
6W12
            G02 12
6W12
       95
            G02 14
                     2+5 REGISTER TO MULTIPLY, OPERAND 1(XK)
                     2+6 REGISTER TO MULTIPLY, OPERAND 1(XK)
6W12
       96
            G02 16
                     2+7 REGISTER TO MULTIPLY, OPERAND 1(XK)
        97
6W12
            G02 18
```

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6W12
        98
             G03 12
                      2+8 REGISTER TO MULTIPLY, OPERAND 1(XK)
6W12
        99
                      2+9 REGISTER TO MULTIPLY, OPERAND 1(XK)
             G03 14
             G03 16
                      2+10 REGISTER TO MULTIPLY, OPERAND 1(XK)
 6W12
       900
                      2+11 REGISTER TO MULTIPLY,
 6W12
                                                   OPERAND 1(XK)
       901
             G03 18
 6W12
            G04 12
                                                   OPERAND
                                                            1(XK)
       902
                      2+12 REGISTER TO MULTIPLY,
6 W12
       903
             G04 14
                      2+13 REGISTER
                                     ŤO
                                        MULTIPLY, OPERAND
                                                            1 (XK)
                                        MULTIPLY, OPERAND
       904
            G04 16
6W12
                     2+14 REGISTER
                                    TO
                                        MULTIPLY, OPERAND
 6W12
       905
             G04 18
                      2+15 REGISTER
                                     ŤO
                                        MULTIPLY,
             G05 12
                                                   OPERAND 1(XK)
 6W1 2
       906
                     2+16 REGISTER TO
6 W1 2
            G05 14
                     2+17
                           REGISTER TO MULTIPLY.
                                                   OPERAND 1(XK)
       907
        90
 6W15
             005
                      2+18 REGISTER
                                        MULTIPLY,
                                                   OPERAND 1(XK)
                 16
                                     TO:
 6W15
             G05 18
                                     ŤO
                                        MULTIPLY.
                                                   OPERAND
                                                            1(XK)
                      2+19
                           REGISTER
 6W15
        92
             G06 12
                      2+20 REGISTER
                                        MULTIPLY, OPERAND
                                     ŤΟ
                                                            1(XK)
                                        MULTIPLY,
        93
 6W15
                                                   OPERAND
                                                            1(XK)
             G06 14
                      2.21 REGISTER
                                     TO
                      2+22 REGISTER
                                        MULTIPLY.
                                                            1(XK)
        94
             G06 16
                                                   OPERAND
 6W15
                                     ŤO
        95
                                        MULTIPLY,
 6W15
             G06 18
                      2*23 REGISTER
                                     TO
                                                   OPERAND
                                                            1(XK)
6W15
                     2*24
                          REGISTER
                                        MULTIPLY, OPERAND
        96
             H01 12
                                     TO
                                                            1(XK)
 6W15
        97
                                        MULTIPLY, OPERAND 1(XK)
             H<sub>0</sub>1 14
                     2+25 REGISTER TO
6W15
        98
                      2+26 REGISTER TO MULTIPLY, OPERAND 1(XK)
            H01 16
 6W15
        99
                     2+27 REGISTER TO
                                        MULTIPLY, OPERAND 1(XK)
MULTIPLY, OPERAND 1(XK)
             H01 18
6W15
       900
            H<sub>0</sub>2 12
                     2.28 REGISTER
                                     ŤO
                                        MULTIPLY, OPERAND
6W15
                                     TO
       901
            H 0 2
                 14
                     2+29 REGISTER
                                                            1(XK)
6W15
       902
                     2+30 REGISTER TO
                                        MULTIPLY, OPERAND 1(XK)
            H02
                 16
                                        MULTIPLY, OPERAND 1(XK)
6W15
       903
            H<sub>0</sub>2 18
                     2+31 REGISTER TO
6W15
                                        MULTIPLY, OPERAND 1(XK)
       904
            H03 12
                     2+32 REGISTER TO
                                        MULTIPLY, OPERAND 1(XK)
       905
            H03 14
6W15
                     2+33 REGISTER TO
                     2+34 REGISTER TO MULTIPLY, OPERAND 1(XK)
6W15
       906
            H03
                16
                     2+35 REGISTER TO MULTIPLY, OPERAND 1(XK)
6W15
       907
            H03 18
6W07
        90
            H04 12
       91
            H04 14
6W07
6W07
            H04 16
       92
6W07
        93
            H04 18
            H05
6W07
        94
                     2+40 REGISTER TO
                                        MULTIPLY, OPERAND 1(XK)
                 12
6W07
        95
            H05 14
                     2+41 REGISTER TO
                                        MULTIPLY,
                                                   OPERAND
        96
            H05 16
                                        MULTIPLY,
6W07
                                                   OPERAND 1(XK)
                     2+42 REGISTER TO
                                        MULTIPLY, MULTIPLY,
6W07
        97
            H05 18
                     2+43 REGISTER TO
                                                   OPERAND 1(XK)
6W07
        98
            H06
                     2+44 REGISTER
                                    TO
                                                   OPERAND 1(XK)
                 12
        99
            H06
                     2+45 REGISTER TO MULTIPLY, OPERAND 1(XK)
6W07
                 14
                     2+46 REGISTER TO MULTIPLY, OPERAND 1(XK)
6W07
       900
            H06 16
6W07
                     2+47 REGISTER TO MULTIPLY, OPERAND 1(XK)
       901
            H06 18
        95
                     REQUEST RELEASE
6W16
            H09
                  1
6W22
       905
            H10
                     GO MULT 1
                  1
        90
                     2+0 REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W14
            IO1 12
                          REGISTER TO MULTIPLY, OPERAND
6W14
             101 14
                     2+1
                                                          2(XJ)
                                   TO MULTIPLY, OPERAND
TO MULTIPLY, OPERAND
6W14
        92
                                                           2(XJ)
                     2+2 REGISTER
             101 16
        93
                          REGISTER
 6W14
             101 18
                     2 * 3
        94
                     2+4 REGISTER TO MULTIPLY, OPERAND
 6W14
             102
                                                           2(XJ)
                12
             102 14
 6 W 1 4
        95
                     2+5 REGISTER TO MULTIPLY, OPERAND 2(XJ)
 6W14
        96
             102 16
                     2+6 REGISTER TO MULTIPLY, OPERAND 2(XJ)
        97
                     2+7 REGISTER TO MULTIPLY, OPERAND 2(XJ)
 6W14
             102 18
        98
             103 12
 6W14
                     2+8 REGISTER TO MULTIPLY, OPERAND 2(XJ)
        99
             103 14
                     2+9 REGISTER TO MULTIPLY, OPERAND 2(XJ)
 6W14
                      2+10 REGISTER TO MULTIPLY, OPERAND 2(XJ)
 6W14
       900
             103
                 16
       901
 6W14
             103 18
                      2+11 REGISTER TO
                                        MULTIPLY, OPERAND 2(XJ)
 6W14
       902
             104 12
                                        MULTIPLY, OPERAND 2(XJ)
                      2+12 REGISTER TO
 6W14
       903
             104 14
                      2+13 REGISTER TO
                                        MULTIPLY, OPERAND 2(XJ)
             104 16
                                     ŤΟ
                                        MULTIPLY, OPERAND 2(XJ)
 6W14
       904
                      2+14 REGISTER
       905
                                        MULTIPLY, OPERAND
                                                            2(XJ)
 6W14
             104
                      2+15 REGISTER
                                     TO
                 18
                                    TO MULTIPLY, OPERAND 2(XJ)
6W14
             105 12
       906
                     2+16 REGISTER
             105 14
                     2+17 REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W14
       907
             105 16
                     2+18 REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W13
        90
        91
                     2+19 REGISTER TO MULTIPLY, OPERAND 2(XJ)
             105 18
6W13
```

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2+20 REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W13
        92
            I06 12
                      2+21 REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W13
        93
             106 14
        94
                      2+22 REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W13
             106 16
        95
                      2+23 REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W13
             106 18
        98
6W28
             107 23
                      ERROR MULT. 1
        92
             108 23
6W16
                      DOUBLE NO. 1
                      ERROR MULT. 1
             108
6W28
       900
                27
        91
                      ROUND NO. 1
6W16
            109
                  3
        90
             109
                 23
                      GO UNIT NO. 1
6W16
        96
            122 12
6W16
                      CLOCK
                      2+24 REGISTER TO MULTIPLY, OPERAND 2(XJ)
        96
            J01 12
6W13
                      2+25 REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W13
        97
            J01 14
                      2+26 REGISTER TO MULTIPLY, OPERAND 2(XJ)
        98
6W13
             J01 16
        99
                      2+27 REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W13
             J01 18
                      2+28 REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W13
       900
             J02
                12
                      2+29 REGISTER TO MULTIPLY, OPERAND 2(XJ)
2+30 REGISTER TO MULTIPLY, OPERAND 2(XJ)
       901
             J02 14
6W13
6W13
       902
             J02 16
                      2+31 REGISTER TO MULTIPLY, OPERAND 2(XJ)
6W13
       903
             J02 18
                      2+32 REGISTER TO MULTIPLY, OPERAND 2(XJ)
2+33 REGISTER TO MULTIPLY, OPERAND 2(XJ)
             J03 12
J03 14
6W13
       904
       905
6W13
                      2+34 REGISTER TO MULTIPLY, OPERAND 2(XJ)
             J03 16
6W13
       906
                      2+35 REGISTER TO MULTIPLY, OPERAND 2(XJ)
             J03 18
6W13
       907
        90
                      2+36 REGISTER TO MULTIPLY, OPERAND (XJ)
6W09
             J04 12
        91
                      2+37 REGISTER TO MULTIPLY, OPERAND (XJ)
6W09
             J04 14
                      2+38 REGISTER TO MULTIPLY, OPERAND (XJ)
             J04 16
J04 18
6W09
        92
                      2+39 REGISTER TO MULTIPLY: OPERAND (XJ)
        93
6W09
                      2+40 REGISTER TO MULTIPLY, OPERAND (XJ)
        94
             J05 12
6W09
                      2+41 REGISTER TO MULTIPLY, OPERAND (XJ)
6W09
        95
            J05 14
                      2+42 REGISTER TO MULTIPLY. OPERAND (XJ)
            J05 16
6W09
        96
                      2+43 REGISTER TO MULTIPLY, OPERAND (XJ)
        97
             J05 18
6W09
                      2+44 REGISTER TO MULTIPLY, OPERAND (XJ)
        98
            J06
6W09
                12
                      2+45 REGISTER TO MULTIPLY, OPERAND (XJ)
        99
6W09
             J06
                 14
                      2+46 REGISTER TO MULTIPLY, OPERAND (XJ)
6W09
       900
             J06 16
                      2+47 REGISTER TO MULTIPLY, OPERAND (XJ)
6W09
       901
            J06 18
        99
                      ERROR MULT. 2
             J07 23
6W28
6W16
       902
             J08 23
                      DOUBLE
                      ERROR MULT. 2
             J08 27
6W28
      901
6W16
            J09
                      ROUND
      901
                  3
             J09 23
                     GO UNIT NO. 2
6W16
       900
6W23
                     2+0 MULTIPLY TO DIVIDE (OPERAND) X++K
2+2 MULTIPLY TO DIVIDE (OPERAND) X++K
        90
            K01
                  1
6W23
        92
            K01
        91
                      2+1 MULTIPLY TO DIVIDE (OPERAND) X++K
6W23
            K01
                  3
6W20
        91
            K01 10
                      2+1 DIVIDE TO MULTIPLY RESULT
                      2+0 DIVIDE TO MULTIPLY RESULT
        90
6W20
            K01 11
                      2+3 MULTIPLY TO DIVIDE (OPERAND) X++K
6W23
        93
            K02
                                                (OPERAND) X++K
        95
                      2+5 MULTIPLY TO DIVIDE
6W23
            K02
                  2
        94
            K02
                      2+4 MULTIPLY TO DIVIDE
                                                (OPERAND) X++K
6W23
                  3
                      2+3 DIVIDE TO MULTIPLY RESULT
6W20
        93
             K02 10
                      2+2 DIVIDE TO MULTIPLY RESULT
        92
6W20
            K02
                 11
                      2+6 MULTIPLY TO DIVIDE (OPERAND) X++K
2+8 MULTIPLY TO DIVIDE (OPERAND) X++K
        96
            K03
6W23
                  1
6W23
        98
            K03
                      2+7 MULTIPLY TO DIVIDE (OPERAND) X++K
        97
6W23
             K03
                  3
6W20
        95
                      2+5 DIVIDE TO MULTIPLY RESULT
             K03
                10
        94
                      2+4 DIVIDE TO MULTIPLY RESULT
6W20
             K03 11
                      2+9 MULTIPLY TO DIVIDE (OPERAND) X++K
2+11 MULTIPLY TO DIVIDE (OPERAND) X++K
        99
            K04
6W23
                  1
6W23
       901
             K 0 4
                  2
                      2+10 MULTIPLY TO DIVIDE (OPERAND) X++K
             K04
       900
6W23
6W20
        97
             K04
                      2+7 DIVIDE TO MULTIPLY RESULT
                 10
                      2+6 DIVIDE TO MULTIPLY RESULT
        96
6W20
            K04
                 11
       902
            Kn5
                      2+12 MULTIPLY TO DIVIDE (OPERAND) X++K
6W23
                  1
                      2+14 MULTIPLY TO DIVIDE (OPERAND) X++K
            K05
6W23
       904
                  2
                      2+13 MULTIPLY TO DIVIDE (OPERAND) X++K
            K05
6W23
      903
                  3
```

```
6W20
         99
              K05 10
                        2+9 DIVIDE TO MULTIPLY RESULT
6W20
         98
              K05
                        2+8 DIVIDE TO MULTIPLY RESULT
                   11
                        2+15 MULTIPLY
              K06
                                          TO DIVIDE (OPERAND) X++K
6W23
       905
                        2+17 MULTIPLY TO DIVIDE (OPERAND) X++K
       907
6W23
              KO6
                        2+16 MULTIPLY TO DIVIDE (OPERAND) X++K
2+11 DIVIDE TO MULTIPLY RESULT
6W23
       906
              K06
6W20
       901
              K06 10
                        2+10 DIVIDE TO MULTIPLY RESULT
2+18 MULTIPLY TO DIVIDE (OPERAND) X++K
        900
6W20
              K06
                   11
         90
              K07
6W24
         92
              K07
                        2+20 MULTIPLY TO DIVIDE (OPERAND) X++K
6W24
                    2
        91
                        2+19 MULTIPLY TO DIVIDE (OPERAND) X++K
              K07
6W24
                    3
                        2+13 DIVIDE TO MULTIPLY RESULT
2+12 DIVIDE TO MULTIPLY RESULT
REQUEST RELEASE
              K07
6W20
       903
                   10
       902
              KO7
6W20
                   11
              K09
       905
6W16
6W22
       906
              K10
                        2+21 MULTIPLY TO DIVIDE (OPERAND) X++K
        93
6W24
              L01
                    1
                        2+23 MULTIPLY TO DIVIDE (OPERAND) X++K
6W24
        95
              L01
                    2
        94
                    3
                        2+22 MULTIPLY TO DIVIDE
                                                      (OPERAND) X++K
6W24
              L01
                        2+15 DIVIDE TO MULTIPLY RESULT
6W20
       905
              L01 10
                        2+14 DIVIDE TO MULTIPLY RESULT
2+24 MULTIPLY TO DIVIDE (OPERAND) X++K
6W20
       904
              L01
                   11
6W24
        96
              L02
                    1
        98
                    2
                        2+26 MULTIPLY TO DIVIDE (OPERAND) X++K
              L02
6W24
                        2+25 MULTIPLY TO DIVIDE (OPERAND) X++K
        97
6W24
              L02
                    3
       907
                        2+17 DIVIDE TO MULTIPLY RESULT
6W20
              L02
                   10
                        2+16 DIVIDE TO MULTIPLY RESULT
6W20
       906
              L02 11
                        2+27 MULTIPLY TO DIVIDE (OPERAND) X++K
2+29 MULTIPLY TO DIVIDE (OPERAND) X++K
        99
             L03
6W24
                    1
6W24
       901
              L03
             L03
       900
                        2+28 MULTIPLY TO DIVIDE (OPERAND) X++K
6W24
                    3
                        2+19 DIVIDE TO MULTIPLY RESULT
        91
              L03 10
6W21
                        2+18 DÍVIDE TO MULTIPLY RESULT
2+30 MULTIPLY TO DIVIDE (OPERAND) X++K
        90
              L03 11
6W21
6W24
       902
              L04
                    1
6W24
       904
              L04
                    2
                        2+32 MULTIPLY TO DIVIDE (OPERAND) X++K
       903
             L04
                        2+31 MULTIPLY TO DIVIDE (OPERAND) X++K
6W24
                    3
                        2+21 DIVIDE TO MULTIPLY RESULT 2+20 DIVIDE TO MULTIPLY RESULT
6W21
        93
              L04 10
        92
              L04 11
6W21
                        2+33 MULTIPLY TO DIVIDE (OPERAND) X++K
             L05
6W24
       905
                    1
                        2+35 MULTIPLY TO DIVIDE (OPERAND) X++K
              L05
       907
6W24
             L05
       906
                        2+34 MULTIPLY TO DIVIDE (OPERAND) X++K
6W24
                    3
                        2+23 DIVIDE TO MULTIPLY RESULT
2+22 DIVIDE TO MULTIPLY RESULT
2+36 MULTIPLY TO DIVIDE (OPERAND) X++K
        95
              L05
6W21
                  11
        94
              L05 11
6W21
         90
6W25
             L06
        92
                        2+38 MULTIPLY TO DIVIDE (QPERAND) X++K
6W25
              L06
              L06
                        2+37 MULTIPLY TO DIVIDE (OPERAND) X++K
6W25
        91
                    3
                        2+25 DIVIDE TO MULTIPLY RESULT
2+24 DIVIDE TO MULTIPLY RESULT
         97
              L06 10
6W21
         96
6W21
              L06
                  11
                        2+39 MULTIPLY TO DIVIDE (OPERAND) X++K
2+41 MULTIPLY TO DIVIDE (OPERAND) X++K
6W25
         93
              LO7
                    1
             L07
         95
6W25
6W25
             L 0 7
                        2+40 MULTIPLY TO DIVIDE (OPERAND) X++K
2+27 DIVIDE TO MULTIPLY RESULT
         94
              LO7
         99
6W21
                   10
             L07
        98
                        2+26 DIVIDE TO MULTIPLY RESULT
6W21
                   11
              LOB
6W16
         94
                        XMIT RESULT NO. 2
                    2
         93
6W16
              L08 26
                        XMIT RESULT NO. 1
        96
6W25
                        2+42 MULTIPLY TO DIVIDE (OPERAND) X++K
              M01
                        2+44 MULTIPLY TO DIVIDE (OPERAND) X++K
2+43 MULTIPLY TO DIVIDE (OPERAND) X++K
         98
6W25
              M01
                    2
         97
6W25
              M01
                    3
       901
                        2+29 DIVIDE TO MULTIPLY RESULT
6W21
              M01 10
                        2+28 DIVIDE TO MULTIPLY RESULT
              M01 11
6W21
       900
                        2+45 MULTIPLY TO DIVIDE (OPERAND) X++K
        99
6W25
              M02
                        2+47 MULTIPLY TO DIVIDE (OPERAND) X++K
2+46 MULTIPLY TO DIVIDE (OPERAND) X++K
6W25
       901
              MO2
                    2
6W25
       900
              M02
                    3
              M02 10
       903
                        2+31 DIVIDE TO MULTIPLY RESULT
6W21
              M02 11
6W21
       902
                        2+30 DIVIDE TO MULTIPLY RESULT
6W25
       903
              MO3
                        2+0 MULTIPLY TO DIVIDE (OPERAND) X++J
```

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2 2+2 MULTIPLY TO DIVIDE (OPERAND) X++J
6W25
        905
               M03
                          2+1 MULTIPLY TO DIVIDE (OPERAND) X++J
2+33 DIVIDE TO MULTIPLY RESULT
6W25
        904
               MD3
                      3
6W21
        905
               M03
                    10
                          2+32 DIVIDE TO MULTIPLY RESULT
        904
6W21
               M03 11
                           2+3 MULTIPLY TO DIVIDE (OPERAND) X++J
6W25
        906
               M04
                          2+5 MULTIPLY TO DIVIDE OPERAND X++J
         90
               M04
6W26
                          2+4 MULTIPLY TO DIVIDE (OPERAND) X++J
2+35 DIVIDE TO MULTIPLY RESULT
2+34 DIVIDE TO MULTIPLY RESULT
6W25
               M04
        907
                      3
        907
               M04
6W21
                     10
               M 0 4
6W21
        906
                     11
         91
               MO5
6W26
         93
               M05
6W26
               M05
         92
6W26
                      3
                          2+37 DIVIDE TO MULTIPLY RESULT 2+36 DIVIDE TO MULTIPLY RESULT
               M05 10
6W22
          91
6W22
         90
               M05
                     11
                          2+9 MULTIPLY TO DIVIDE OPERAND X++J
2+11 MULTIPLY TO DIVIDE OPERAND X++J
2+10 MULTIPLY TO DIVIDE OPERAND X++J
         94
6W26
               M06
6W26
         96
               M06
         95
6W26
               M06
               M06 10
6W22
         93
                          2+39 DIVIDE TO MULTIPLY RESULT
                          2+38 DÍVIDE TO MULTIPLY RESULT
2+12 MULTIPLY TO DIVIDE OPERAND X++J
2+14 MULTIPLY TO DIVIDE OPERAND X++J
         92
               M06 11
6W22
         97
6W26
               M07
6W26
         99
               M07
                           2+13 MULTIPLY TO DIVIDE OPERAND X++J
         98
6W26
               M07
                          2+41 DIVIDE TO MULTIPLY RESULT 2+40 DIVIDE TO MULTIPLY RESULT
         95
               M07 10
6W22
6W22
         94
               M07
                     11
                          2+15 MULTIPLY TO DIVIDE OPERAND X++J
6W26
        900
               No1
                          2+17 MULTIPLY TO DIVIDE OPERAND X++J
6W26
        902
               N01
                          2+16 MULTIPLY TO DIVIDE OPERAND X++J
6W26
        901
               NO1
                      3
                          2+43 DIVIDE TO MULTIPLY RESULT
2+42 DIVIDE TO MULTIPLY RESULT
2+18 MULTIPLY TO DIVIDE OPERAND X++J
         97
               N01 10
6W22
         96
6W22
               NO1 11
        903
6W26
               N02
                          2+20 MULTIPLY TO DIVIDE OPERAND X++J
2+19 MULTIPLY TO DIVIDE OPERAND X++J
6W26
        905
               N02
6W26
        904
               N02
                          2+45 DIVIDE TO MULTIPLY RESULT 2+44 DIVIDE TO MULTIPLY RESULT
6W22
         99
               N02 10
         98
6W22
               N02 11
                          2+21 MULTIPLY TO DIVIDE OPERAND X++J
2+23 MULTIPLY TO DIVIDE OPERAND X++J
6W26
        906
               N03
         90
               N 0 3
6W27
                      2
                          2+22 MULTIPLY TO DIVIDE OPERAND X++J
6W26
        907
               N03
                          2+47 DIVIDE TO MULTIPLY RESULT
               N03 10
6W22
        901
                          2+46 DIVIDE TO MULTIPLY RESULT
2+26 MULTIPLY TO DIVIDE OPERAND X++J
               N03 11
6W22
        900
         93
6W27
               N 0 4
                          2+25 MULTIPLY TO DIVIDE OPERAND X++J
2+24 MULTIPLY TO DIVIDE OPERAND X++J
         92
6W27
               N 0 4
                      6
6W27
         91
               N 0 4
         96
               NO4 23
                          2+29 MULTIPLY TO DIVIDE OPERAND X++J
6W27
                          2+28 MULTIPLY TO DIVIDE OPERAND X++J
2+27 MULTIPLY TO DIVIDE OPERAND X++J
         95
6W27
               NO4 25
6W27
         94
               NO4 27
                          2+32 MULTIPLY TO DIVIDE OPERAND X++J
6W27
         99
               N05
                      4
                          2+31 MULTIPLY TO DIVIDE OPERAND X++J
6W27
         98
               N05
                          2+30 MULTIPLY TO DIVIDE OPERAND X++J
2+35 MULTIPLY TO DIVIDE OPERAND X++J
2+34 MULTIPLY TO DIVIDE OPERAND X++J
         97
               N 0 5
6W27
                      8
               N 0 5
6W27
        902
                    23
               N05
6W27
        901
                    25
               N05 27
                          2+33 MULTIPLY TO DIVIDE OPERAND X++J
6W27
        900
                          2+38 MULTIPLY TO DIVIDE OPERAND X++J
6W27
        905
               N 0 6
                          2+37 MULTIPLY TO DIVIDE OPERAND X++J
        904
6W27
               N06
                      6
                           2+36 MULTIPLY TO DIVIDE OPERAND X++J
        903
6W27
               N06
                      8
                          2+41 MULTIPLY TO DIVIDE X++J
6W28
         90
               N06 23
                          2+40 MULTIPLY TO DIVIDE OPERAND X++J
               NO6 25
6W27
        907
                          2+39 MULTIPLY TO DIVIDE OPERAND X++J
6W27
        906
               N06 27
         93
                          2+44 MULTIPLY TO DIVIDE X++J
6W28
               N07
                          2+43 MULTIPLY TO DIVIDE X*+J
2+42 MULTIPLY TO DIVIDE X*+J
         92
6W28
               NO7
                      6
6W28
         91
               NO7
                      8
               N07 12
                          D P MULT 1
6W22
        907
               NO7 19
                          D P MULT 2
6W22
        908
```

```
6W28 96 N07 23 2+47 MULTIPLY TO DIVIDE X++J
6W28 95 N07 25 2+46 MULTIPLY TO DIVIDE X++J
6W28 94 N07 27 2+45 MULTIPLY TO DIVIDE X++J
```

```
A37
7W05
       900
                   2
                       2+0 MEMORY TO REGISTER
                       2+1 MEMORY TO REGISTER
7W05
       901
             A37
                 26
                       2+2 MEMORY TO REGISTER
2+3 MEMORY TO REGISTER
7W05
       902
             A38
                   2
7W05
             8 E A
       903
                  26
                       2+4 MEMORY TO REGISTER
7W05
       904
             A39
                   2
             A39
7W05
       905
                       2+5 MEMORY TO REGISTER
                  26
                       2*6 MEMORY TO REGISTER
2*7 MEMORY TO REGISTER
7W05
       906
             A40
                   2
7W05
       907
             A40
                  26
                       2+8 MEMORY TO REGISTER
        90
7W05
             A41
                   2
                       2+9 MEMORY TO REGISTER
7W05
        91
             A41
                 26
        92
                       2+10 MEMORY TO REGISTER
7W05
             A42
                   2
                       2+11 MEMORY TO REGISTER
2+12 MEMORY TO REGISTER
        93
7W05
             A42
                  26
        94
             B37
7W05
                   2
        95
             B37
                 26
                       2+13 MEMORY TO REGISTER
7W05
                       2+14 MEMORY TO REGISTER
7W05
        96
             B38
                   2
             B38
                       2+15 MEMORY TO REGISTER
7W06
       900
                  26
                       2+16 MEMORY TO REGISTER
2+17 MEMORY TO REGISTER
7W06
       901
             B39
                   2
             B39
7W06
       902
                  26
7W06
       903
             B40
                       2+18 MEMORY TO REGISTER
                  2
                       2+19 MEMORY TO REGISTER
       904
             B40
                 26
7W06
       905
                       2+20 MEMORY TO REGISTER
7W06
             B41
                   2
                       2+21 MEMORY TO REGISTER
7W06
       906
             B41 26
                       2+22 MEMORY TO REGISTER
7W06
       907
             B42
                   2
                       2+23 MEMORY TO REGISTER
        90
7W06
             B42
                  26
        91
             C37
                       2+24 MEMORY TO REGISTER
7W06
                   2
        92
             C37 26
                       2+25 MEMORY TO REGISTER
7W06
             C38
        93
                       2+26 MEMORY TO REGISTER
7W06
                   2
7W06
        94
             C38
                  26
                       2+27 MEMORY TO REGISTER
                       2+28 MEMORY TO REGISTER
2+29 MEMORY TO REGISTER
        95
7W06
             C39
                   2
        96
                 26
7W06
             C39
                       2+30 MEMORY TO REGISTER
       900
             C40
7W07
                  2
             C40 26
                       2+31 MEMORY TO REGISTER
7W07
       901
                       2+32 MEMORY TO REGISTER
7W07
       902
             C41
                   2
             C41 26
                       2+33 MEMORY TO REGISTER
7W07
       903
       904
                       2+34 MEMORY TO REGISTER
7W07
             C42
                   2
                       2+35 MEMORY TO REGISTER
7W07
       905
             C42
                 26
                       2+0 MULTIPLY TO REGISTER
        90
             D34
7W10
                   2
                       2+1 MULTIPLY TO REGISTER
2+2 MULTIPLY TO REGISTER
2+3 MULTIPLY TO REGISTER
        91
7W10
             D34
                  26
             D35
7W10
        92
                   2
        93
             D35
                 26
7W10
7W10
        94
             D36
                   2
                       2+4 MULTIPLY TO REGISTER
        95
                       2+5 MULTIPLY TO REGISTER
7W10
             D36 26
7W10
        96
             D37
                       2+6 MULTIPLY TO REGISTER
                   2
                       2+7 MULTIPLY TO REGISTER
2+8 MULTIPLY TO REGISTER
7W10
        97
             D37
                  26
        98
             D38
7W10
                   2
7W10
                       2+9 MULTIPLY TO REGISTER
        99
             D38
                  26
                       2+10 MULTIPLY TO REGISTER
7W10
       900
             D39
                   2
       901
             D39
                       2+11 MULTIPLY TO REGISTER
7W10
                  26
7W10
       902
             D40
                       2+12 MULTIPLY TO REGISTER
                   2
                       2+13 MULTIPLY TO REGISTER
7W10
       903
             D40
                  26
             D41
                       2+14 MULTIPLY TO REGISTER
7W10
       904
                   2
                       2+15 MULTIPLY TO REGISTER
7W10
       905
             D41 26
             D42
                       2+16 MULTIPLY TO REGISTER
7W10
       906
                  . 2
                       2417 MULTIPLY TO REGISTER PATA 2+30 REG. TO MEM.
7W10
       907
             D42
                  26
        94
             E26
7W09
                   3
                 21
7W09
        96
             E26
                       DATA 2#32 REG. TO MEM.
                       DATA 2+31 REG. TO MEM.
DATA 2+33 REG. TO MEM.
DATA 2+35 REG. TO MEM.
        95
7W09
             E26
                  25
             E27
7W09
        97
                   3
             E27 21
7W09
        99
                       DATA 2+34 REG. TO MEM.
7W09
        98
             E27 25
```

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2+18 MULTIPLY TO REGISTER
7W11
         90
              E34
                    2
                               MULTIPLY TO REGISTER MULTIPLY TO REGISTER MULTIPLY TO REGISTER
7W11
         91
              E34
                   26
                         2+19
              E35
         92
7W11
                     2
                         2+20
7W11
         93
              E35
                   26
                         2+21
                         2+22 MULTIPLY
2+23 MULTIPLY
                                           TO REGISTER
         94
7W11
              E36
                     . 5
7W11
         95
              E36
                   26
                               MULTIPLY
MULTIPLY
MULTIPLY
MULTIPLY
7W11
         96
              E37
                     2
                         2+24
                                            ŤO
                                               REGISTER
                                            TO REGISTER
         97
              E37
7W11
                    26
                         2+25
7W11
         98
                         2+26
              E38
                     2
7W11
                                            ŤO
         99
              E38
                    26
                         2+27
                         2+27 MULTIPLY TO REGISTER
2+28 MULTIPLY TO REGISTER
2+30 MULTIPLY TO REGISTER
2+31 MULTIPLY TO REGISTER
2+32 MULTIPLY TO REGISTER
2+34 MULTIPLY TO REGISTER
2+34 MULTIPLY TO REGISTER
7W11
        900
              E39
                     2
                   26
7W11
        901
              E39
       902
7W11
              E40
                    2
7W11
       903
              E40
                    26
       904
7W11
              E41
       905
                   26
7W11
              E41
                               MULTIPLY TO
7W11
       906
              E42
                    .5
                         2+34
                                               REGISTER
                               MULTIPLY TO
7W11
       907
              E42
                                               REGISTER
                   26
                         2+35
                               2+0 REG. TO MEM.
2+2 REG. TO MEM.
7W08
                         DATA
       900
              F26
                     3
              F26
       902
                   21
                         DATA
              F26
                         DATA 2+1 REG. TO MEM.
7W08
       901
                   25
7W08
       903
              F27
                     3
                         DATA
                               2+3 REG. TO MEM.
              F27
                                    REG.
                         DATA
DATA
                                            TO
                   21
                                                MEM.
7W08
       905
                                2+5
7W08
       904
              F27
                   25
                                2+4
                                     REG.
                                                MEM.
                                    REG.
7W08
              F28
                                            TO
                                                MEM.
       906
                         DATA
                               2+6
                     3
              F28
                         DATA
                                    REG.
                                            ŤO
7W08
         90
                   21
                               2+8
                                               MEM.
       907
                   25
              F28
                         DATA
                               2+7 REG. TO MEM.
7W08
         91
93
                                    REG.
              F29
                         DATA
                               2+9
                                            TO MEM.
7W08
                     3
              F29
                               2+11 REG. TO MEM.
                   21
7W08
                         DATA
         92
              F29
                         DATA
                               2+10
                                      REG.
                                             TO
7W08
                    25
         94
              F30
                               2+12 REG.
                                             TO
                    3
                         DATA
         96
                               2+14
              F30
                                      REG.
                         DATA
                                             TO MEM.
7W08
                   21
7W08
         95
              F30
                    25
                         DATA
                               2+13 REG.
                                             TO
                                                MEM.
                               2+15
2+17
         97
              F31
                                      REG.
7W08
                     3
                         DATA
                                             TO
                                                 MEM.
              F31
                   21
                                      REG.
                                                 MEM.
7W08
         99
                         DATA
                                             TO
         98
              F31
                               2+16 REG.
7W08
                   25
                         DATA
                                             TO MEM.
                               2+18 REG.
7W09
              F32
                         DATA
                                             TO MEM.
       900
                     3
              F32
                   21
                         DATA
                               2+20
                                      REG.
                                             TO MEM.
7W09
       902
                               2+19
              F32
                                     REG.
                                             TO MEM.
       901
                         DATA
7W09
                    25
              F33
                               2+21 REG.
                                             TO MEM.
7W09
       903
                         DATA
                     3
                               2+23 REG.
7W09
       905
              F33
                         DATA
                                             TO MEM.
                   21
                               2+22 REG.
7W09
       904
              F33
                         DATA
                                             TO MEM.
                    25
                                      REG.
                                             TO MEM.
              F34
                         DATA
                               2+24
7W09
       906
                     3
                               2+26
              F34
                                      REG.
         90
7W09
                   21
                         DATA
7W09
        907
              F34
                         DATA 2+25
                                      REG.
                                             TO MEM.
                   25
                         DATA 2+27
7W09
         91
              F35
                     3
                                      REG. TO MEM.
                         DATA 2+29 REG. TO MEM.
DATA 2+28 REG. TO MEM.
         93
              F35
                   21
7W09
7W09
         92
              F35
                    25
                         INCREMENT
                                      RESULT BIT 2+0
              F38
7W21
        900
                     2
7W21
       901
              F38
                   26
                         INCREMENT RESULT BIT
                         INCREMENT RESULT BIT
7W21
        902
              F39
                                                    2 • 2
                     2
       903
              F39
                         INCREMENT RESULT BIT 2+3
7W21
                   26
                         INCREMENT RESULT INCREMENT RESULT
7W21
        904
              F40
                                               BIT
                    · 2
       905
                                                     2+5
7W21
              F40
                    26
                                                BIT
              F41
                         INCREMENT RESULT
7W21
       906
                     2
                                               BIT
                                                     2 * 6
7W21
                   26
                         INCREMENT RESULT
                                                     2 * 7
       907
              F41
                                               BIT
                         INCREMENT RESULT BIT
                                                    2 + 8
              F42
7W21
        908
                     2
              F42
         90
                   26
                         INCREMENT RESULT BIT 2+9
7W21
                         A TO INCR.
A TO INCREMENT
         96
              G26
                                                  GO
7W19
                     5
         91
7W19
              026
                                                  1
7W19
         90
              G26
                           TO
                               INCREMENT
                                                   0
                   10
                               INCREMENT
7W19
         92
              G26
                   26
                           TO
```

```
7W16
       904
            G27
                      MULTIPLY TO X
7W16
       905
             627
                  5
                      MULTIPLY TO X
                                            2
             G27
7W16
       903
                 16
                      MULTIPLY TO
7W16
            G27
       902
                      MULTIPLY TO X
                                            GO
                 17
7W17
        90
             G28
                      MEMORY TO D
                      MEMORY TO D
7W17
        91
             G28
7W17
        92
             G28
                      MEMORY TO D
                 16
                      MEM. TO D GO
2+0 REGISTER TO MULTIPLY, OPERAND 1(XK)
7W19
        94
             G28
                 17
        90
            G29
7W12
                  3
        92
                      2+2 REGISTER TO MULTIPLY, OPERAND 1(XK)
            G29
                 21
7W12
7W12
        91
             G29
                      2+1 REGISTER TO MULTIPLY, OPERAND 1(XK)
                      2+3 REGISTER TO MULTIPLY, OPERAND 1(XK)
        93
7W12
            G30
                  3
            G30 21
7W12
        95
                      2+5 REGISTER TO MULTIPLY, OPERAND 1(XK)
7W12
        94
             G30
                 25
                      2+4 REGISTER
                                     TO
                                        MULTIPLY, OPERAND 1(XK)
        96
                      2+6 REGISTER
                                    TO MULTIPLY, OPERAND 1(XK)
7W12
             G31
                  3
            G31 21
        98
                                     TO MULTIPLY, OPERAND I(XK)
7W12
                      2+8 REGISTER
                      2+7 REGISTER TO MULTIPLY, OPERAND 1(XK)
        97
7W12
             G31 25
7W12
        99
             G32
                      2+9 REGISTER TO MULTIPLY, OPERAND 1(XK)
                  3
                      2+11 REGISTER TO MULTIPLY, OPERAND 1(XK)
2+10 REGISTER TO MULTIPLY, OPERAND 1(XK)
            G32 21
7W12
       901
7W12
       900
             G32 25
                      2+12 REGISTER TO MULTIPLY, OPERAND 1(XK)
7W12
       902
            G33
                  - 3
             G33 21
                      2+14 REGISTER TO MULTIPLY, OPERAND 1(XK)
7W12
       904
                      2+13 REGISTER TO MULTIPLY, OPERAND 1(XK)
7W12
       903
             G33 25
                      2+15 REGISTER TO MULTIPLY, OPERAND 1(XK)
7W12
       905
             G34
                  3
                      2+17 REGISTER TO MULTIPLY, OPERAND 1(XK)
2+16 REGISTER TO MULTIPLY, OPERAND 1(XK)
       907
7W12
             G34 21
7W12
       906
             G34 25
7W20
        93
             G37
                      EXCHANGE
        91
            G38
7W21
                      INCREMENT RESULT BIT 2+10
                  2
                      INCREMENT RESULT BIT 2+11
INCREMENT RESULT BIT 2+12
        92
7W21
            G38
                 26
        93
            G39
7W21
                  2
        94
                      INCREMENT RESULT BIT 2+13
             G39
7W21
                 26
7W21
        95
             G40
                  2
                      INCREMENT RESULT BIT 2+14
        96
                      INCREMENT RESULT BIT 2+15
            G40 26
7W21
                      INCREMENT RESULT BIT 2+16
INCREMENT RESULT BIT 2+17
        97
7W21
             G41
                  2
        98
7W21
             G41
                 26
       904
                      X TO MEMORY
7W20
             H26
                                            GO
7W20
      905
             H26
                      X TO MEMORY
                  6
                      X TO MEMORY
7W20
       906
             H26
7W20
       907
            H26
                 27
                      X TO MEMORY
7W17
       902
             H27
                      INCREMENT TO A
                      INCREMENT TO A
            H27
7W17
      903
                  5
7W17
            H27 16
                      INCREMENT TO
       901
                      INCREMENT TO
            H27
                                            GO
7W17
       900
                 17
        93
                      INCREMENT TO
7W16
            H28
                                    X
                                            1
        94
7W16
            H28
                  5
        92
            H28
                      INCREMENT TO X
7W16
                16
7W16
                      INCREMENT TO X
        91
            H28
                 17
                                            GO
                      2+18 REGISTER TO MULTIPLY, OPERAND 1(XK)
        90
            H29
7W15
                  3
                      2+20 REGISTER TO MULTIPLY, OPERAND 1(XK)
        92
            H29
                 21
7W15
                      2+19 REGISTER TO MULTIPLY, OPERAND 1(XK)
            H29
7W15
        91
                 25
                      2+21 REGISTER TO MULTIPLY, OPERAND 1(XK)
7W15
        93
             H30
                  3
                      2+23 REGISTER TO MULTIPLY, OPERAND 1(XK)
7W15
        95
             H30
                 21
                      2+22 REGISTER TO MULTIPLY, OPERAND 1(XK)
        94
7W15
            H30 25
7W15
        96
                      2+24 REGISTER TO MULTIPLY, OPERAND
             H31
                  3
                      2+26 REGISTER TO MULTIPLY, OPERAND 1(XK)
7W15
        98
             H31
                 21
                      2+25 REGISTER TO MULTIPLY, OPERAND 1(XK)
        97
7W15
             H31 25
                      2+27 REGISTER TO MULTIPLY, OPERAND 1(XK)
7W15
        99
             H32
                      2+29 REGISTER TO MULTIPLY, OPERAND 1(XK)
7W15
       901
             H32 21
                      2+28 REGISTER TO MULTIPLY, OPERAND 1(XK)
7W15
       900
            H32 25
                      2+30 REGISTER TO MULTIPLY, OPERAND 1(XK)
7W15
       902
            H33
                  3
                      2+32 REGISTER TO MULTIPLY, OPERAND 1(XK)
7W15
            H33 21
       904
                     2+31 REGISTER TO MULTIPLY, OPERAND 1(XK)
7W15
       903
            H33 25
```

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2+33 REGISTER TO MULTIPLY, OPERAND 1(XK)
7W15
      905
            H34
                  3
                     2+35 REGISTER TO MULTIPLY, OPERAND 1(XK)
7W15
      907
            H34
                21
                     2+34 REGISTER TO MULTIPLY, OPERAND 1(XK)
            H34
7W15
       906
                 25
7W26
        90
            H37
                     2+0 ADD
                              TO REGISTER
                  2
7W26
        91
            H37
                              TO REGISTER
                     2+1 ADD
                 26
7W26
        92
            H38
                 2
                     2+2 ADD
                              TO REGISTER
7W26
       93
            H38
                 26
                     2 * 3
                         ADD
                              TO REGISTER
7W26
       94
            H39
                     2+4
                              TO
                          ADD
                                 REGISTER
                 . 2
7W26
        95
            H39
                              TO
                                 REGISTER
                26
                     2+5
                         ADD
       96
7W26
                                 REGISTER
            H40
                 2
                     2+6
                         ADD
                              TO
       97
7W26
            H40
                26
                     2+7
                         ADD
                              TO
                                 REGISTER
       98
7W26
            H41
                     2+8
                         ADD
                              TO
                                 REGISTER
                  2
       99
7W26
            H41 26
                     2+9 ADD TO REGISTER
7W26
                     2+10 ADD TO REGISTER
      900
            H42
                 2
                     2+11 ADD TO REGISTER
7W26
      901
            H42 26
            122 12
                     CLOCK
7W16
      901
                     XL TO ADD (2)
7W18
      904
            126
                                           GO
                 5
7W18
      906
            126
                                          1
7W18
      905
            126 10
                     XL TO ADD (2)
                                           O
                     KTO MULTIPLÝ
                                          GO
7W17
                                    (1)
            126 21
7W17
       94
            126 24
                     K TO MULTIPLY (1)
                                           0
      907
                     XL TO ADD (2)
K TO MULTIPLY
7W18
            126
                26
                                           2
       95
            127
7W17
                 5
                                    (1)
7W16
       99
            127
                     XJ TO MULTIPLY (2)
                                           GO
            127
                     K TO MULTIPLY (1)
7W17
       96
                                           2
                10
            127
7W17
                       TO MULTIPLY (2)
       98
                21
            127
7W17
       99
                24
                       TO MULTIPLY (2)
                                           2
            127
                     J TO MULTIPLY (2)
       97
7W17
                26
                                           0
       93
7W19
            128
                     ADD TO X
7W16
       90
            128
                 - 5
                     ADD TO
7W16
            128 16
                     ADD
                         TO X
      907
                                           ٥
      906
            128
                     ADD
                         TO X
                                          GO
7W16
                17
       90
            129
                         REGISTER TO MULTIPLY, OPERAND 2(XJ)
                     2+0
7W14
                 3
       92
            129
                21
                     2 * 2
                         REGISTER TO MULTIPLY, OPERAND 2(XJ)
7W14
            129
                                   TO MULTIPLY, OPERAND 2(XJ)
                     2+1
                         REGISTER
       91
7W14
                25
7W14
       93
            130
                     2+3
                         REGISTER TO MULTIPLY, OPERAND 2(XJ)
                 3
7W14
       95
            130
                     2+5
                         REGISTER
                                   TO
                                      MULTIPLY, OPERAND 2(XJ)
                21
       94
                         REGISTER TO
                                      MULTIPLY, OPERAND 2(XJ)
7W14
                25
                     2+4
            130
                                      MULTIPLY, OPERAND 2(XJ)
7W14
       96
                     2+6
                         REGISTER TO
            131
                 3
                     2+8 REGISTER TO
       98
                                      MULTIPLY, OPERAND 2(XJ)
7W14
            131 21
7W14
       97
            131 25
                         REGISTER TO MULTIPLY, OPERAND 2(XJ)
                     2+7
       99
                     2+9 REGISTER TO MULTIPLY, OPERAND 2(XJ)
7W14
            132
                 3
                     2+11 REGISTER TO MULTIPLY, OPERAND 2(XJ)
7W14
      901
            132 21
                          REGISTER
                                        MULTIPLY, OPERAND 2(XJ)
      900
7W14
            132 25
                     2+10
                                     TO
                                        MULTIPLY, OPERAND 2(XJ)
7W14
      902
            133
                 3
                     2+12 REGISTER
                                     ŤO
            133 21
                                        MULTIPLY, OPERAND 2(XJ)
7W14
      904
                     2-14 REGISTER
                                     40
                                        MULTIPLY, OPERAND
                                                            2(XJ)
7W14
      903
            133
                 25
                     2+13 REGISTER
                                     TO
                     2+15 REGISTER TO MULTIPLY, OPERAND 2(XJ)
7W14
      905
            134
                 3
7W14
      907
            134 21
                     2+17
                          REGISTER TO MULTIPLY, OPERAND 2(XJ)
                     2+16 REGISTER TO MULTIPLY, OPERAND 2(XJ)
7W14
      906
            134
                25
7W20
      900
            J26
                  5
                     D**1 TO X**1
7W20
      901
            J26
                  7
                     D++2 TO X++2
7W20
      902
            J26
                     D**3
                              X++3
                10
                          TO
7W20
                     D++4
                           TO X++4
      903
            J26
                21
       98
            J26
7W20
                          TO X++5
                24
                     D++5
       95
                          INCR.
                                          GO
7W19
            J27 21
                     X TO
            J27 24
7W18
       90
                     X TO INCREMENT
                                          0
7W18
       91
            J28
                 5
                     X TO INCREMENT
                     XJ TO ADD (1)
                                          GO
7W18
      900
            J28
       92
                     X TO INCREMENT
7W18
            J28
                                          2
                10
7W18
      902
            J28
                21
                     XJ TO ADD (1)
            J28 24
7W18
      903
                     XJ TO ADD (1)
                                           2
```

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7W18
      901
            J28 26
                     XJ TO ADD (1)
                      2+18 REGISTER TO MULTIPLY, OPERAND 2(XJ)
7W13
        90
            J29
                      2+20 REGISTER TO MULTIPLY, OPERAND 2(XJ)
            J29
7W13
                 21
        92
                      2+19 REGISTER TO MULTIPLY, OPERAND
                                                             2(XJ)
7W13
        91
            J29
                25
                      2+21 REGISTER TO MULTIPLY, OPERAND 2(XJ)
7W13
        93
            J30
                  3
                      2+23 REGISTER TO MULTIPLY, OPERAND 2(XJ)
                21
7W13
        95
            J30
                     2+22 REGISTER TO MULTIPLY, OPERAND 2(XJ)
2+24 REGISTER TO MULTIPLY, OPERAND 2(XJ)
        94
7W13
            J30 25
        96
7W13
             J31
                 -3
                      2+26 REGISTER TO MULTIPLY, OPERAND 2(XJ)
            J31 21
        98
7W13
                      2+25 REGISTER TO MULTIPLY, OPERAND 2(XJ)
        97
7W13
            J31
                 25
                     2+27 REGISTER TO MULTIPLY, OPERAND 2(XJ)
2+29 REGISTER TO MULTIPLY, OPERAND 2(XJ)
        99
            J32
7W13
                  3
7W13
      901
            J32 21
                      2+28 REGISTER TO MULTIPLY, OPERAND 2(XJ)
7W13
      900
            J32 25
                      2+30 REGISTER TO MULTIPLY, OPERAND 2(XJ)
2+32 REGISTER TO MULTIPLY, OPERAND 2(XJ)
7W13
      902
             J33
                  3
7W13
      904
             J33 21
                      2+31 REGISTER TO MULTIPLY, OPERAND 2(XJ)
7W13
      903
             J33 25
                      2+33 REGISTER TO MULTIPLY, OPERAND 2(XJ)
7W13
            J34
      905
                  3
                      2+35 REGISTER TO MULTIPLY, OPERAND 2(XJ)
      907
             J34 21
7W13
                      2+34 REGISTER TO MULTIPLY, OPERAND 2(XJ)
7W13
      906
            J34 25
      902
            J37
                      2+12 ADD TO REGISTER
7W26
                  2
            J37
7W26
      903
                 26
                      2+13 ADD
                               TO REGISTER
            J38
                               TO REGISTER
7W26
      904
                 2
                      2+14 ADD
7W26
      905
                      2+15 ADD TO REGISTER
            J38 26
                               TO REGISTER
7W26
      906
             J39
                      2+16 ADD
                  2
      907
            J39 26
                      2+17 ADD
                                TO REGISTER
7W26
        90
7W27
            J40
                  2
                      2+18 ADD
                                TO REGISTER
            J40 26
7W27
        91
                      2+19 ADD
                               TO REGISTER
7W27
        92
            J41
                  2
                      2+20 ADD TO REGISTER
        93
7W27
             J41
                      2+21 ADD TO REGISTER
                 26
7W27
        94
            J42
                  2
                      2+22 ADD
                               TO REGISTER
        95
7W27
             J42
                      2+23 ADD TO REGISTER
                 26
7W22
      900
            K27
                      INCREMENT OPERAND 1 BIT 2+0
                  3
                      INCREMENT OPERAND 1 BIT 2+2
            K27 21
7W22
      902
            K27
                      INCREMENT OPERAND 1 BIT 2+1
7W22
      901
                 25
                      INCREMENT OPERAND 1 BIT 2+3
7W22
      903
            K28
                 .3
                      INCREMENT OPERAND 1 BIT 2*5
7W22
      905
            K28
                 21
      904
                      INCREMENT OPERAND 1 BIT 2+4
7W22
            K28 25
                      INCREMENT OPERAND 1 BIT 2+6
      906
            K29
7W22
                 3
            K29 21
                      INCREMENT OPERAND 1 BIT 2+8
7W22
      908
            K29
                      INCREMENT OPERAND 1 BIT 2+7
      907
                 25
7W22
                                          1 BIT 2+9
7W22
        90
            K30
                      INCREMENT OPERAND
            K30 21
                      INCREMENT OPERAND 1 BIT 2+11
        92
7W22
        91
93
7W22
            K30 25
                      INCREMENT OPERAND 1 BIT 2+10
                      INCREMENT OPERAND 1 BIT 2*12
7W22
            K31
                  3
                      INCREMENT OPERAND 1 BIT 2+14
7W22
        95
            K31 21
                      INCREMENT OPERAND 1 BIT 2+13
7W22
        94
            K31
                 25
                      INCREMENT OPERAND 1 BIT 2+15
        96
7W22
            K32
                  3
            K32 21
                      INCREMENT OPERAND 1 BIT 2+17
7W22
        98
                      INCREMENT OPERAND 1 BIT 2+16
        97
7W22
            K32 25
        97
                      INCREMENT TO B
7W16
            K33
                                            1
        98
                      INCREMENT TO
                                            2
7W16
            K33
                  5
7W16
        96
            K33
                      INCREMENT TO B
                                            n
                16
7W16
        95
            K33
                      INCREMENT TO B
                 17
7W17
       906
            K34
                      SHIFT TO B
                                            1
            K34
                      SHIFT TO B
                                            2
7W17
      907
                  5
                      SHIFT TO B
7W17
       905
            K34
                 16
            K34
       904
                                            GO
7W17
                 17
        96
            K37
                      2+24 ADD TO REGISTER
7W27
                  2
        97
7W27
            K37
                 26
                      2+25 ADD TO REGISTER
        98
            K38
                      2*26 ADD
7W27
                               TO REGISTER
                  2
        99
            K38
                26
                                TO REGISTER
7W27
                      2+27 ADD
7W27
      900
            K39
                  2
                      2#28 ADD TO REGISTER
```

```
7W27
       901
                26
                      2+29 ADD TO REGISTER
7W27
      902
            K40
                     2+30 ADD
                                TO
                                   REGISTER
7W27
      903
            K40
                 26
                      2+31
                           ADD
                                TO
                                   REGISTER
7W27
      904
            K41
                      2+32 ADD
                                TO
                                   REGISTER
                  2
      905
                      2+33 ADD TO REGISTER
7W27
            K41 26
7W27
      906
            K42
                     2+34 ADD TO REGISTER
                 2
7W27
      907
            K42
                 26
                      2+35 ADD TO REGISTER
        92
                     D+3 TO X+3 UPPER
7W30
            L26
7W30
        91
                     D+2 TO X+2 UPPER
            L26
                  6
        90
                     D+1 TO X+1 UPPER
7W30
            L26
                  8
            L26 25
7W30
        94
                     D+5 TO X+5 UPPER
        93
                 27
7W30
            L26
                     D+4
                          70
                             X#4 UPPER
                     INCREMENT OPERAND K BIT 2+0 INCREMENT OPERAND K BIT 2+2
            L27
      900
7W23
                  3
            L27
7W23
      902
                 21
            L27
                                          K BIT
                                                 2*1
7W23
                      INCREMENT OPERAND
      901
                 25
                                                 2 * 3
      903
            L28
                      INCREMENT
                                 OPERAND
                 3
      905
            L28
                21
                      INCREMENT
                                 OPERAND
                                                 2+5
7W23
                                 OPERAND
7W23
            L28
                      INCREMENT
      904
                25
7W23
            L29
      906
                  3
                      INCREMENT
                                 OPERAND
            L29
                21
7W23
                      INCREMENT
                                 OPERAND
                                            BIT
                                                 2+8
      908
7W23
      907
            L29
                25
                      INCREMENT
                                 OPERAND
                                          K
                                                 2 * 7
                                                 2+9
                      INCREMENT
                                 OPERAND
7W23
        90
            L30
                 :3
                21
        92
            L30
                      INCREMENT OPERAND
                                                 2+11
7W23
        91
7W23
            L30 25
                      INCREMENT OPERAND
7W23
        93
            L31 3
                      INCREMENT OPERAND
                                                 2*12
                      INCREMENT
                                OPERAND
                                            BIT
7W23
        95
            L31 21
                                                 2+14
7W23
        94
                      INCREMENT
                                 OPERAND
                                            BIT
                                                 2*13
            L31 25
        96
                      INCREMENT OPERAND
                                                 2+15
7W23
            L32
                 3
                     INCREMENT OPERAND
        98
                                          K BIT
                                                 2*17
            L32 21
7W23
7W23
        97
            L32 25
                     INCREMENT OPERAND
                                          K BIT
                     BJ TO INCREMENT
7W19
                                           GO
      900
            L34
                  5
            L34
                  7
                            INCREMENT
7W19
      902
                     BJ TO
                                           1
            L34 10
7W19
      901
                     BJ
                        ŤO
                            INCREMENT
                                           0
            L34 21
                     BK TO
                            INCREMENT
7W19
      904
                                           GO
                            INCREMENT
7W19
      905
            L34 24
                     BK TO
                     BJ TO INCREMENT
      903
            L34 26
7W19
                                           2
        90
            L35
                     2+0 REGISTER TO ADD, OPERAND 2(BJ+XJ)
7W28
                 3
        92
            L35 21
                     2+2
                         REGISTER
                                    TO ADD.
                                             OPERAND
7W28
       91
            L35
                                    TO ADD, OPERAND 2(BJ+XJ)
                     2.1
                          REGISTER
                25
7W28
       93
                          REGISTER TO ADD, OPERAND 2(BJ+XJ)
7W28
            L36
                     2 * 3
                 3
                                       ADD, OPERAND 2(BJ+XJ)
        95
                          REGISTER TO
7W28
            L36 21
                     2+5
            L36
                         REGISTER TO
                                             OPERAND
7W28
        94
                25
                     2+4
                                       ADD,
                                                      2(BJ+XJ)
                                             OPERAND
7W28
        96
            L37
                          REGISTER
                                    TO
                                       ADD.
                  3
                     2 * 6
                                    TO ADD, OPERAND 2(BJ+XJ)
            L37
                          REGISTER
        98
                21
7W28
                     2+8
7W28
        97
            L37
                25
                     2+7 REGISTER TO ADD, OPERAND 2(BJ+XJ)
                     2+9 REGISTER TO ADD, OPERAND 2(BU+XJ)
        99
7W28
            L38
                 . 3
                     2+11 REGISTER TO ADD, OPERAND 2(BJ+XJ)
2+10 REGISTER TO ADD, OPERAND 2(BJ+XJ)
                21
7w28
      901
            L38
                     2-10
7W28
      900
            L38
                 25
            L39
                                              OPERAND 2(BJ+XJ)
7W28
                     2+12
                          REGISTER
                                     TO
                                         ADD.
      902
                  3
                21
                                         ADD.
7W28
      904
            L39
                     2+14
                           REGISTER
                                     ŤO
                                               OPERAND 2(BJ+XJ)
            L39
                     2+13 REGISTER TO
                                         ADD, OPERAND 2(BJ+XJ)
      903
7W28
                25
                                         ADD.
                                              OPERAND 2(BJ+XJ)
7W28
      905
            L40
                  3
                     2+15 REGISTER TO
                                               OPERAND
7W28
      907
                           REGISTER
                                         ADD,
            L40
                 21
                     2+17
                                     TO
                                                        って(BJ+XJ)
                                               OPERAND 2(BJ+XJ)
7W28
                     2+16
                           REGISTER
      906
            L40
                 25
                                     TO
                                         ADD.
                                         ADD.
        90
                     2+18
                                               OPERAND 2(XJ+BJ)
7W29
            L41
                           REGISTER
                                     TO
                  3
                          REGISTER
                                              OPERAND 2(XJ+BJ)
7W29
        92
            L41
                     2+20
                                     ŤO
                                         ADD,
        91
                                              OPERAND 2(XJ+BJ)
7W29
                                         ADD,
            L41 25
                     2+19
                          REGISTER
                                     TO
        93
                                         ADD, OPERAND 2(XJ+BJ)
7W29
            L42
                  3
                     2+21 REGISTER TO
                                              OPERAND 2(XJ+BJ)
7W29
        95
            L42 21
                     2+23 REGISTER
                                     TO
                                         ADD.
        94
                                               OPERAND 2(XJ+BJ)
7W29
            L42
                     2+22 REGISTER TO ADD,
                 25
7W19
      906
            M34
                  5
                     BK TO INCREMENT
       93
            M34
                                           GO
                     B TO ADD (1)
7W18
```

```
M34 10
7W19
       907
                      BK TO INCREMENT
                                            2
             M34 21
7W18
        95
                      B TO ADD (1)
                                            1
7W18
        96
             M34
                 24
                      В
                        TO ADD
                                (1)
                                            2
7W18
        94
            M34
                      B TO ADD (1)
                 26
                                            0
7W29
        96
             M35
                      2+24 REGISTER TO ADD, OPERAND 2(XJ+BJ)
                  3
                      2+26 REGISTER TO ADD, OPERAND 2(XJ+BJ)
        98
7W29
             M35
                 21
                                         ADD, OPERAND 2(XJ+BJ)
7W29
        97
            M35
                      2+25 REGISTER TO
                 25
                                          ADD, OPERAND 2(XJ+BJ)
7W29
        99
             M36
                      2#27 REGISTER TO
                  3
                                          ADD, OPERAND 2(XJ+BJ)
       901
                      2+29 REGISTER TO
7W29
             M36
                 21
                                          ADD, OPERAND 2(XJ+BJ)
7W29
       900
             M36
                 25
                      2+28 REGISTER TO
                      2+30 REGISTER TO ADD, OPERAND 2(XJ+BJ)
7W29
       902
             M37
                  3
                      2+32 REGISTER TO ADD, OPERAND 2(XJ+BJ)
            M37
7W29
       904
                 21
       903
             M37
                      2+31 REGISTER TO
                                          ADD, OPERAND 2(XJ+BJ)
7W29
                 25
                                          ADD, OPERAND 2(XJ+BJ)
                      2+33 REGISTER TO ADD, OPERAND 2(XJ+BJ)
2+35 REGISTER TO ADD, OPERAND 2(XJ+BJ)
7W29
       905
             M38
                  3
       907
             M38
                 21
7W29
                      2+34 REGISTER TO ADD, OPERAND 2(XJ+BJ)
2+0 REGISTER TO ADD, OPERAND 1(XK)
       906
7W29
             M38
                 25
             M39
7W24
                  3
        92
            M39
                      2+2 REGISTER TO ADD, OPERAND 1(XK)
7W24
                 21
                      2+1 REGISTER TO ADD, OPERAND 1(XK)
2+3 REGISTER TO ADD, OPERAND 1(XK)
             M39
7W24
        91
                 25
        93
7W24
             M40
                  3
                                     TO ADD, OPERAND 1(XK)
                 21
        95
7W24
             M40
                      2+5 REGISTER
                                        ADD, OPERAND 1(XK)
                      2+4 REGISTER
                                    TO
7W24
        94
             M40
                 25
        96
                      2+6 REGISTER TO ADD, OPERAND
            M41
7W24
                                                       1(XK)
                  3
                                        ADD, OPERAND 1(XK)
7W24
        98
             M41
                 21
                      2+8 REGISTER
                                     TO
                      2+7 REGISTER TO ADD, OPERAND 1(XK)
        97
7W24
            M41
                 25
7W24
        99
                      2+9 REGISTER TO ADD, OPERAND 1(XK)
             M42
                  3
7W24
       901
             M42
                 21
                      2+11 REGISTER TO ADD, OPERAND 1(XK)
                      2+10 REGISTER TO ADD, OPERAND 1(XK)
7W24
       900
             M42
                 25
                      2+12 REGISTER TO ADD, OPERAND 1(XK)
2+14 REGISTER TO ADD, OPERAND 1(XK)
             N35
7W24
       902
                  3
            N35
       904
                 21
7W24
            N35
                      2+13 REGISTER TO
                                         ADD, OPERAND 1(XK)
7W24
       903
                 25
                                         ADD, OPERAND 1(XK)
7W24
       905
             N36
                  3
                      2+15
                           REGISTER TO
       907
                      2+17 REGISTER TO ADD, OPERAND 1(XK)
7W24
             N36
                21
                      2+16 REGISTER TO ADD, OPERAND 1(XK)
7W24
       906
            N36
                25
                                          ADD, OPERAND
7W25
        90
             N37
                      2+18 REGISTER TO
                  3
                                         ADD, OPERAND 1(XK)
        92
            N37
7W25
                 21
                      2+20 REGISTER TO
        91
            N37
                                         ADD, OPERAND 1(XK)
7W25
                 25
                      2+19 REGISTER TO
                                          ADD, OPERAND 1 (XK)
7W25
        93
            N38
                      2*21 REGISTER TO
                  3
                 21
                                         ADD, OPERAND 1(XK)
7W25
        95
            N38
                      2+23 REGISTER TO
                                          ADD, OPERAND 1(XK)
7W25
        94
            N38
                 25
                      2+22 REGISTER TO
                                          ADD, OPERAND 1(XK)
        96
                           REGISTER TO
            N39
7W25
                  3
                      2+24
        98
            N39
                      2+26 REGISTER TO
                                         ADD, OPERAND 1(XK)
7W25
                 21
                      2+25 REGISTER TO ADD, OPERAND 1(XK)
        97
7W25
             N39
                 25
        99
                      2*27 REGISTER TO
2*29 REGISTER TO
                                          ADD, OPERAND
                                                        1(XK)
7W25
             N40
                  3
                                          ADD, OPERAND
7W25
       901
             N40
                 21
                                                        1 (XK)
                                          ADD, OPERAND 1(XK)
       900
             N40
                      2+28 REGISTER TO
7W25
                 25
                                          ADD, OPERAND 1(XK)
7W25
       902
             N41
                  3
                      2+30 REGISTER TO
                                         ADD, OPERAND 1(XK)
7W25
                      2+32 REGISTER TO
       904
                21
             N41
                                         ADD, OPERAND 1(XK)
7W25
       903
            N41
                 25
                      2+31 REGISTER TO
                      2+33 REGISTER TO
                                         ADD, OPERAND 1(XK)
7W25
       905
            N42
                  3
                 21
       907
                      2+35 REGISTER TO ADD, OPERAND 1(XK)
7W25
             N42
                                               OPERAND 1(XK)
7W25
       906
             N42
                 25
                      2+34 REGISTER TO ADD,
        90
                      2+0 SHIFT TO REGISTER
7W31
             038
                  2
        91
                      2+1 SHIFT TO REGISTER
7W31
             038
                 26
                      2+2 SHIFT TO REGISTER
7W31
        92
             039
                  2
            039
7W31
        93
                      2+3 SHIFT TO
                                     REGISTER
                 26
7W31
        94
             040
                      2+4 SHIFT TO
                                     REGISTER
        95
                      2+5 SHIFT TO
7W31
             040
                                     REGISTER
                 26
                      2+6 SHIFT
                                 TO
7W31
        96
             041
                  2
                                     REGISTER
                          SHİFT
        97
                                 ŤO
7W31
             041
                 26
                      2+7
                                     REGISTER
        98
                      2#8 SHIFT TO
                                     REGISTER
7W31
            042
                  2
```

2+9 SHIFT TO REGISTER

7W31

99

042 26

```
7W31 900 P38 2 2*10 SHIFT TO REGISTER
7W31 901 P38 26 2*11 SHIFT TO REGISTER
7W31 902 P39 2 2*12 SHIFT TO REGISTER
7W31 903 P39 26 2*13 SHIFT TO REGISTER
7W31 904 P40 2 2*14 SHIFT TO REGISTER
7W31 905 P40 26 2*15 SHIFT TO REGISTER
7W31 906 P41 2 2*16 SHIFT TO REGISTER
7W31 907 P41 26 2*17 SHIFT TO REGISTER
```

## CHASSIS 8

```
8W05
      900
            A01
                 2
                     2+36 MEMORY TO REGISTER
                     2+37 MEMORY TO REGISTER
8W05
      901
            A01 26
8W05
       902
            A02
                     2+38
                          MEMORY
                                  TO REGISTER
                  2
                                  TO REGISTER
8W05
      903
            A02 26
                     2+39
                          MEMORY
                     2+40
                          MEMORY
8W05
      904
            A 0 3
                                  TO REGISTER
                 2
       905
                          MEMORY
                                     REGISTER
8W05
            A03 26
                     2+41
                                  ŤO
                     2+42
                          MEMORY
8W05
      906
            A 0 4
                                  TO REGISTER
                  2
8W05
      907
            A 0 4
                26
                     2+43
                          MEMORY
                                  TO REGISTER
                          MEMORY TO REGISTER
            A 0 5
8W05
      908
                     2+44
                  2
        90
            A 05
                 26
                     2+45 MEMORY REGISTER
8W06
        91
8W06
            A06
                  2
                     2+46 MEMORY REGISTER
        92
            A06 26
                     2+47
                          MEMORY REGISTER
8W06
        93
                          MEMORY
8W06
            A07
                  2
                     2+48
                                  REGISTER
        94
                          MEMORY REGISTER
            A07 26
                     2+49
8W06
8W06
        95
            A08
                  2
                     2+50 MEMORY REGISTER
       96
            A08 26
                     2+51 MEMORY REGISTER
8W06
8W10
      906
            A 0 9
                     2+36 REGISTER TO DISTRIBUTOR
                  3
      907
            A 0 9
                21
                     2+37 REGISTER TO DISTRIBUTOR
8W10
                     2+38 REGISTER TO DISTRIBUTOR
       90
            A 0 9
8W10
                 25
8W10
        91
                     2+39 REGISTER TO DISTRIBUTOR
            A10
                  3
                     2+40 REGISTER TO DISTRIBUTOR
8W10
        92
            A10
                21
8W10
        93
            A10 25
                     2+41 REGISTER TO DISTRIBUTOR
        94
                     2+42 REGISTER TO DISTRIBUTOR
8W10
            A11
                 3
                     2+43 REGISTER TO DISTRIBUTOR
        95
8W10
            A11
                21
        96
            A11 25
                     2+44 REGISTER TO DISTRIBUTOR
8W10
                     2+45 REGISTER TO DISTRIBUTOR
8W11
      900
            A12
                 3
                     2+46 REGISTER TO DISTRIBUTOR
      901
            A12 21
8W11
8W11
      902
            A12 25
                     2+47 REGISTER TO DISTRIBUTOR
                     2+52 MEMORY REGISTER
        97
8W06
            B01
                  5
8W06
        98
            B01 26
                     2+53 MEMORY REGISTER
        99
                     2+54 MEMORY REGISTER
8W06
            B02
                     2+55 MEMORY REGISTER
8W06
      900
            B02 26
                     2+56 MEMORY REGISTER
8W06
      901
            B03
                  2
                     2+57 MEMORY REGISTER
8W06
      902
            B03 26
                     2+58 MEMORY REGISTER
            B04
8W06
      903
                  2
            B04 26
                          MEMORY REGISTER
8W06
       904
                     2*59
       90
            B05
                     2+36 MULTIPLY TO REGISTER
8W08
                  2
                26
8 w n 8
        91
            B05
                     2+37 MULTIPLY TO REGISTER
        92
                     2+38 MULTIPLY TO REGISTER
8 W O 8
            B06
                  2
                     2+39 MULTIPLY TO REGISTER
        93
8W08
            B06 26
                     2+40 MULTIPLY TO REGISTER
8 W 0 8
        94
            B07
                  2
        95
                     2+41 MULTIPLY TO REGISTER
            B07
8008
                26
                     2+42 MULTIPLY TO REGISTER
8008
        96
            808
                  2
                     2+43 MULTIPLY TO REGISTER
8W08
        97
            808
                26
8W11
      903
            B 0 9
                     2+48 REGISTER TO DISTRIBUTOR
                  3
8W11
      904
            B09 21
                     2+49 REGISTER TO DISTRIBUTOR
                     2+50 REGISTER TO DISTRIBUTOR
      905
            B09 25
8W11
8W11
            B10
                     2+51 REGISTER TO DISTRIBUTOR
      906
                  3
                     2+52 REGISTER TO DISTRIBUTOR
2+53 REGISTER TO DISTRIBUTOR
8W11
      907
            B10 21
8W11
       90
            B10
                25
        91
8W11
            811
                     2+54 REGISTER TO DISTRIBUTOR
                  3
                     2+55 REGISTER TO DISTRIBUTOR
8W11
        92
            B11 21
        93
                     2+56 REGISTER TO DISTRIBUTOR
8W11
            B11 25
8W11
        94
            812
                 -3
                     2+57 REGISTER TO DISTRIBUTOR
                     2+58 REGISTER TO DISTRIBUTOR
        95
                21
8W11
            B12
8W11
        96
            B12 25
                     2+59 REGISTER TO DISTRIBUTOR
        98
                     2+44 MULTIPLY TO REGISTER
8W08
            C01
                2
        99
8W08
            C01 26
                     2+45 MULTIPLY TO REGISTER
8 W D 8
      900
            C02
                2
                     2+46 MULTIPLY TO REGISTER
                     2+47 MULTIPLY TO REGISTER
8 W D 8
      901
            C02 26
```

```
8W18
      902
            C03
                2
                     2+48 EXPONENT RESULT-REG.
                     2+49 EXPONENT RESULT-REG.
2+50 EXPONENT RESULT-REG.
2+51 EXPONENT RESULT-REG.
      903
8W18
            C03 26
8W18
       904
            C04
                  2
            C04 26
      905
8W18
8W18
                     2+52 EXPONENT RESULT-REG.
      906
            C05
                 2
            C05 26
                     2+53 EXPONENT RESULT-REG.
8W18
      907
8W19
      902
            COS
                 2
                     2+54 EXPONENT RESULT-REG
                     2+55 EXPONENT
8W19
      903
            C06
                                    RESULT-REG
                26
            007
8W19
      904
                     2+56 EXPONENT RESULT-REG
                  2
            007 26
                     2+57 EXPONENT RESULT-REG
8W19
      905
                     2+58 EXPONENT RESULT-REG
8W19
      906
            C08
                  2
            C08 26
                          EXPONENT RESULT-REG
8W19
      907
                     2+59
                                     TO MULTIPLY, OPERAND 1(XK)
                          REGISTER
8W07
       90
            C09
                  3
                     2+36
8W07
       91
                21
            C09
                     2+37 REGISTER
                                        MULTIPLY, OPERAND 1(XK)
                                     ŤO
                                        MULTIPLY, OPERAND 1(XK)
            C09 25
8W07
       92
                     2+38 REGISTER
                                    ŤO
            C10
                                        MULTIPLY, OPERAND 1(XK)
8W07
       93
                     2+39 REGISTER TO
                  3
        94
            C10 21
                     2+40
                          REGISTER
                                    Ť0
                                        MULTIPLY, OPERAND
                                                            1(XK)
8W07
                                        MULTIPLY, OPERAND 1(XK)
8W07
       95
                     2+41 REGISTER
            C10 25
8W07
                     2+42 REGISTER TO
                                        MULTIPLY, OPERAND
                                                            1(XK)
        96
            C11
                  3
                                        MULTIPLY, OPERAND 1(XK)
       97
8W07
            C11 21
                     2+43 REGISTER TO
8W07
       98
            C11 25
                     2+44 REGISTER TO
                                        MULTIPLY, OPERAND 1(XK)
       99
                     2+45 REGISTER TO
                                        MULTIPLY, OPERAND
                                                            1(XK)
8WD7
            C12
                 3
                     2+46 REGISTER TO MULTIPLY, OPERAND 1(XK)
8W07
      900
            C12: 21
                     2+47 REGISTER TO MULTIPLY, OPERAND 1(XK)
      901
8W07
            C12 25
8W18
       90
            009
                     2+48 X++K (EXPONENT)
                 3
       91
                          X++K (EXPONENT)
            D09 21
8W18
                     2+49
                     2+50
        92
            D09
                          X++K (EXPONENT)
8W18
                25
       93
                     2+51 X++K (EXPONENT)
8W18
            D10
                 3
8W18
            D10 21
                     2+52 X++K (EXPONENT)
        94
8W18
       95
            D10 25
                     2+53 X++K (EXPONENT)
                     2+54 X++K
       96
                                (EXPONENT)
8W18
            D11
                 3
8W18
       97
                     2+55 X++K
                                (EXPONENT)
            D11 21
       98
                     2+56 X++K (EXPONENT)
8W18
            D11 25
       99
                     2+57 X++K (EXPONENT)
8W18
            D12
                 3
            D12 21
8W18
      900
                     2+58 X++K (EXPONENT)
                     2+59 X++K (EXPONENT)
            D12 23
8W18
      901
            D12 25
                     2+59 REGISTER TO MULTIPLY, OPERAND 1(XK)
8W07
      902
8W21
      905
            E01
                     X TO ADD
                 - 5
                                     n
                  7
                     X TO ADD
8W21
      906
            EO1
                                     1
8W21
      907
            E01 10
                     X TO ADD
                                     2
            E01 21
                     X TO ADD
                                     GO
8W21
       904
8W22
       91
            E01 24
                     EXCHANGE
       90
8W13
                     INCR SIGN
            E06
                56
                     2+36 REGISTER TO MULTIPLY, OPERAND (XJ)
       90
            E09
BW09
                  3
8W09
        91
            E09 21
                     2+37 REGISTER TO
                                        MULTIPLY, OPERAND (XJ)
                     2+38 REGISTER
                                        MULTIPLY, OPERAND (XJ)
       92
8W09
            E09 25
                                     ŤO
                                        MULTIPLY, OPERAND (XJ)
        93
                     2+39 REGISTER
8W09
            E10
                  .3
                                     TO
                                        MULTIPLY, OPERAND
8W09
        94
            E10 21
                     2+40 REGISTER
                                     TO
                                                            (LX)
                                        MULTIPLY, OPERAND (XJ)
        95
                     2+41 REGISTER
8W09
            E10
                25
                                     TO
8W09
        96
                     2+42 REGISTER
                                     Ť٥
                                        MULTIPLY, OPERAND (XJ)
            E11
                  3
                                        MULTIPLY, OPERAND (XJ)
        97
                     2+43 REGISTER
                                     ŤΟ
8W09
            E11 21
                     2+44 REGISTER TO
                                        MULTIPLY, OPERAND (XJ)
       98
8W09
            E11 25
                                        MULTIPLY, OPERAND MULTIPLY, OPERAND
                     2+45 REGISTER
                                     TO
        99
            E12
                                                            (LX)
8W09
                  3
                                                            (XJ)
8W09
       900
            E12 21
                     2+46 REGISTER
                                     TO
                     2+47 REGISTER TO MULTIPLY, OPERAND (XJ)
8W09
      901
            E12 25
                     X TO ADD
8W21
       90
            F01
                 5
                                     0
       91
8W21
            F01:
                     X TO ADD
                                     1
        92
            F01 10
                     X TO ADD
8W21
                                     2
       93
            F01 21
8W21
                       TO ADD
                                     90
      901
            F01 24
                     TRANSMIT LONG
8W12
                                     ADD
       95
            F02
                     K TO MULTIPLY O
8W20
                  5
        96
            F02
                     K TO MULTIPLY 1
8W20
```

```
97
             F02 10
                      K TO MULTIPLY 2
8W20
8W20
        94
             F02
                      K TO MULTIPLY GO
                 21
        90
             F09
8W19
                      2+48 X++J (EXPONENT)
                   3
                 21
             F09
8W19
        91
                      2+49 X++J (EXPONENT)
                      2+50 X++J (EXPONENT)
             F09 25
8W19
        92
8W19
        93
             F10
                      2+51 X++J
                                  (EXPONENT)
                  3
                 21
             F10
                      2+52 X++J
8W19
        94
                                  (EXPONENT)
             F10
        95
                                  (EXPONENT)
                      2+53 X++J
8W19
                 25
        96
             F11
                      2+54 X++J
8W19
                  3
                                 (EXPONENT)
        97
             F11 21
                      2+55 X++J (EXPONENT)
8W19
            F11 25
8W19
        98
                      2+56 X++J (EXPONENT)
8W19
        99
             F12
                      2+57 X++J (EXPONENT)
                  3
                      2+58 X++J (EXPONENT)
2+59 X++J (EXPONENT)
8W19
       900
             F12
                 21
       901
             F12 23
8W19
                      2+59 REGISTER TO MULTIPLY, OPERAND (XJ)
8W09
       902
             F12 25
                      J TO MULTIPLY O
8W21
             G01
       901
                  57
8W21
       902
             G01
                      J TO MULTIPLY 2
8W21
       903
             G01 10
8W21
                      J TO MULTIPLY GO
             G01 21
       900
             G01 24
8W12
       902
                      TRANSMIT SHIFT
        92
                      ADD MINUS
8W13
             G01 26
8W22
       905
                      X TO MEMORY
             002
                  5
                                       0
8W22
       906
             G02
                  7
                      X TO MEMORY
                                       1
                      X TO MEMORY
8W22
       907
             G02 10
                                       2
8W22
             G02 21
                      X TO MEMORY
       904
                                       g0
                      TRANSMIT ADD
             G02 24
8W12
       903
        92
             G 0 5
8W20
                      MEMORY TO D
                      MEMORY TO D
MEMORY TO D
MEMORY TO D
        91
             G 0 5
8W20
                  5
                                       0
             G05 16
        93
BW20
                                       2
8W20
        90
             G05 17
                                       GO
                      MULTIPLY TO X
8W20
       903
             G06
                                       1
                      MULTIPLY TO X 0
             G06
                   5
8W20
       902
8W20
       904
             G06 16
                      MULTIPLY TO X 2
                      MULTIPLY TO X GO
8W20
       901
             G06 17
8W20
       907
             G 0 7
                      ADD TO X
                  4
                                       1
                      ADD TO X
8W20
             007
       906
                  5
                                       0
             G07 16
        98
8W20
                      ADD TO X
                                       2
       905
             G07
                      ADD TO X
                                       GO
8W20
                 17
                      INCR TO X
INCR TO X
8W21
        96
             G 08
                                   1
8W21
        95
             G08
                  5
                                   0
        97
             G08 16
8W21
                      INCR TO X
        94
                      INCR TO X
8W21
             G08 17
8W23
        90
                      D+1 TO X+1 UPPER
             G11
                  5
                      D+2 TO X+2 UPPER
D+3 TO X+3 UPPER
8W23
        91
             G11
                  7
        92
             G11 10
8W23
8W23
        93
             G11 21
                      D+4 TO X+4 UPPER
                      D+5 TO X+5 UPPER
8W23
        94
             G11 24
        95
             G16
                  5
                      PACK
8W12
                      NORM. (SHIFT)
UN-PACK
8W12
       908
             G16
        96
8W12
             G16 10
             G16 21
        97
                      SHIFT (JK)
8W12
                      SHIFT LEFT
        99
8W12
             G16 24
        98
                      SHIFT NOMINAL
8W12
             G16 26
8W12
        90
             H01
                      LONG ADD IN
                      ROUND SHIFT
8W12
       900
             H01
        91
             H01 10
                      ADD IN
8W12
        93
                      ROUND ADD
8W12
             H01 21
        94
8W12
             H01 24
                      ADD
8W12
       907
             H01 26
                      SHIFT IN
        92
            H () 2
                      ENTER (JK) 2+0
8W22
                  - 4
8W22
        93
             H02
                      ENTER (JK) 2+1
                  6
8W22
        94
             H02
                      ENTER (JK) 2+2
```

```
8W22
            H02 22
                     ENTER (JK) 2+3
8W22
        96
            H02 25
                     ENTER (JK) 2+4
        97
                     ENTER (JK) 2+5
8W22
            H02 27
                     2+0 SHIFT TO REGISTER
8W30
        90
            H03
8W30
        91
            H03
                     2+1 SHIFT
                                TO
                                   REGISTER
        92
            H03 25
                     2+2 SHIFT
8W30
                                TO REGISTER
        93
                     2+3 SHIFT TO REGISTER
8W30
            H03
                27
8W30
        94
            H04
                     2+4
                         SHIFT
                                TO REGISTER
                 1
            H04
                     2+5 SHIFT 2+6 SHIFT
8W30
        95
                                TO REGISTER
                 5
8W30
       96
            H04 25
                                TO REGISTER
       97
                         SHÎFT
8W30
                     2+7
                                TO REGISTER
            H04
                27
       98
            H05
                     2+8 SHIFT TO REGISTER
8W30
                     2+9 SHIFT TO REGISTER
       99
            H05
8W30
                     REQUEST RELEASE SHIFT
8W12
      904
            H06
                     REQUEST RELEASE ADD
REQUEST RELEASE LONG ADD
8W22
       98
            H06
                 6
8W12
      906
            H06
                 8
                     LONG ADD INFINITE
8W13
            H06 10
      901
                     LONG ADD INDEFINITE
8W13
      900
            H06 12
                     ERROR (EXP = 1777 FROM CHASSIS 7)
8W18
      908
            H06 19
            H06 21
                     ERROR (EXP = 3777 FROM CHASSIS 8)
8W19
      908
8W21
       98
            H06 23
                     LONG ADD ZERO
                                    CH 5
8W21
       99
            H06 25
                     LONG ADD SIGN
                                     CH 5
8W22
       99
                     LONG ADD SER
                                    CONTROL
            H06 27
                     2+10 SHIFT TO
8W30
      900
            H17
                                    REGISTER
                 1
            H17
                     2+11 SHIFT TO
8W30
      901
                 5
                                    REGISTER
                     2+12 SHIFT TO
2+13 SHIFT TO
            H17 25
8W30
      902
                                    REGISTER
            H17
                27
8W30
      903
                                    REGISTER
8W30
                     2+14 SHIFT TO
            H18
      904
                                    REGISTER
                 1
                     2+15 SHIFT TO REGISTER
8W30
      905
            H18
                     2+16 SHIFT TO REGISTER
            H18 25
8W30
      906
                     2+17 SHIFT TO REGISTER
2+17 REGISTER TO ADD, OPERAND 2(BJ+XJ)
            H18 27
8W30
      907
            117 23
8W28
      907
            119
8W12
      905
                     LONG ADD - PLUS
8W12
       92
            119
                     MASK
            121 12
                     CLOCK
8W20
      900
                     2+0 REGISTER TO ADD, OPERAND 1(XK)
8W26
       90
            J01 11
                     2+1 REGISTER TO ADD. OPERAND 1(XK)
            J01: 13
8W26
       91
                                   TO ADD, OPERAND 1(XK)
8W26
                     2+2 REGISTER
        92
            J02 11
       93
                     2+3 REGISTER TO ADD, OPERAND 1(XK)
8W26
            J02 13
8W26
       94
            J03 11
                     2+4 REGISTER TO ADD, OPERAND 1(XK)
            J03 13
        95
                     2+5
                         REGISTER
                                   TO ADD, OPERAND 1(XK)
8W26
       96
                                   TO ADD, OPERAND 2(BJ+XJ)
                     2+6 REGISTER
8W28
            J04
                                   TO ADD, O-E-AND 100K)
            J04
        96
                     2+6 REGISTER
8W26
                  8
                                   TO ADD, OPERAND 2(BJ+XJ)
       97
            J04 21
                     2+7 REGISTER
8W28
                     2+7 REGISTER TO ADD, OPERAND 1/XK)
       97
            J04 24
8W26
                                   TO ADD, OPERAND 2(BJ+XJ)
       98
            J05
                     2+8 REGISTER
8W28
                                            OPERAND 1(XK)
       98
                                   TO ADD,
8W26
            J05
                     2+8 REGISTER
                 8
       99
            J05 21
8W28
                     2+9 REGISTER TO ADD, OPERAND 2(BJ+XJ)
                     2+9 REGISTER TO ADD, OPERAND 1(XK)
8W26
       99
            J05 24
            J06
                     2+10 REGISTER TO ADD, OPERAND 2(BJ+XJ)
8W28
      900
                     2+10 REGISTER TO ADD, OPERAND 1(XK)
8W26
      900
            106
                 8
                                        ADD, OPERAND 2(BJ+XJ)
                     2+11 REGISTER
8W28
      901
            J06 21
                                    TO
                                        ADD, OPERAND 1(XK)
8W26
                     2+11 REGISTER TO
      901
            J06 24
                                        ADD, OPERAND 1(XK)
            J07
                                    ŤQ
8W26
      902
                     2+12 REGISTER
                 6
                                        ADD, OPERAND 2(BJ+XJ)
            J07
      902
                     2+12 REGISTER
8W28
                                    ŤO
                                        ADD, OPERAND
            J07 21
                     2+13 REGISTER
8W28
      903
                                     TO
                                                      2(BJ+XJ)
            J07
                                        ADD, OPERAND 1(XK)
8W26
      903
                24
                     2+13 REGISTER
                                    TO
                                        ADD, OPERAND 1(XK)
            JOB
                     2+14
                          REGISTER
8W26
      904
                                    ŤO
                                        ADD, OPERAND 2(BJ+XJ)
      904
            J08
                     2*14 REGISTER TO
8W28
      905
                     2+15 REGISTER TO ADD, OPERAND 2(BJ+XJ)
8W28
            J08 21
                     2+15 REGISTER TO ADD, OPERAND 1(XK)
8W26
      905
            J08 24
8W26
      906
            J09
                     2+16 REGISTER TO ADD, OPERAND 1(XK)
                  6
```

```
8W28
      906
            J09
                     2+16 REGISTER TO ADD, OPERAND 2(BJ+XJ)
                     2+17 REGISTER TO ADD, OPERAND 1(XK)
      907
8W26
            J09
       90
                     2+18 REGISTER TO ADD, OPERAND 1(XK)
8W27
            J10
                     2+18 REGISTER TO ADD, OPERAND 2(XJ+BJ)
8W29
        90
            J10
                     2+19 REGISTER TO ADD, OPERAND 2(XJ+BJ)
2+19 REGISTER TO ADD, OPERAND 1(XK)
8W29
        91
            J10
                 21
       91
8W27
            J10
                 24
                                        ADD, OPERAND 1(XK)
8W27
        92
                     2+20 REGISTER TO
            J11
                     2+20 REGISTER TO ADD, OPERAND 2(XJ+BJ)
8W29
        92
            J11
                     2+21 REGISTER TO ADD, OPERAND 2(XJ+BJ)
        93
            J11 21
8W29
                     2+21 REGISTER TO ADD, OPERAND 1(XK)
2+22 REGISTER TO ADD, OPERAND 1(XK)
2+22 REGISTER TO ADD, OPERAND 2(XJ+BJ)
        93
8W27
            J11 24
8W27
        94
            K01
                  6
8W29
        94
            K01
                     2+23 REGISTER TO ADD, OPERAND 2(XJ+BJ)
8W29
        95
            K01 21
                     2+23 REGISTER TO ADD, OPERAND 1(XK)
        95
8W27
            K01
                 24
                     2+24 REGISTER TO ADD, OPERAND 1(XK)
8W27
        96
            K02
                  6
                           REGISTER TO ADD, OPERAND 2(XJ+BJ)
8W29
        96
            K02
                     2 * 24
                                        ADD, OPERAND 2(XJ+BJ)
                     2+25 REGISTER TO
2+25 REGISTER TO
        97
            K02 21
8W29
                                        ADD, OPERAND 1(XK)
        97
            K02
8W27
                     2+26 REGISTER TO ADD, OPERAND 1(XK)
        98
8W27
            K03
8W29
        98
                     2+26 REGISTER TO
                                         ADD, OPERAND 2(XJ+BJ)
            K03
                  7
                                        ADD, OPERAND 2(XJ+BJ)
8W29
        99
            K03
                 21
                     2+27 REGISTER TO
                     2+27 REGISTER TO ADD, OPERAND 1(XK)
        99
            K03
8W27
                 24
                     2+28 REGISTER TO ADD, OPERAND 1(XK)
8W27
       900
            K04
                  6
                     2+28 REGISTER TO ADD, OPERAND 2(XJ+BJ)
8W29
      900
            K04
            K04 21
                     2+29 REGISTER TO ADD, OPERAND 2(XJ+BJ)
8W29
      901
                                         ADD, OPERAND 1(XK)
            KO4
                     2+29 REGISTER TO
8W27
      901
                 24
                     2+30 REGISTER TO ADD, OPERAND 1(XK)
            K05
8W27
      902
                                        ADD, OPERAND 2(XJ+BJ)
8W29
      902
            K05
                     2+30 REGISTER TO
                     2+31 REGISTER TO ADD, OPERAND 2(XJ+BJ)
8W29
      903
            K05 21
                     2+31 REGISTER TO
      903
            K05 24
                                        ADD, OPERAND 1(XK)
8W27
                                         ADD, OPERAND 1(XK)
            K06
                     2+32 REGISTER
                                    TO
8W27
      904
                  6
                                         ADD, OPERAND 2(XJ+BJ)
                     2+32 REGISTER TO
8W29
      904
            K06
                                              OPERAND 2(XJ+BJ)
            K06 21
                     2+33 REGISTER TO
                                        ADD.
8W29
      905
                     2+33 REGISTER TO ADD, OPERAND 1(XK)
      905
            K06 24
8W27
                                         ADD, OPERAND 1(XK)
            K07
                     2+34 REGISTER TO
8W27
      906
                                        ADD, OPERAND 2(XJ+BJ)
            KO7
                     2+34 REGISTER TO
8W29
      906
                     2+35 REGISTER TO ADD, OPERAND 2(XJ+BJ)
8W29
      907
            K07 21
                     2+35 REGISTER TO ADD, OPERAND 1(XK)
      907
            K07 24
8W27
                     2+0 REGISTER TO ADD, OPERAND 2(BJ+XJ)
        90
            L09 14
8W28
                     2+1 REGISTER TO ADD, OPERAND 2(BJ+XJ)
            L09 18
8W28
        91
                     2+2 REGISTER TO ADD, OPERAND 2(BJ+XJ)
8W28
        92
            L10
                14
                     2+3 REGISTER TO ADD, OPERAND 2(BJ+XJ)
        93
            L10
8W28
                18
                     2+4 REGISTER TO ADD, OPERAND 2(BJ+XJ)
8W28
            L11
                 14
                     2+5 REGISTER TO ADD, OPERAND 2(BJ+XJ)
        95
8W28
            L11 18
8W24
        90
            MO1
                     2+0 ADD TO REGISTER
                  1
                     2+1 ADD TO REGISTER
8W24
        91
            M01
                  6
        92
                 23
                     2+2 ADD TO REGISTER
8W24
            MO1
8W24
        93
            MO1
                 28
                     2+3 ADD TO REGISTER
                     2+4 ADD TO REGISTER
8W24
        94
            M02
                  1
        95
                     2+5 ADD
                              TO REGISTER
8W24
            M02
                              TO REGISTER
        96
            M02
                     2+6 ADD
8W24
                 23
                              TO REGISTER
8W24
        97
            M02
                 28
                     2+7 ADD
                     2+8 ADD TO REGISTER
        98
            M03
8W24
                  1
        99
8W24
            M03
                     2+9 ADD TO REGISTER
                     2+10 ADD TO REGISTER
                23
8W24
       900
            M03
8W24
       901
            MO3
                 28
                     2+11 ADD
                               TO
                                   REGISTER
            M 0 4
                               TO
                     2+12 ADD
                                   REGISTER
8W24
      902
8W24
      903
            M 0 4
                     2+13 ADD
                               TO REGISTER
                  6
      904
            M04
                 23
                     2+14 ADD
                               TO
8W24
      905
            M04
                     2+15 ADD
                               TO
                 28
                                   REGISTER
8W24
            Mn5
8W24
       906
                  1
                     2+16 ADD
                               TO
                                   REGISTER
8W24
       907
            M05
                     2+17 ADD TO REGISTER
```

```
2+18 ADD TO REGISTER
8W25
        90
            M05 23
                     2+19 ADD
2+20 ADD
                                   REGISTER
8W25
            M05 28
                               TO
        92
                               TO REGISTER
8W25
            NO1
8W25
        93
            N01
                     2-21 ADD
                               TO
                                  REGISTER
                  6
8W25
                               TO
                                  REGISTER
        94
            NO1 23
                     2+22 ADD
        95
                     2+23 ADD
                               TO REGISTER
            NO1 28
        96
            N02
8W25
                     2+24 ADD
                               TO
                                  REGISTER
            NO2 23
        97
                                   REGISTER
8W25
                     2+25 ADD
                               TO
8W25
        98
                     2+26 ADD
                               TO REGISTER
            NO2 28
NO3 1
                               TO
                                  REGISTER
8W25
        99
                     2+27 ADD
                     2+28 ADD
2+29 ADD
                               TO REGISTER
8W25
      900
                 1 6
8W25
      901
            NO3
                               TO
8W25
            NO3 23
                     2+30 ADD
                                   REGISTER
      902
      903
            NO3 28
                     2+31 ADD
                               TO
                                  REGISTER
8W25
                     2+32 ADD
                               TO REGISTER
      904
            N04
                 1
            N04
                     2+33 ADD TO REGISTER
      905
8W25
      906
            NO4 23
                     2+34 ADD
                               TO REGISTER
      907
                     2+35 ADD TO REGISTER
            N04 28
```

## CHASSIS 9

```
97
                     MEMORY MARGIN
9W11
            C14 14
            G35 12
                     CLOCK
9W20
       900
                     2+3 CHASSIS 9 SELECT (010)
2+2 CHASSIS 9 SELECT (010)
            G38
9W20
       904
9W20
       903
            G38
                     2+4 CHASSIS 9 SELECT (010)
9W20
       905
            G38 14
            G38 20
        98
                     WRIT
9W14
        97
                     GO
9W14
            G38 21
            G39 18
9W20
       901
                     2+0 BANK SELECT
9W20
       902
            G39
                22
                     2+1 BANK SELECT
        99
            G39 28
                      ACCEPT
9W07
            G40 11
                     2+5 ADDRESS TO MEMORY
9W20
       906
9W20
       907
            G40 14
                     2+6 ADDRESS TO MEMORY
9W20
            G40 17
                     2+7 ADDRESS TO MEMORY
       908
                     2+8 ADDRESS TO MEMORY
2+9 ADDRESS TO MEMORY
        90
            G40 18
9W20
            G41 11
9W20
        91
                      2+10 ADDRESS TO MEMORY
        92
9W20
            G41 14
        93
                      2+11 ADDRESS TO MEMORY
9W20
            G41 17
                     2+12 ADDRESS TO MEMORY
        94
            G41 18
9W20
9W20
        95
            G42 11
                      2+13 ADDRESS TO MEMORY
9W20
        96
            G42 14
                      2+14 ADDRESS TO MEMORY
            G42 17
                     2+15 ADDRESS TO MEMORY
9W20
        97
            G42 18
9W20
        98
                     2+16 ADDRESS TO MEMORY
9W07
       900
            H19
                     2 + 0
                     2+0 FAN OUT TO MEMORY
9W03
       900
            H19
       901
            H19
9W07
                  8
                      2+1
                     2+1 FAN OUT TO MEMORY
2+2 FAN OUT TO MEMORY
            H19
9W03
      901
                 19
            H19
9W03
       902
                 24
9W07
       902
            H19
                      2 * 2
                 28
            H20
9W07
       903
                      2+3
       903
                  2
                      2+3 FAN OUT TO MEMORY
9W03
            H20
9W07
      904
            H20
                  8
                     2+4
9W03
                     2+4 FAN OUT TO MEMORY
      904
            H20 19
                      2+5 FAN OUT TO MEMORY
9W03
       905
            H20 24
       905
            H20 28
                     2+5
9W07
9W07
       906
            H21
                      2+6
                  1
9W03
       906
            H21
                      2+6 FAN OUT TO MEMORY
                  2
9W07
       907
            H21
                  8
                      2+7
                     2±7 FAN OUT TO MEMORY
9W03
       907
            H21 19
                     2+8 FAN OUT TO MEMORY
9W03
        90
            H21 24
9W07
        90
            H21 28
                     2+8
9W07
        91
            H22
                      2+9
                  1
                      2+9 FAN OUT TO MEMORY
        91
9W03
            H22
                  2
        92
9W07
            H22
                      2+10
                  8
9W03
        92
            H22 19
                     2+10 FAN OUT TO MEMORY
            H22 24
9W03
        93
                      2+11 FAN OUT TO MEMORY
9W07
        93
            H22
                 28
                      2+11
        94
            H24
                      2+12
9W07
9W03
        94
            H24
                      2+12 FAN OUT TO MEMORY
        95
            H24
9W07
                      2 * 13
9W03
        95
            H24 19
                      2+13 FAN OUT TO MEMORY
9W03
        96
            H24
                 24
                      2+14 FAN OUT TO MEMORY
        96
            H24 28
9W07
                     2+14
            H25
                     2+15
9408
       900
            H25
                      2+15 FAN OUT TO MEMORY
9W04
       900
9W08
       901
            H25
                      2+16
                  A
                      2+16 FAN OUT TO MEMORY
9W04
       901
            H25
                 19
            H25
                      2+17 FAN OUT TO MEMORY
9W04
      902
                 24
            H25
9W08
      902
                28
                      2417
9W08
      903
            H26
                      2+18
                  1
9W04
      903
            H26
                  2
                     2+18 FAN OUT TO MEMORY
```

```
9W08
       904
            H26 8
                     2+19
       904
9W04
            H<sub>26</sub> 19
                     2+19 FAN OUT TO MEMORY
9W04
       905
            H26 24
                     2+20 FAN OUT TO MEMORY
                     2+20
9 W 0 8
       905
            H26 28
9W08
       906
            H27
                      2+21
            H27
                     2+21 FAN OUT TO MEMORY
9W04
       906
                  2
9 W 0 B
       907
            H27
                  8
                     2+22
            H27 19
       907
9W04
                     2+22 FAN OUT TO MEMORY
9W04
        90
            H27
                     2+23 FAN OUT TO MEMORY
                 24
            H27
        90
9W08
                28
                     2 * 23
9W08
        91
            H29
                     2+24
                  1
9W04
        91
            H29
                  2
                     2+24 FAN OUT TO MEMORY
        92
            H29
9W08
                  A
                     2 * 25
9W04
        92
            H29
                19
                     2+25 FAN OUT TO MEMORY
       93
            H29
9W04
                 24
                     2+26 FAN OUT TO MEMORY
9W08
        93
            H29
                28
                     2+26
9 W D 8
        94
            H30
                     2+27
                  1
                     2+27 FAN OUT TO MEMORY
9W04
        94
            H30
                  2
        95
9 W 0 8
            H30
                  A
                     2+28
        95
9W04
            H30
                      2+28 FAN OUT TO MEMORY
                19
9W04
                     2+29 FAN OUT TO MEMORY
        96
            H30 24
        96
            H30 28
9W08
                     2+29
9W39
      900
            H31
                     2+30
                 1
                     2+30 FAN OUT TO MEMORY
9W05
            H31
      900
                  2
9W39
      901
            H31
                 8
                     2+31
9W05
      901
            H31 19
                     2+31 FAN OUT TO MEMORY
                     2+32 FAN OUT TO MEMORY
9W05
      902
            H31
                24
9W39
      902
            H31 28
                     2+32
9W39
      903
            H32
                     2+33
                  2
9W05
      903
            H32
                     2+33 FAN OUT TO MEMORY
9W39
      904
            H32
                  8
                     2+34
9W05
      904
                     2+34 FAN OUT TO MEMORY
            H32
                19
9W05
      905
            H32 24
                     2+35 FAN OUT TO MEMORY
9W39
      905
            H32 28
                     2 * 35
9W39
      906
            H34
                     2*36
9W05
      906
            H34
                     2+36 FAN OUT TO MEMORY
9W39
      907
            H34
                  8
                     2+37
9W05
       907
            H34 19
                     2+37 FAN OUT TO MEMORY
                     2+38 FAN OUT TO MEMORY
        90
            H34 24
9W05
9W39
        90
            H34 28
                     2+38
9W39
        91
            H35
                  1
                     2+39
        91
            H35
9W05
                     2+39 FAN OUT TO MEMORY
                  2
9W39
        92
            H35
                     2+40
                  8
9W05
        92
            H35
                     2+40 FAN OUT TO MEMORY
                19
                     2+41 FAN OUT TO MEMORY
9W05
        93
            H35 24
        93
            H35
9W39
                28
                     2+41
        94
9W39
            H36
                     2 * 42
        94
9W05
            H36
                     2+42 FAN OUT TO MEMORY
                  2
9W39
        95
            H36
                  8
                     2+43
        95
9W05
            H36 19
                     2+43 FAN OUT TO MEMORY
        96
                     2+44 FAN OUT TO MEMORY
9W05
            H36 24
       96
9W39
            H36
                28
                     2 + 44
9W09
      900
            H37
                     2+45
                  1
9W06
      900
            H37
                  2
                     2+45 FAN OUT TO MEMORY
            H37
9W09
                     2 * 46
      901
                  8
9W06
      901
            H37
                19
                     2*46 FAN OUT TO MEMORY
            H37
                     2#47 FAN OUT TO MEMORY
9W06
      902
                 24
      902
9W09
            H37 28
                     2+47
9W09
      903
            H39
                     2+48
            H39
9W06
      903
                     2+48 FAN OUT TO MEMORY
                  2
            H39
9W09
      904
                     2 + 49
                  8
            H39 19
9W06
      904
                     2+49 FAN OUT TO MEMORY
```

```
9W06
       905
            H39 24
                     2+50 FAN OUT TO MEMORY
            H39 28
9W09
       905
                      2+50
9409
       906
             H40
                      2 * 51
                  1
                      2+51 FAN OUT TO MEMORY
9W06
       906
             H40
       907
9W09
             H40
                      2+52
                  8
9W06
       907
             H40
                 19
                      2+52 FAN OUT TO MEMORY
9W06
        90
            H40
                 24
                      2+53 FAN OUT TO MEMORY
        90
                 28
9W09
            H40
                      2 + 53
        91
9W09
            H41
                      2+54
                  1
        91
            H41
9W06
                      2+54 FAN OUT TO MEMORY
                  2
9 W 0 9
        92
            H41
                  8
                      2+55
        92
                      2+55 FAN OUT TO MEMORY
            H41 19
9W06
9W06
            H41 24
        93
                      2+56 FAN OUT TO MEMORY
9W09
        93
            H41 28
                      2+56
        94
                      2 * 57
9W09
            H42
                      2+57 FAN OUT TO MEMORY
        94
            H42
9W06
                  2
        95
9W09
            H42
                      2+58
                  8
        95
9W06
            H42 19
                      2+58 FAN OUT TO MEMORY
9W06
        96
            H42 24
                      2+59 FAN OUT TO MEMORY
        96
9W09
            H42 28
                      2 * 59
        99
             124 12
                      READ PERIPHERAL
9W14
                      2+30 CENTRAL TO PERIPHERAL DATA
             125
9W11
       900
                  1
            125
9W14
       900
                  3
                      2+30
9W13
       900
            125
                  5
                      2+30
            125
9W39
       900
                      2+30
9W15
       900
            125
                  9
                      2+30
9W16
       900
             125 11
                      2+30
            125
9W17
       900
                 19
                      2+30
                 21
            125
       900
                      2 + 30
9W18
            125 23
9W19
       900
                      2+30
                      2+30 BUFFER TO CONTROL
2+30 MEMORY TO REGISTER
2+31 CENTRAL TO PERIPHERAL DATA
9W10
            125 26
       900
       900
             125
                 28
9W12
9W11
       901
            126
9W14
       901
            126
                  3
                      2+31
9W13
       901
            126
                   5
                      2*31
9W39
       901
            126
                  7
                      2+31
9W15
       901
             126
                  9
                      2+31
9W16
       901
             126 11
                      2+31
                      2 * 31
9W17
       901
            126
                 19
            126 21
                      2+31
9W18
       901
9W19
       901
            126 23
                      2.31
                      2+31 BUFFER TO CONTROL
9W10
       901
             126
                 26
                      2+31 MEMORY TO REGISTER
9W12
       901
            126 28
            127
                      2+32 CENTRAL TO PERIPHERAL DATA
9W11
       902
                  1
            127
                      2+32
9W14
       902
                  3
9W13
       902
            127
                  5
                      2+32
9W39
       902
             127
                  7
                      2 * 32
            127
9W15
                      2*32
                  9
       902
            127 11
                      2+32
9W16
       902
9W17
       902
             127
                 19
                      2+32
             127 21
9W18
       902
                      2+32
             127
9W19
       902
                 23
                      2.32
                      2+32 BUFFER TO CONTROL
             127
9W10
       902
                 26
            127
9W12
       902
                 28
                      2+32 MEMORY TO REGISTER
                      2+33 CENTRAL TO PERIPHERAL DATA
9W11
       903
             128
                  1
       903
            128
                      2*33
9W14
                  3
9W13
       903
             128
                  5
                      2+33
9W39
       903
             128
                      2 * 33
            128
                      2 + 33
9W15
       903
                  9
9W16
       903
            128 11
                      2 * 33
            128 19
9W17
       903
                      2 * 33
9W18
      903
            128 21
                      2 + 33
```

```
9W19
        903
              128 23
                        2+33
                  26
                        2+33 BUFFER TO CONTROL
       903
              128
9W10
                        2+33 MEMORY TO REGISTER
9W12
       903
              128
                   28
9W11
              129
       904
                        2+34 CENTRAL TO PERIPHERAL DATA
9W14
       904
              129
                        2+34
                    3
              129
9W13
       904
                    5
                        2+34
                    7
                        2+34
9W39
       904
              129
9W15
       904
              129
                    9
                        2+34
              129
       904
                       2+34
9W16
                  11
              129
9W17
       904
                  19
                        2+34
              129
9W18
       904
                   21
                        2+34
9W19
       904
              129
                   23
                        2+34
                        2+34 BUFFER TO CONTROL
2+34 MEMORY TO REGISTER
9W10
       904
              129
                   26
9W12
       904
              129
                   28
9W11
       905
                        2+35 CENTRAL TO PERIPHERAL DATA
              132
                    1
       905
9W14
              132
                    3
                        2 * 35
9W13
       905
              132
                        2+35
       905
                    7
9W39
              132
                        2+35
9W15
       905
              132
                    9
                        2+35
9W16
              132
       905
                        2+35
                  11
       905
9W17
              132
                   19
                        2+35
9W18
              132
       905
                  21
                        2+35
9W19
       905
              132
                  23
                        2+35
       905
                        2+35 BUFFER TO CONTROL
9W10
              132 26
9W12
       905
              132
                  28
                        2+35 MEMORY TO REGISTER
                        2+36 CENTRAL TO PERIPHERAL DATA
9W11
       906
              133
                    1
9W14
              133
                        2+36
       906
                    3
9W13
       906
              133
                    5
                        2+36
                    7
9W39
       906
              133
                        2+36
                    9
9W15
       906
              133
                        2+36
9W16
       906
              133
                        2+36
                  11
9W17
       906
              133
                        2+36
                   19
9W18
       906
              133
                  21
                        2+36
9W19
              133 23
       906
                        2 * 36
                        2+36 BUFFER TO CONTROL
2+36 MEMORY TO REGISTER
9W10
       906
              133
                  26
9W21
       900
              133
                  28
              134
9W11
                        2+37 CENTRAL TO PERIPHERAL DATA
       907
9W14
       907
              134
                        2+37
       907
              134
                       2+37
9W13
                    5
9W39
       907
              134
                    7
                        2+37
       907
              134
9W15
                    9
                        2+37
              134
9W16
       907
                        2+37
                  11
9W17
       907
              134
                        2+37
                  19
       907
              134 21
9W18
                        2 * 37
       907
                  23
9W19
              134
                        2+37
                        2+37 BUFFER TO CONTROL
2+37 MEMORY TO REGISTER
9W10
       907
              134
                   26
              134
9W21
        901
                   58
                        2+38 CENTRAL TO PERIPHERAL DATA
9W11
         90
              135
                    1
              135
9W14
         90
                        2 * 38
                        2+38
9W13
         90
              135
                    5
9W39
         90
              135
                    7
                        2+38
              135
9W15
         90
                    9
                        2+38
9W16
         90
              135
                   11
                        2+38
9W17
         90
              135
                   19
                        2+38
9W18
         90
              135
                   21
                        2+38
              135
9W19
         90
                        2+38
                  23
                        2+38 BUFFER TO CONTROL
2+38 MEMORY TO REGISTER
2+39 CENTRAL TO PERIPHERAL DATA
9W10
         90
              135
                   26
9W21
        902
              135
                   28
         91
9W11
              136
                    1
9W14
         91
              136
                        2 + 39
                    3
         91
9W13
              136
                    5
                        2+39
9W39
         91
              136
                        2+39
```

```
2+39
9W15
        91
            136 9
             136 11
9W16
        91
                      2 + 39
9W17
        91
             136 19
                      2+39
        91
9W18
             136 21
                      2 * 39
             136 23
                      2+39
9W19
        91
        91
                      2+39 BUFFER TO CONTROL
9W10
             136 26
9W21
       903
             136 28
9W11
        92
             138
                  1
                      2+40 CENTRAL TO PERIPHERAL DATA
        92
9W14
             138
                   3
                      2+40
        92
9W13
             138
                   5
                      2+40
9W39
        92
             138
                   7
                      2+40
                  9
             138
9W15
        92
                      2+40
             138 11
        92
9W16
                      2+40
        92
9W17
             138
                 19
                      2+40
             138 21
9W18
        92
                      2+40
9W19
        92
             138 23
                      2+40
9W10
        92
                      2+40 BUFFER TO CONTROL
             138 26
                      2+40 MEMORY TO REGISTER
9W21
       904
             138
                 28
                      2+41 CENTRAL TO PERIPHERAL DATA
        93
9W11
             139
                  1
             139
9W14
        93
                      2+41
                   3
9W13
        93
            139
                   5
                      2+41
        93
            139
                  7
9W39
                      2+41
        93
             139
9W15
                   9
                      2 + 41
             139
        93
9W16
                 11
                      2+41
        93
             139
9W17
                      2+41
                 19
        93
             139 21
                      2+41
9W18
9W19
        93
             139 23
                      2 441
        93
             139
                      2+41 BUFFER TO CONTROL
9W10
                 26
                      2+41 MEMORY TO REGISTER
9W21
       905
             139
                 28
                      2+42 CENTRAL TO PERIPHERAL DATA
9W11
        94
             140
9W14
             140
                      2 + 42
                   3
        94
9W13
             140
                   5
                      2 * 42
        94
             140
                  7
                      2+42
9W39
9W15
        94
             140
                  9
                      2 + 42
        94
9W16
             140 11
                      2 + 42
        94
             140 19
                      2+42
9W17
9W18
        94
             140 21
                      2+42
        94
                      2+42
9W19
             140 23
        94
             140 26
                      2+42 BUFFER TO CONTROL
9W10
                      2+42 MEMORY TO REGISTER
9W21
       906
             140
                 28
                      2+43 CENTRAL TO PERIPHERAL DATA
9W11
        95
             141
        95
                      2+43
9W14
            141
        95
             141
                      2+43
9W13
                  5
        95
9W39
             141
                  7
                      2 + 43
9W15
        95
             141
                  9
                      2 * 43
        95
9W16
             141 11
                      2 + 43
9W17
        95
            141 19
                      2 + 43
        95
             141 21
                      2 + 43
9W18
9W19
        95
             141 23
                      2 + 43
9W10
        95
             141 26
                      2*43 BUFFER TO CONTROL
             141 28
9W21
       907
                      2443 MEMORY TO REGISTER
9W11
                      2+44 CENTRAL TO PERIPHERAL DATA
        96
             142
                  1
        96
             142
                      2 + 44
9W14
                   3
        96
             142
                      2 - 44
9W13
                  5
9W39
        96
             142
                  7
                      2+44
        96
             142
9W15
                  9
                      2 + 44
        96
9W16
             142 11
                      2+44
        96
             142 19
9W17
                      2+44
             142 21
        96
9W18
                      2+44
9W19
        96
             142
                 53
                      2 + 4 4
        96
             142 26
                      2+44 BUFFER TO CONTROL
9W10
9W21
       908
             142 28
                      2444 MEMORY TO REGISTER
```

```
9W36
       907
                      CLOCK (1 M SEC)
             J31
                  1
9W34
       907
             J31
                      CLOCK (1US)
                  3
       906
             J31 13
9W34
9W36
       906
             J31 15
                      INACTIVE
9W34
       903
             J33
                  3
9W34
       905
            J33
                  5
                      EMPTY
                  7
                      FULL
9W34
       904
             J33
9W36
       902
                 10
            J33
                      ACTIVE
9W36
       903
            J33
                 21
                      INACTIVE
      905
9W36
             J33 22
                      EMPTY
9W36
       904
             J33
                 23
                      FULL
       902
9W34
             J33 28
                      ACTIVE
9W34
        92
            J35
                      2+2 INPUT DATA
                  3
        91
9W34
            J35
                      2+1 INPUT DATA
                          INPUT DATA
        90
            J35
                  7
                      2+0
9W34
        95
            J35
9W36
                  8
                      2+5
        94
            J35
                          INPUT DATA
9W36
                  9
                      2+4
            J35
        93
                          INPUT DATA
9W36
                 10
                      2+3
        92
            J35
9W36
                 21
                      2+2
                          INPUT DATA
                          INPUT DATA
        91
            J35
                22
                     2+1
9W36
9W36
        90
            J35
                          INPUT DATA
                 23
                      2+0
9W34
        95
            J35
                 24
                      2+5
                          INPUT DATA
        94
            J35
9W34
                          INPUT DATA
                      2 * 4
                 26
9W34
        93
            J35
                 28
                      2+3 INPUT DATA
        98
            J37
                      2+8 INPUT DATA
9W34
                  3
            J37
9W34
        97
                      2+7 INPUT DATA
                  5
            J37
9W34
        96
                  7
                      2+6 INPUT DATA
            J37
9W36
                      2+11 INPUT DATA
       901
                  8
9W36
       900
            J37
                      2+10 INPUT DATA
9W36
        99
            J37
                      2+9 INPUT DATA
                 10
            J37
        98
                 21
9W36
                      2+8 INPUT DATA
            J37
                          INPUT DATA
9W36
        97
                 22
                      2 + 7
            J37
        96
9W36
                      2+6 INPUT DATA
                23
            J37
                      2+11 INPUT DATA
9W34
      901
                 24
            J37
9W34
       900
                 26
                     2+10 INPUT DATA
        99
            J37
                      2+9 INPUT DATA
9W34
                 28
9W37
      906
            J38
                  1
                      FUNCTION
                      FUNCTION
9W35
      906
            J38
                  3
9W35
      902
            J38
                13
                      ACTIVE
                      INACTIVE
9W37
      903
            J38
                 14
            J38
9W37
      902
                 15
                      ACTIVE
9W35
       903
            J38
                 16
                      INACTIVE
            J38
9W35
      904
                      FULL
                 26
                      FULL
9W37
       904
            J38
                 28
      905
9W37
            J39
                      EMPTY
                  1
                      EMPTY
      905
            J39
9W35
                  3
                      MASTER CLEAR
9W35
       907
            J39
                 13
                      MASTER CLEAR
            J39
       907
9W37
                 15
        90
            J40
                      2+0 OUTPUT DATA
9W37
                  1
9W35
        90
            J40
                      2+0 OUTPUT DATA
                  3
        91
            J40 13
9W35
                     2+1 OUTPUT DATA
9W37
        92
            J40 14
                      2+2 OUTPUT DATA
9W37
        91
            J40
                 15
                      2+1
                          OUTPUT DATA
9W35
        92
            J40
                      2+2 OUTPUT DATA
                 16
                26
        93
            J40
                      2+3 OUTPUT DATA
9W35
        93
                      2+3 OUTPUT DATA
            J40
9W37
                28
9W37
        94
            J41
                      2+4 OUTPUT DATA
        94
9W35
            J41
                      2+4 OUTPUT DATA
        95
9W35
            J41 13
                      2+5 OUTPUT DATA
9W37
        96
            J41 14
                      2+6 OUTPUT DATA
        95
9W37
             J41 15
                      2+5 OUTPUT DATA
        96
9W35
            J41 16
                      2+6 OUTPUT DATA
```

```
J41 26 2+7 QUTPUT DATA
9W35
                                      2+7 OUTPUT DATA
2+8 OUTPUT DATA
2+8 OUTPUT DATA
2+9 OUTPUT DATA
9W37
              97
                     J41 28
                     J42 1
J42 3
J42 13
9W37
              98
              98
9W35
9W35
              99
                      J42 14 2+10 OUTPUT DATA
J42 15 2+9 OUTPUT DATA
J42 16 2+10 OUTPUT DATA
J42 26 2+11 OUTPUT DATA
J42 28 2+11 OUTPUT DATA
9W37
            900
9W37
9W35
             99
            900
9W35
            901
9W37
            901
```

```
97
            C14 14
                      MEMORY MARGIN
10W11
                       2+5 ADDRESS TO MEMORY
10W20
        906
             G01 11
10W20
        907
             G01 14
                       2+6 ADDRESS TO MEMORY
10W20
        908
             G01 17
                       2+7 ADDRESS TO MEMORY
10W20
         90
             G01 18
                       2+8 ADDRESS TO MEMORY
         91
                       2+9 ADDRESS TO MEMORY
             G02 11
10W20
                       2+10 ADDRESS TO MEMORY
10W20
         92
             G02 14
                       2+11 ADDRESS TO MEMORY
         93
10W20
             G02 17
                       2+12 ADDRESS TO MEMORY
2+13 ADDRESS TO MEMORY
             G02 18
         94
10W20
10W20
         95
             G03 11
         96
                       2+14 ADDRESS TO MEMORY
             G03 14
10W20
                      2+15 ADDRESS TO MEMORY
2+16 ADDRESS TO MEMORY
         97
             G03 17
10W20
         98
10W20
             G03 18
                       2+3 CHASSIS TO SELECT (011)
10W20
        904
             G 0 4
                  - 5
                       2+2 CHASSIS 10 SELECT (011)
2+4 CHASSIS 10 SELECT (011)
10W20
        903
             G 0 4
                   8
             G04 14
10W20
        905
             G04 20
         98
                       WRITE
19W14
         97
                       GO
10W14
             004 21
             G05 18
                       2+0 BANK SELECT
10W20
        901
                       2+1 BANK SELECT
             G05 22
10W20
        902
         97
             G05 28
                       ACCEPT FROM CHAS, 10
10W13
10W20
        900
             G13 12
                       CLOCK
                       2+0
10W07
        900
             HO1
10W03
        900
             H01
                       2+0 FAN OUT TO MEMORY
                   2
10W07
        901
             HO1
                       2+1
                       2+1 FAN OUT TO MEMORY
10W03
             H01 19
        901
                       2+2 FAN OUT TO MEMORY
10W03
        902
             H01 24
                       2+2
10W07
        902
             H01 28
                       2+3
10W07
        903
             H02
                  1
                       2+3 FAN OUT TO MEMORY
10W03
        903
             H02
                   2
        904
             H02
10W07
                   8
                       2 + 4
        904
             H02 19
                       2+4 FAN OUT TO MEMORY
10W03
10W03
        905
             H02 24
                       2+5 FAN OUT TO MEMORY
10W07
        905
             H02 28
                       2+5
10W07
        906
             H03
                   1
                       2+6
                       2+6 FAN OUT TO MEMORY
        906
10W03
             H03
                   2
10W07
        907
             H03
                   8
                       2+7
                       2+7 FAN OUT TO MEMORY
        907
             H03 19
10W03
10W03
         90
             H03 24
                       2+8 FAN OUT TO MEMORY
10W07
         90
             H03 28
                       2 * 8
         91
             H04
10W07
                       2+9
                       2+9 FAN OUT TO MEMORY
         91
             H04
10W03
         92
             H04
                       2+10
10W07
                  8
             H04 19
                       2+10 FAN OUT TO MEMORY
         92
10W03
10W03
         93
             H04 24
                       2+11 FAN OUT TO MEMORY
             H04
         93
                  28
10W07
                       2+11
                       2+12
10W07
         94
             H06
                  1
10W03
         94
             H06
                   2
                       2+12 FAN OUT TO MEMORY
         95
                       2+13
             H06
10W07
                   8
         95
             H06 19
                       2+13 FAN OUT TO MEMORY
10W03
10W03
         96
             H06
                  24
                       2+14 FAN OUT TO MEMORY
         96
             H06 28
10W07
                       2-14
10W08
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             H07
                       2+15
                   1
             H07
                       2+15 FAN OUT TO MEMORY
10W04
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                       2+16
             H07
10W08
        901
                   A
10W04
        901
             H07
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                       2+16 FAN OUT TO MEMORY
             H07
                 24
                       2-17 FAN OUT TO MEMORY
10W04
        902
             H07
        902
                  28
                       2+17
10W08
        903
             H08
                       2+18
10W08
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        903
             H08
                       2+18 FAN OUT TO MEMORY
10W04
                  . 2
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             H08 8
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10W04
        904
              H08 19
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        905
                       2+20 FAN OUT TO MEMORY
10W04
             H08 24
10W08
        905
             H08 28
                       2+20
10W08
        906
             H09
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                       2+21
             H09
                       2+21 FAN OUT TO MEMORY
10W04
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                   2
        907
             H09
10W08
                       2 * 22
                   8
10W04
        907
             H<sub>0</sub>9 19
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10W04
             H09 24
         90
                       2+23 FAN OUT TO MEMORY
         90
             H09 28
10W08
                       2+23
         91
10W08
                       2+24
             H11
10W04
         91
              H11
                       2+24 FAN OUT TO MEMORY
10W08
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             H11
                       2+25
         92
             H11 19
                       2+25 FAN OUT TO MEMORY
10W04
         93
                  24
                       2+26 FAN OUT TO MEMORY
10W04
              H11
         93
                       2+26
10W08
             H11
                  28
         94
             H12
                       2 * 27
10W08
                       2+27 FAN OUT TO MEMORY
         94
10W04
             H12
                   2
         95
             H12
                       2+28
10W08
                   8
                       2+28 FAN OUT TO MEMORY
2+29 FAN OUT TO MEMORY
         95
             H12 19
10W04
10W04
         96
             H12
                  24
         96
             H12 28
10W08
                       2+29
10W09
        900
             H<sub>1</sub>3
                       2+30
                  1
                      2+30 FAN OUT TO MEMORY
             H13
10W05
        900
                   2
10W09
        901
             H13
                   8
                       2+31
                       2+31 FAN OUT TO MEMORY
10W05
        901
             H13
                  19
             H13 24
10W05
        902
                       2+32 FAN OUT TO MEMORY
10W09
        902
             H13 28
                       2+32
10W09
        903
             H14
                       2*33
10W05
        903
             H14
                       2+33 FAN OUT TO MEMORY
                   2
10W09
        904
             H14
                       2+34
10W05
        904
              H14 19
                       2+34 FAN OUT TO MEMORY
10W05
        905
             H14 24
                       2+35 FAN OUT TO MEMORY
        905
             H14 28
                      2+35
10W09
10W09
        906
             H16
                       2+36
                  1
                       2+36 FAN OUT TO MEMORY
10W05
        906
             H16
                   2
        907
                      2+37
10W09
             H16
                   8
        907
                       2+37 FAN OUT TO MEMORY
10W05
             H16 19
10W05
         90
             H16 24
                      2+38 FAN OUT TO MEMORY
         90
             H16 28
                      2*38
10W09
10W09
         91
             H17
                   4
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             H17
                       2+39 FAN OUT TO MEMORY
10W05
         91
                   2
         92
             H17
10W09
                      2+40
                   8
             H17 19
10W05
         92
                      2+40 FAN OUT TO MEMORY
                       2+41 FAN OUT TO MEMORY
10W05
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             H17 24
         93
             H17
10W09
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                       2 + 41
         94
             H18
                       2+42
10W09
         94
                       2+42 FAN OUT TO MEMORY
10W05
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10W09
             H18
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10W05
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             H18 19
                       2+43 FAN OUT TO MEMORY
10W05
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             H18
                 24
                       2444 FAN OUT TO MEMORY
         96
10W09
             H18 28
                       2 + 44
             H19
                       2+45
10W39
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                   1
             H19
                       2+45 FAN OUT TO MEMORY
10W06
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             H19
                       2+46
10W39
        901
                   8
             H19 19
                       2+46 FAN OUT TO MEMORY
10W06
        901
             H19 24
10W06
        902
                       2+47 FAN OUT TO MEMORY
             H19
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10W39
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10W39
        903
              H21
                       2 + 48
                  1
10W06
        903
             H21
                      2+48 FAN OUT TO MEMORY
                   2
10W39
        904
             H21 8
                       2+49
        904
             H21 19
                       2449 FAN OUT TO MEMORY
10W06
```

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        905
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10W39
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10W39
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10W06
        906
             H22
                   2
             H22
        907
                      2+52
10W39
                   8
10W06
        907
             H22 19
                      2+52 FAN OUT TO MEMORY
         90
                      2+53 FAN OUT TO MEMORY
10W06
             H22 24
10W39
         90
             H22 28
                      2+53
10W39
         91
             H23
                      2+54
         91
             H23
                      2+54 FAN OUT TO MEMORY
10W06
                   2
10W39
         92
             H23
                      2+55
                   8
         92
             H23 19
                      2+55 FAN OUT TO MEMORY
10W06
         93
             H23
                      2+56 FAN OUT TO MEMORY
10W06
                 24
         93
             H23 28
                      2+56
10W39
             H24
10W39
         94
                      2+57
                  1
         94
                      2+57 FAN OUT TO MEMORY
10W06
             H24
             H24
         95
                      2+58
10W39
                   8
         95
             H24 19
                      2+58 FAN OUT TO MEMORY
10W06
                      2+59 FAN OUT TO MEMORY
10W06
         96
             H24
                 24
         96
             H24
                      2+59
                 28
10W39
                      2+45 CENTRAL TO PERIPHERAL DATA
10W11
        900
             101
                  1
                      2+45
10W14
        900
             101
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10W13
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10W15
       900
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10W39
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       900
             101
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10W16
       900
             101 11
10W17
       900
             101 19
                      2+45
10W18
                      2+45
       900
             101 21
       900
             101 23
                      2+45
10W19
10W10
       900
             101 26
                      2+45
10W12
             101 28
                      2+45 MEMORY REGISTER
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                      2+46 CENTRAL TO PERIPHERAL DATA
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             102
             102
       901
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10W13
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                      2+46
             102
                      2+46
10W15
       901
10W39
       901
             102
                   9
                      2+46
             102 11
10W16
       901
                      2 + 46
       901
             102 19
                      2+46
10W17
10W18
       901
             102 21
                      2+46
             102 23
                      2+46
10W19
       901
             102 26
                      2+46
10W10
       901
                      2+46 MEMORY REGISTER
10W12
        91
             102 28
                      2+47 CENTRAL TO PERIPHERAL DATA
             103
10W11
       902
10W14
        902
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                   3
10W13
       902
             103
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                      2+47
             103
10W15
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10W39
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             103
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                      2+47 MEMORY REGISTER
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10W17
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10W18
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                      2+48
             104 23
10W19
                      2 + 48
       903
```

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903
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10W12
        93
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10W11
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10W39
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       904
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10W16
       904
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             105
                      2+49
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10W18
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10W19
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       904
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10W10
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10W15
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                      2+52
10W19
       907
             110
                      2+52
                 23
10W10
        907
             110 26
                      2+52
                      2+52 MEMORY REGISTER
10W12
         97
             110 28
10W11
         90
                      2+53 CENTRAL TO PERIPHERAL DATA
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                   1
10W14
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             111
                   3
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                      2+53
10W13
             111
10W15
         90
             111
                      2+53
10W39
         90
                   9
                      2 + 53
             111
10W16
         90
             111 11
                      2+53
         90
                      2+53
10W17
             111
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10W18
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10W19
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             111
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                      2+54
10W39
         91
             112
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                      2+54
```

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91 112 11
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            112 19
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10W18
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             112 21
                     2+54
        91
                     2+54
10W19
            112 23
10W10
        91
            112 26
                     2+54
        99
                     2+54 MEMORY REGISTER
10W12
            112 28
10W11
        92
            114
                     2+55 CENTRAL TO PERIPHERAL DATA
                 1
10W14
        92
            114
                  3
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        92
            114
10W13
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        92
            114
                 7
10W15
                     2+55
        92
10W39
            114
                  9
                     2+55
        92
            114 11
10W16
                     2+55
10W17
        92
            114 19
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        92
            114 21
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10W19
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        92
10W10
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            114 28
10W12
       900
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            115
        93
10W14
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        93
            115
10W13
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                     2+56
                  ź
        93
            115
                     2+56
10W15
10W39
        93
            115
                 9
                     2+56
10W16
        93
            115 11
                     2+56
        93
            115 19
                     2+56
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            115 21
                     2+56
10W18
10W19
        93
            115 23
                     2+56
10W10
        93
            115 26
                     2+56
10W12
       901
            115 28
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                     2+57 CENTRAL TO PERIPHERAL DATA
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10W39
        94
            116
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        94
10W16
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10W17
        94
            116 19
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        94
10W18
            116 21
                     2+57
        94
                     2+57
10W19
            116 23
        94
10W10
            116 26
                     2+57
                     2+57 MEMORY REGISTER
10W12
       902
            116 28
            117
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10W11
        95
                 1
            117
10W14
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                     2+58
                  3
10W13
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            117
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                     2 * 58
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            117
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10W15
        95
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10W39
                  9
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            117 11
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                     2+58
10W16
10W17
        95
            117 19
                     2+58
        95
            117 21
                     2+58
10W18
        95
            117
                23
                     2+58
10W19
        95
             117 26
                     2+58
10W10
10W12
        903
            117 28
                     2+58 MEMORY REGISTER
                     2+59 CENTRAL TO PERIPHERAL DATA
10W11
        96
            118
                 1
        96
                     2+59
            118
10W14
                  3
10W13
        96
            118
                  5
                     2.59
10W15
        96
            118
                  7
                     2459
        96
                     2459
10W39
            118
                  9
10W16
        96
                     2 + 59
            118 11
        96
            118 19
10W17
                     2 * 5 9
        96
            118 21
                     2+59
10W18
        96
                     2+59
10W19
             118 23
        96
10W10
             118 26
                     2459
       904
            118 28
                     2.59 MEMORY REGISTER
10W12
10W14
        99
            119 12 READ PERIPHERAL
```

```
91
10W23
              J01
                  5
                       2+1 READ DATA
10W23
         90
                       2+0 READ DATA
              J01 12
         93
                       2+3 READ DATA
10W23
              J01 19
10W23
         92
                       2+2 READ DATA
              J01
                  26
10W23
         95
                       2+5 READ DATA
              J02
         94
                       2+4 READ DATA
10W23
              J02
                  12
         97
                       2+7 READ DATA
10W23
              J02
                  19
10W23
         96
              J02
                       2+6 READ DATA
                  26
         99
                       2+9 READ DATA
10W23
              J03
                   5
         98
              J03
                       2+8 READ DATA
10W23
                  12
        901
10W23
              103
                  19
                       2+11
10W23
        900
              J03
                  26
                       2+10
10W23
        903
              J04
                       CLOCK
                       REV MARK
              J04
10W23
        902
                  12
                       2+8 WRITE DATA
2+7 WRITE DATA
         98
              J05
10W24
         97
10W24
              J05
              J05
                       2+6 WRITE DATA
         96
10W24
                    8
10W24
        901
              J05
                 23
                       2+11
              J05
10W24
        900
                  25
                       2+10
                       2+9 WRITE DATA
         99
              J05
                  27
10W24
10W24
         92
              J06
                       2+2 WRITE DATA
         91
                       2+1 WRITE DATA
10W24
              J06
                    6
         90
                       2+0 WRITE DATA
10W24
              J06
                    8
         95
                       2+5 WRÎTE DATA
                  23
10W24
              J06
10W24
         94
              J06
                  25
                       2+4 WRÎTE DATA
                       2+3 WRITE DATA
10W24
         93
              J06
                  27
              J07
                       WRITE
        904
10W24
10W24
        903
              J07
                       GROUP
              J07
                       POS CONTROL
10W24
        902
        905
              J07
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                       CLOCK
10W24
10W25
        906
              K01
                       FUNCTION
                       FULL
10W25
        904
              K01
10W25
        903
              K01
                   8
                       INACTIVE
10W25
              K01 23
                       ACTIVE
        902
                       MASTER CLEAR
10W25
        907
              K01 25
10W25
        905
              K01
                  27
                       EMPTY
                       2+2 OUTPUT DATÁ
10W25
         92
              K02
                   4
                       2+1 OUTPUT DATA
         91
10W25
              K02
                   6
                       2+0 OUTPUT DATA
10W25
         90
              K02
                       2+5 OUTPUT DATA
10W25
         95
              K02
                  23
10W25
         94
              K02
                  25
                       2+4 OUTPUT DATA
         93
                       2+3 OUTPUT DATA
10W25
              K02
                  27
10W25
         98
                       2+8 OUTPUT DATA
              K03
                   4
         97
                       2+7 OUTPUT DATA
10W25
              K03
                   6
         96
                       2+6 OUTPUT DATA
10W25
              K03
                   8
        901
              K03
10W25
                       2+11 OUTPUT DATA
                  23
                       2+10 OUTPUT DATA
10W25
        900
              K03
                  25
         99
                  27
                       2+9 OUTPUT DATA
10W25
              K03
10W27
        902
              K 0 4
                       ACTIVE
              K04
                       FULL
        904
10W27
                   6
10W27
        903
              K 0 4
                       INACTIVE
                   8
              K 0 4
        905
                  27
                       EMPTY
10W27
         92
              Kn5
                       2+2 INPUT DATA
10W27
                       2+1 INPUT DATA
         91
              K05
10W27
         90
              K05
                       2+0 INPUT DATA
10W27
                   A
         95
              K05
                       2 + 5
                           INPUT DATA
10W27
                  23
                       2+4 INPUT DATA
         94
              K05
10W27
                  25
         93
              K05
10W27
                  27
                       2+3 INPUT DATA
         98
              K06
                       2+8 INPUT DATA
10W27
         97
10W27
              K06
                       2+7 INPUT DATA
                   6
         96
10W27
              K06
                       2+6 INPUT DATA
                   8
        901
              K06 23
                       2+11 INPUT DATA
10W27
```

```
10W27
       900
             K06 25
                     2+10 INPUT DATA
10W27
         99
             K06 27
                      2+9 INPUT DATA
10W28
         90
             L01
                      2+0 OUTPUT DATA
         91
                      2+1 OUTPUT DATA
10W28
             L01
                      2+2 OUTPUT DATA
10W28
         92
             L01 10
                      2+3 OUTPUT DATA
         93
10W28
             L01 21
                      2+4 OUTPUT DATA
2+5 OUTPUT DATA
10W28
         94
             L01 24
         95
10W28
             L01 26
         96
                      2+6 OUTPUT DATA
10W28
             L02
                   5
         97
10W28
             L02
                      2+7 OUTPUT DATA
         98
                      2+8 OUTPUT DATA
10W28
             L02 10
             L02 21
10W28
         99
                      2+9 OUTPUT DATA
                      2+10 OUTPUT DATA
10W28
       900
             L02 24
                      2+11 OUTPUT DATA
10W28
             L02 26
       901
                      FUNCTION
10W28
       906
             L03
             L03
                 7
                      ACTIVE
10W28
       902
10W28
       904
             L03 10
                      FULL
                      INACTIVE
10W28
       903
             L03 21
             L03 24
                      MASTER CLEAR
       907
10W28
10W28
       905
             L03 26
                      EMPTY
10W26
                      INACTIVE
       903
             LO7
                   5
                      FULL
10W26
       904
             L07
                   7
                      2+6 INPUT DATA
         96
10W26
             LOB
                   5
         97
             L08
                      2+7 INPUT DATA
                   7
10W26
                      2+8 INPUT DATA
2+9 INPUT DATA
         98
10W26
             L08 10
         99
10W26
             L08 21
10W26
        900
             L08 24
                      2+10 INPUT DATA
10W26
10W26
                      2+11 INPUT DATA 2+0 INPUT DATA
        901
             L08 26
             L09
        91
                      2+1 INPUT DATA
10W26
             L09
                   7
                      2+2 INPUT DATA
10W26
         92
             L09 10
         93
                      2+3 INPUT DATA
             L09 21
10W26
10W26
         94
             L09 24
                      2+4 INPUT DATA
                      2+5 INPUT DATA
        95
             L09 26
10W26
             L26
10W26
       906
                   1
10W27
       906
             L26
       907
10W27
             L26 13
10W26
       907
             L26 15
```

		(
		(
		*
		Q
		4
		(
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```
12W03
         90
             A01
                      2+0 OUTPUT DATA
                  1
12W01
         90
             A01
                      2+0 OUTPUT DATA
                  3
                      2+1 OUTPUT DATA
12W01
         91
             A01 13
                      2+2 OUTPUT DATA
12W03
         92
             A01:14
                      2+1 OUTPUT DATA
12W03
         91
             A01 15
         92
12W01
             A01 16
12W01
         93
             A01 26
                      2+3 OUTPUT DATA
         93
                      2+3 OUTPUT DATA
12W03
             A01 28
12W03
         94
             A02
                      2+4 OUTPUT DATA
         94
12W01
             A 0 2
                  3
                      2+4 OUTPUT DATA
                      2+5 OUTPUT DATA
         95
12W01
             A02 13
12W03
         96
             A02 14
                      2+6 OUTPUT DATA
         95
                      2+5 OUTPUT DATA
12W03
             A02 15
                      2+6 OUTPUT DATA
2+7 OUTPUT DATA
             A02 16
         96
12W01
         97
             A02 26
12W01
         97
                      2+7 OUTPUT DATA
12W03
             A02 28
             A03
         98
12W03
                      2+8 OUTPUT DATA
                  1
12W01
         98
             AO3
                      2+8 OUTPUT DATA
                  3
         99
                      2+9 OUTPUT DATA
12W01
             A03 13
12W03
        900
             A03 14
                      2+10 OUTPUT DATA
12W03
         99
             A03 15
                      2+9 OUTPUT DATA
12W01
                     2+10 OUTPUT DATA
       900
             A03 16
                     2+11 OUTPUT DATA
12W01
       901
             A03 26
                     2+11 OUTPUT DATA
12W03
        901
             A03 28
12W03
       902
             801
                     ACTIVE
12W01
       902
             801
                      ACTIVE
                  3
12W01
        903
             B01 13
                      INACTIVE
12W03
        904
             B01 14
                      FULL
12W03
        903
             B01 15
                      INACTIVE
       904
                      FULL
12W01
             B01 16
12W01
       905
             B01 26
                      EMPTY
                      EMPTY
        905
12W03
             B01 28
                      FUNCTION
12W03
       906
             802
                  1
                      FUNCTION
12W01
       906
             B02
                      MASTER CLEAR
       907
             B02 13
12W01
                      MASTER CLEAR
12W03
        907
             B02 15
                      CLOCK (100NSEC)
12W02
       906
             D01
                 12
12W02
                      RESYNC (1US)
        907
             D03 12
12W04
        907
             D07
             007
12W04
        906
                   7
12W02
         98
             E01
                      2+8 INPUT DATA
                   3
                      2+7 INPUT DATA
12W02
         97
             E01
                   5
                      2+6 INPUT DATA
         96
                   7
12W02
             E01
                      2+11 INPUT DATA
12W04
        901
             E01
12W04
                      2+10 INPUT DATA
        900
             E01
                  - 0
12W04
         99
             E01 10
                      249 INPUT DATA
                      2+8 INPUT DATA
         98
12W04
             E01 21
         97
                      2+7 INPUT DATA
12W04
             E01 22
12W04
         96
             E01 23
                      2+6 INPUT DATA
                      2+11 INPUT DATA
12W02
        901
             E01 24
                      2+10 INPUT DATA
12W02
        900
             E01 26
                      249 INPUT DATA
         99
12W02
             E01 28
             E02
12W02
         92
                      2+2 INPUT DATA
                  3
         91
             E02
                      241 INPUT DATA
12W02
                      200 INPUT DATA
12W02
         90
             E02
12W04
         95
             E02
                   8
                      2+5
                          INPUT DATA
                          INPUT DATA
12W04
         94
             E 0 2
                   9
                      2 . 4
12W04
         93
             E02 10
                          INPUT DATA
                      2+3
12W04
         92
             E02 21
                      242 INPUT DATA
         91
             E02 22
                      2+1 INPUT DATA
12W04
```

```
90
12W04
                        2+0 INPUT DATA
              E02 23
          95
12W02
                        2+5 INPUT DATA
              E02 24
12W02
          94
              E02 26
                        2+4 INPUT DATA
12W02
          93
              E02 28
                        2+3 INPUT DATA
12W02
         904
              E03
                    .3
                        FULL
        903
                        INACTIVE
12W02
              E03
                    5
12W02
         902
              E03
                    7
                        ACTIVE
12W04
         905
              E03 10
                        EMPTY
12W04
        904
              E03 21
                        FULL
              E03 22
12W04
        903
                        INACTIVE
              E03 23
12W04
        902
                        ACTIVE
        905
                        EMPTY
12W02
              E03 28
              E34 14
E34 20
                        UNBLANK LEFT
UNBLANK RIGHT
         98
12W08
         99
12W08
                        UNBLANK LEFT CONSOLE OUNBLANK RIGHT CONSOLE O
         98
              E35 14
12W06
              E35 20
12W06
         99
              F 05
12W06
         90
                        KEYBOARD BIT O
                    1
              F 05
12W06
         91
                        KEYBOARD BIT 1
         92
              F 05
12W06
                        KEYBOARD BIT 2
                   - 5
              F05 12
                        KEY DOWN
12W06
         97
         96
              F05
12W06
                  19
              F 0 5
         93
12W06
                  24
                        KEYBOARD BIT 3
12W06
         94
              F05 25
                        KEYBOARD BIT 4
         95
              F05 28
                        KEYBOARD BIT 5
12W06
12W08
         90
              F06
                        KEYBOARD INPUT BIT 0
                    1
                        KEYBOARD INPUT BIT 1
KEYBOARD INPUT BIT 2
12W08
         91
              F06
         92
              F06
12W08
                    5
12W08
         97
              F06 12
                        KEY UP
         96
              F06 19
12W08
                        KEY DOWN
         93
12W08
              F06 24
                        KEYBOARD INPÚT BIT 3
12W08
         94
              F06 25
                        KEYBOARD INPUT BIT
         95
12W08
              F06 28
                        KEYBOARD INPUT BIT 5
              F07 25
                        FOCUS AND ASTIGMATISM
12W05
        908
              F07 27
12W07
        908
                        FOCUS AND ASTIGMATISM
        900
              F13 10
12W06
                        SMALL SIZE
12W06
        901
              F13
                        MED. SIZE
                  11
                        SMALL SIZE
MED. SIZE
12W08
        900
              F13
                  12
              F13
12W08
        901
                  13
              F16
                        X + 0
12W05
         90
                    1
         91
              F16
                        X+1
12W05
                    2
12W07
         90
              F16
                    3
                        X + 0
         91
12W07
              F16
                    6
                        X+1
         92
12W07
              F16 10
                        X+2
12W05
         92
              F16
                        X+2
                  14
         99
              F16 17
12W07
                        Y # 0
12W05
         99
              F16 19
                        Y + 0
              F16 25
12W07
        900
                        Y+1
        901
                  26
12W07
              F16
                        Y + 2
12W05
        900
              F16 27
                        Y+1
12W05
        901
              F16 28
                        Y * 2
12W07
         93
              F19
                    4
                        X + 3
         95
12W07
              F19
                    7
                        X+5
         95
              F19
12W05
                        X+5
                    8
12W05
         93
              F19
                        X+3
                  10
              F19
         94
12W05
                        X+4
                  11
         94
              F19
12W07
                   13
                        X + 4
              F19
         98
12W07
                   15
                        X+8
         96
              F19
                        X+6
12W07
                   17
         97
              F19
12W05
                  18
                        X + 7
         96
              F19
12W05
                   19
                        X+6
         97
              F19 20
                        X#7
12W07
12W05
         98
              F19
                        X+8
```

```
902
12W05
             F22
                       Y+3
12W05
        903
              F22
                       Y+4
                   2
12W07
        902
              F22
                       Y * 3
12W07
        903
              F22
                       Y + 4
                    6
12W07
        904
              F22 10
                       Y+5
              F22 14
12W05
        904
                       Y+5
12W07
        905
              F22 17
                       Y + 6
              F22 19
12W05
        905
                       Y + 6
12W07
        906
              F22 25
                       Y * 7
12W07
        907
              F22 26
                       Y +8
12W05
        906
              F22 27
                       Y+7
              F22 28
12W05
        907
                       Y + 8
                       HORIZ. PULL ANALOG
HORIZ. PUSH ANALOG
              F26 13
12W08
        906
12W08
        905
              F26
                  14
                       VERT. PULL ANALOG
        904
              F26 15
12W08
              F26 16
12W08
        903
                       VERT. PUSH ANALOG
                       HORIZ. PULL ANALOG
HORIZ. PUSH ANALOG
12W06
              F27 13
        906
              F27 14
12W06
        905
                       VERT. PULL ANALOG
              F27
12W06
        904
                  15
              F27 16
                       VERT. PUSH ANALOG
12W06
        903
         90
                       2+0 OUTPUT DATA
12W16
              G01
                  1
                       2+0 OUTPUT DATA
         90
12W14
              G01
                  3
                       2+1 OUTPUT DATA
12W14
         91
              G01 13
                       2+2 OUTPUT DATA
12W16
         92
              G01 14
             G01 15
                       2+1 OUTPUT DATA
12W16
         91
12W14
              G01 16
                       2+2 OUTPUT DATA
         92
                       2+3 OUTPUT DATA
         93
12W14
              G01 26
12W16
         93
                       2+3 OUTPUT DATA
              G01 28
                       2+4 OUTPUT DATA
2+4 OUTPUT DATA
12W16
         94
              G 0 2
                   1
         94
12W14
              G 0 2
                   3
12W14
         95
             G02 13
                       2+5 OUTPUT DATA
12W16
         96
              G02 14
                       2+5 OUTPUT DATA
         95
              G02 15
12W16
                       2+6 OUTPUT DATA
12W14
         96
              G02 16
                       2+7 OUTPUT DATA
         97
              G02 26
12W14
                       2+7 OUTPUT DATA
12W16
         97
              G02 28
                       2+8 OUTPUT DATA
         98
12W16
              G03
                   1
                       2+8 OUTPUT DATA
12W14
         98
              G03
                   3
12W14
         99
              G03 13
                       2+9 OUTPUT DATA
12W16
              G03 14
                       2+10 OUTPUT DATA
        900
                       2+9 OUTPUT DATA
12W16
         99
              G03 15
              G03 16
                       2+10 OUTPUT DATA
12W14
        900
                       2+11 OUTPUT DATA
2+11 OUTPUT DATA
              G03 26
G03 28
12W14
        901
12W16
        901
12W16
                       ACTIVE
        902
             H01
                  1
              H01
                       ACTIVE
12W14
        902
12W14
                       INACTIVE
        903
              H01 13
              H01 14
                       FULL
12W16
        904
             H01 15
H01 16
12W16
        903
                       INACTIVE
                       FULL
12W14
        904
12W14
        905
              H01 26
                       EMPTY
                       EMPTY
12W16
        905
              H01 28
12W16
        906
              H02
                       FUNCTION
              H02
                       FUNCTION
12W14
        906
                   3
12W14
        907
              H02 13
                       MASTER CLEAR
12W16
        907
              H02 15
                       MASTER CLEAR
                       CLOCK (100US)
12W15
        906
              J01 12
              J03 12
12W15
        907
                       RESYNC (1US)
        907
12W17
              J07
              J07
12W17
        906
         98
12W15
              K01
                       2+8 INPUT DATA
                   3
         97
12W15
              K01
                   5
                       2+7 INPUT DATA
```

```
96
12W15
              K01
                    7
                       2*6 INPUT DATA
12W17
         901
              K01
                       2+11 INPUT DATA
                    8
12W17
                       2+10 INPUT DATA
         900
              K01
                    9
12W17
         99
              K01 10
                       2#9 INPUT DATA
12W17
          98
              K01
                       2+8 INPUT DATA
                   21
          97
12W17
              K01 22
                       2+7 INPUT DATA
         96
12W17
                       2+6 INPUT DATA
              K01 23
                       2+11 INPUT DATA
12W15
        901
              K01 24
12W15
        900
              K01
                  26
                       2+10 INPUT DATA
         99
12W15
                       2+9 INPUT DATA
              K01 28
12W15
         92
                       2+2 INPUT DATA
              K02
                   3
         91
12W15
              K02
                    5
                       241 INPUT DATA
12W15
          90
              K02
                    7
                       2+0 INPUT DATA
12W17
          95
              K02
                    8
                       2+5
                            INPUT DATA
12W17
         94
                    9
                       2 * 4
                            INPUT DATA
              K<sub>0</sub>2
         93
12W17
              K<sub>0</sub>2 10
                       2+3 INPUT DATA
         92
12W17
                       2+2 INPUT DATA
              K02 21
12W17
         91
              K02 22
                       2+1 INPUT DATA
         90
12W17
              K02 23
                       2+0 INPUT DATA
         95
12W15
              K02 24
                       245 INPUT DATA
         94
12W15
              Kn2 26
                       2+4 INPUT DATA
12W15
         93
              K02 28
                       2#3 INPUT DATA
12W15
        904
              K03
                       FULL
                  3
12W15
        903
              K<sub>0</sub>3
                   5
                       INACTIVE
12W15
        902
              K03
                   7
                       ACTIVE
        905
              K03 10
12W17
                       EMPTY
12W17
        904
              K03 21
                       FULL
        903
12W17
              K03 22
                       INACTIVE
12W17
        902
              K03 23
                       ACTIVE
12W15
        905
             K03 28
                       EMPTY
             K34 14
12W21
         98
                      UNBLANK LEFT
         99
              K34 20
                       UNBLANK RIGHT
12W21
12W19
         98
              K35
                       UNBLANK LEFT
12W19
         99
              K35
                       UNBLANK RIGHT
                  20
              L05
12W19
         90
                       KEYBOARD BIT O
         91
              L05
12W19
                       KEYBOARD BIT 1
              L05
12W19
         92
                       KEYBOARD BIT 2
                   5
         97
              L05 12
12W19
                       KEY UP
         96
              L05
                       KEY DOWN
12W19
                  19
12W19
         93
              L05
                  24
                       KEYBOARD BIT
12W19
              L05
         94
                       KEYROARD BIT
                  25
              L05 28
12W19
         95
                       KEYBOARD BIT 5
         90
12W21
                       KEYBOARD BIT O
              L06
                   1
         91
12W21
              L06
                       KEYBOARD BIT 1
12W21
         92
              L06
                   5
                       KEYBOARD BIT 2
         97
12W21
              L06 12
                       KEY UP
         96
                       KEY DOWN
12W21
              L06
                  19
         93
              L06 24
12W21
                       KEYBOARD BIT 3
         94
              L06 25
12W21
                       KEYBOARD BIT 4
12W21
         95
                       KEYBOARD BIT 5
              L06 28
12W18
        908
              L07 25
                       FOCUS AND ASTIGMATISM
12W20
              L07 27
        908
                       FOCUS AND ASTIGMATISM
12W19
        900
              L13 10
                       SMALL SIZE
12W19
              L13 11
        901
                       MED. SIZE
12W21
        900
              L13 12
                       SMALL SIZE
12W21
        901
              L13
                       MED. SIZE
                  13
12W18
         90
                       X + 0
              L16
12W18
         91
              L16
                   2
                       X+1
12W20
         90
              L16
                       X + 0
                   3
12W20
         91
              L16
                       X +1
                   6
         92
12W20
              L16
                       X+2
                  10
         92
12W18
              L16
                  14
                       X * 2
```

```
99
12W20
             L16 17
                        Y + 0
         99
12W18
              L16 19
                        Y + 0
12W20
        900
              L16 25
                        Y+1
12W20
        901
              L16 26
                        Y+2
12W18
        900
              L16 27
                        Y+1
                        Y+2
              L16 28
12W18
        901
12W20
         93
              L19
                        X+3
12W20
         95
              L19
                    7
                        X+5
         95
              L19
                        X+5
12W18
                    8
         93
             L19 10
                        X+3
12W18
         94
              L19 11
12W18
                        X + 4
         94
              L19 13
12W20
                        X +4
12W20
              L19 15
L19 17
          98
                        X + 8
         96
                        X + 6
12W20
              L19 18
12W18
         97
                        X+7
              L19 19
L19 20
          96
12W18
                        X+6
          97
                        X+7
12W20
12W18
         98
              L19 21
                        X+8
12W18
12W18
        902
903
              L22
                        Y+3
              L22
                        Y+4
              L22
                        Y+3
12W20
        902
              L22
12W20
        903
                        Y+4
12W20
        904
              L22 10
                        Y+5
                        Y+5
12W18
        904
              L22 14
        905
                        Y+6
              L22 17
12W20
              L22 19
                        Y * 6
12W18
        905
                        Y + 7
12W20
        906
              L22 25
12W20
        907
              L22 26
                        Y+8
12W18
        906
              L22 27
                        Y+7
12W18
        907
              L22 28
                        Y + 8
                        HORIZ. PULL ANALOG
              L26 13
12W21
        906
                        HORIZ. PUSH ANALOG
VERT. PULL ANALOG
12W21
        905
              L26 14
              L26 15
        904
12W21
              L26 16
L27 13
L27 14
                        VERT. PUSH ANALOG
HORIZ. PULL ANALOG
12W21
        903
12W19
        906
                        HORIZ. PUSH ANALOG
12W19
        905
              L27 15
                        VERT. PULL ANALOG
12W19
        904
                        VERT. PUSH ANALOG
        903
              L27 16
12W19
```

```
905
             C14 14
                      MEMORY MARGIN
SHID
        900
             G35 12
                      CLOCK
13w18
                      2+3 CHASSIS 13 SELECT (100)
2+2 CHASSIS 13 SELECT (100)
             G38
13W18
        904
3W18
        903
             G38
                   Q
                       2+4 CHASSIS 13 SELECT (100)
13W18
        905
             G38
                  13
13W07
         98
             G38
                 20
                       MEMORY WRITE
         97
13W07
             G38 21
                      MEMORY GO
             G39 18
13W18
                      2+0 BANK SELECT
        901
13W18
        902
             G39 22
                      2+1 BANK SELECT
             G39 28
        97
83W13
             G40 11
                      2+5 ADDRESS TO MEMORY
43W18
        906
                      2+6 ADDRESS TO MEMORY
53W18
        907
             G40 14
        908
             G40 17
                      2+7 ADDRESS TO MEMORY
13W18
                      2+8 ADDRESS TO MEMORY
2+9 ADDRESS TO MEMORY
4 3W18
         90
             G40 18
13W18
         91
             G41 11
                      2+10 ADDRESS TO MEMORY
13W18
         92
             G41 14
             G41 17
                      2+11 ADDRESS TO MEMORY
13W18
         93
                      2+12 ADDRESS TO MEMORY
         94
13W18
             G41:18
13W18
         95
             G42 11
                      2+13 ADDRESS TO MEMORY
                      2+14 ADDRESS TO MEMORY
             G42 14
13W18
         96
         97
                       2+15 ADDRESS TO MEMORY
13W18
             G42 17
         98
                      2+16 ADDRESS TO MEMORY
13W18
             G42 18
        900
             H19
13W06
                      2+0
13W39
        900
             H19
                      2+0 FAN OUT TO MEMORY
13W06
        901
             H19
                      2+1
       901
             H19 19
                      2+1 FAN OUT TO MEMORY
13W39
             H19 24
                       2+2 FAN OUT TO MEMORY
13W39
        902
             H19 28
13W06
        902
                      2 * 2
             H20
        903
13W06
                      2 * 3
13W39
        903
             H20
                       2+3 FAN OUT TO MEMORY
                   2
        904
13W06
             H20
                   8
                      2 + 4
                      2+4 FAN OUT TO MEMORY
18W39
        904
             H20 19
                       2+5 FAN OUT TO MEMORY
13W39
        905
             H20 24
        905
             H20 28
                      2+5
13W06
13W06
        906
             H21
                       2+6
                   1
                      2+6 FAN OUT TO MEMORY
13W39
        906
             H21
13W06
        907
             H21
                      2+7
                      2+7 FAN OUT TO MEMORY
13W39
        907
             H21 19
13W39
         90
                      2+8 FAN OUT TO MEMORY
             H21 24
13W06
         90
             H21 28
                       2+8
13W06
         91
             H22
                       2+9
                   1
         91
                      2+9 FAN OUT TO MEMORY
             H22
13W39
         92
13W06
             H22
                       2.10
             H22 19
         92
                       2+10 FAN OUT TO MEMORY
13W39
                       2+11 FAN OUT TO MEMORY
         93
             H22:
13W39
                  24
         93
             H22
                      2+11
13W06
                  28
         94
             H24
                       2+12
15W06
                   1
13W39
         94
             H24
                       2+12 FAN OUT TO MEMORY
             H24
         95
                      2+13
13W06
                       2+13 FAN OUT TO MEMORY
13W39
         95
             H24 19
                       2+14 FAN OUT TO MEMORY
13W39
         96
             H24
                  24
         96
             H24
                      2+14
13W06
                  28
             H25
13W07
        900
                       2+15
             H25
                       2+15 FAN OUT TO MEMORY
13W03
        900
13W07
        901
             H25
                       2+16
             H25
                 19
                       2+16 FAN OUT TO MEMORY
13W03
        901
             H25
        902
                       2+17 FAN OUT TO MEMORY
13W03
                  24
        902
             H25
                  28
                       2+17
13W07
13W07
        903
             H26
                       2+18
                   1
             H26
        903
                       2+18 FAN OUT TO MEMORY
13W03
                   2
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2+19
13W07
        904
             H26 8
13W03
        904
              H26 19
                       2+19 FAN OUT TO MEMORY
        905
                       2+20 FAN OUT TO MEMORY
13W03
              H26
                  24
13W07
        905
             H26
                       2+20
                  28
             H27
13W07
        906
                       2 + 21
                       2+21 FAN OUT TO MEMORY
13W03
        906
              H27
                   2
              H27
13W07
        907
                   8
                       2+22
             H27
        907
                       2+22 FAN OUT TO MEMORY
13W03
                  19
                       2+23 FAN OUT TO MEMORY
         90
              H27
                  24
13W03
         90
              H27
13W07
                  28
                       2+23
         91
             H29
13W07
                       2+24
13W03
         91
              H29
                       2+24 FAN OUT TO MEMORY
13W07
         92
              H29
                       2+25
         92
             H29
                       2+25 FAN OUT TO MEMORY
13W03
                  19
                       2+26 FAN OUT TO MEMORY
13W03
         93
             H29
                  24
13W07
         93
              H29
                  28
                       2+26
         94
                       2+27
13W07
              H30
                       2+27 FAN OUT TO MEMORY
13W03
         94
              H30
                   2
         95
13W07
              H30
                   8
                       2+28
13W03
         95
              H30
                  19
                       2+28 FAN OUT TO MEMORY
                       2+29 FAN OUT TO MEMORY
13W03
         96
              H30
                  24
         96
13W07
              H30
                  28
                       2+29
13W08
        900
              H31
                       2+30
                   1
              H31
                       2+30 FAN OUT TO MEMORY
13W04
        900
                   2
             H31
13W08
        901
                       2+31
                   8
13W04
        901
              H31 19
                       2+31 FAN OUT TO MEMORY
13W04
        902
              H31 24
                       2+32 FAN OUT TO MEMORY
13W08
        902
                       2+32
              H31
                  28
              H32
13W08
        903
                       2+33
13W04
        903
              H32
                   2
                       2+33 FAN OUT TO MEMORY
13W08
        904
              H32
                       2+34
                       2+34 FAN OUT TO MEMORY
        904
13W04
              H32 19
13W04
        905
              H32
                  24
                       2+35 FAN OUT TO MEMORY
        905
                       2+35
13W08
              H32
                  28
13W08
                       2+36
        906
              H34
                   1
                       2+36 FAN OUT TO MEMORY
13W04
        906
              H34
             H34
        907
                       2+37
13W08
                   8
                       2+37 FAN OUT TO MEMORY
2+38 FAN OUT TO MEMORY
13W04
        907
              H34
                  19
13W04
         90
              H34
                  24
                       2+38
13W08
         90
              H34
                  28
         91
              H35
13W08
                       2+39
                   1
         91
                      2+39 FAN OUT TO MEMORY
             H35
13W04
                       2 + 40
13W08
         92
              H35
                   8
13W04
         92
              H35
                  19
                       2+40 FAN OUT TO MEMORY
             H35
         93
                  24
                       2441 FAN OUT TO MEMORY
13W04
13W08
         93
              H35
                       2+41
         94
                       2 * 42
13W08
              H36
         94
              H36
                   2
                       2+42 FAN OUT TO MEMORY
13W04
13W08
         95
              H36
                       2 + 43
13W04
         95
              H36
                 19
                       2+43 FAN OUT TO MEMORY
                  24
13W04
         96
              H36
                       2444 FAN OUT TO MEMORY
              H36 28
         96
                       2 + 4 4
13W08
13W09
        900
              H37
                       2+45
                   1
              H37
13W05
        900
                       2+45 FAN OUT TO MEMORY
        901
              H37
                       2 + 46
13W09
                   8
              H37 19
                       2+46 FAN OUT TO MEMORY
13W05
        901
              H37 24
                       2+47 FAN OUT TO MEMORY
13W05
        902
             H37
13W09
        902
                  28
                       2 + 47
13W09
        903
              H39
                   1
                       2+48
             H39
                       2+48 FAN OUT TO MEMORY
13W05
        903
                   2
13W09
        904
              H39
                       2+49
                   8
13W05
        904
              H39 19
                       2#49 FAN OUT TO MEMORY
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13W05
       905
             H39 24
                      2+50 FAN OUT TO MEMORY
             H39 28
13W09
        905
                      2+50
13W09
        906
             H40
                      2+51
13W05
        906
             H40
                      2+51 FAN OUT TO MEMORY
                   2
13W09
        907
             H40
                   8
                      2+52
13W05
        907
                      2+52 FAN OUT TO MEMORY
             H40
                 19
        90
13W05
                      2+93 FAN OUT TO MEMORY
             H40
                 24
13W09
         90
             H40
                  28
                      2*53
13W09
         91
             H41
                      2+54
                      2+54 FAN OUT TO MEMORY
13W05
         91
             H41
        92
13W09
             H41
                      2+55
                      2+55 FAN OUT TO MEMORY
13W05
         92
             H41 19
         93
             H41 24
13W05
                      2+56 FAN OUT TO MEMORY
13W09
         93
             H41 28
                      2+56
         94
13W09
             H42
                      2+57
13W05
         94
             H42
                      2+57 FAN OUT TO MEMORY
                   2
13W09
         95
             H42
                      2+58
         95
             H42 19
                      2+58 FAN OUT TO MEMORY
13W05
         96
             H42 24
                      2+59 FAN OUT TO MEMORY
13W05
             H42
128
13W09
         96
                      2+59
                 28
                      2+0 PAN OUT TO MEMORY
13W15
       900
13W11
       900
             128
                      2+0 FAN OUT TO MEMORY
                      2+1 FAN OUT TO MEMORY
             128
13W14
       901
                   7
13W39
       901
             128
                   9
                      2+1 FAN OUT TO MEMORY
13W12
       901
             128
                      2+1 FAN OUT
                                   TO MEMORY
                 11
                          FAN OUT TO MEMORY
             128
                      2+1
13W13
       901
                 13
13W13
       900
             128
                      2+0
                          FAN OUT TO MEMORY
                 18
             128 20
                          FAN OUT TO MEMORY
13W12
       900
                      2+0
13W39
       900
             128 22
                      2+0
                          FAN
                               OUT TO MEMORY
13W14
       900
             128
                 24
                      5 + 0
                          FAN
                               OUT
                                   TO
                                      MEMORY
             128
                      2+0
                          FAN OUT
                                   TO MEMORY
13W16
       900
                 26
13W17
             128
                 28
       900
                      2+0 FAN OUT TO MEMORY
             129
                      2+2 FAN OUT TO MEMORY
13W13
       902
                   3
13W12
                                   TO MEMORY
       902
             129
                   5
                      2+2 FAN OUT
13W17
       902
             129
                   7
                      2+2 FAN
                               OUT
                                   TO MEMORY
             129
                      2+2 FAN OUT
                                   TO MEMORY
13W16
       902
                   9
13W11
             129 11
                      2+2 FAN OUT
       902
                                   TO MEMORY
             129 13
                      2+2 FAN OUT TO MEMORY
13W15
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13W15
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                      2+1 FAN
                               OUT TO
                                      MEMORY
                 18
                          FAN
13W11
       901
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                 20
                      2+1
                               OUT TO
                                       MEMORY
             129
                      2+1 FAN
                               OUT TO MEMORY
13W16
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                      2+1 FAN OUT TO
13W17
       901
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             129 26
                      2+2 FAN
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13W14
             129 28
                      2+2
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             130
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                                   ŤO
                               OUT
13W11
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             130
                      2+3
                          FAN
                                   TO MEMORY
                   5
                          FAN
                               QUT
                                   TO MEMORY
13W14
       904
             130
                   7
                      2+4
                          FAN
13W39
       904
                   9
                               OUT TO MEMORY
             130
                      2+4
       904
                      2+4
                          FAN OUT TO MEMORY
13W12
             130 11
13W13
             130 13
       904
                      2+4
                          FAN
                               OUT TO MEMORY
13W13
       903
             130
                      5 * 3
                          FAN
                               OUT
                                   TO MEMORY
                 18
                          FAN
                               OUT TO MEMORY
13W12
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                 20
                      2+3
       903
             130 22
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                               OUT TO
13W39
                      2+3
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                          FAN OUT TO MEMORY
13W14
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             130 24
                      2 * 3
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       903
                      2+3
                          FAN
                               OUT TO MEMORY
             130 26
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                          FAN
                               OUT
                                      MEMORY
13W17
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                      2+3
                                   ŤΟ
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13W13
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                      2+5
                          FAN
                               OUT
                                   ŤΟ
                                      MEMORY
                          FAN
13W17
       905
             131
                      2+5
                               OUT
                                   TO
                                      MEMORY
13W16
       905
             131
                   9
                      2+5
                          FAN OUT TO MEMORY
       905
                          FAN
13W11
             131
                 11
                      2+5
                               OUT TO
                                      MEMORY
       905
                          FAN
13W15
             131
                 13
                      2+5
                               OUT
                                   TO
                                      MEMORY
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13W15
        904
             131 18
                      2+4 FAN OUT TO MEMORY
13W11
                      2+4 FAN OUT
        904
             131 20
                                    TO
13W16
        904
             131
                      2+4 FAN OUT
                                    TO
                                       MEMORY
                  22
13W17
                      2+4 FAN OUT
        904
             131
                  24
                                    ŤO
                                       MEMORY
                      2+5 FAN OUT
13W39
        905
             131
                  26
                                    TO
                                       MEMORY
                  28
                      2+5 FAN OUT
13W14
        905
                                    ŤΟ
                                       MEMORY
             131
13W15
             132
                      2+6 FAN OUT
                                    TO
        906
                   3
                      2+6 FAN OUT
                                   TO MEMORY
13W11
        906
              132
                   5
                      2+7 FAN
                               QUT
                                       MEMORY
13W14
        907
             132
                   7
                                    TO
                      2+7 FAN
                               OUT
                                    TO MEMORY
13W39
        907
             132
                   9
             132
                      2+7 FAN OUT
        907
                                    TO MEMORY
13W12
                  11
13W13
                      2+7 FAN OUT
                                       MEMORY
        907
             132
                  13
                                   TO
                      2+6 FAN OUT
13W13
        906
             132
                                    TO
                                       MEMORY
                  18
13W12
                      2+6 FAN OUT
        906
             132
                  20
                                    TO
                                       MEMORY
                      2+6 FAN OUT
                                       MEMORY
13W39
        906
             132
                  22
                                    TO
                      2+6 FAN OUT
13W14
        906
             132
                  24
                                    TO MEMORY
                      2+6 FAN
                                       MEMORY
13W16
        906
             132
                  26
                               OUT
                                   10
                      2+6 FAN OUT
                                   TO MEMORY
        906
             132 28
13W17
13W13
                      2+8 FAN OUT
         90
             133
                                    TO MEMORY
                   3
13W12
                          FAN
                                       MEMORY
         90
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                      2+8
                               QUT
                                    ŤΟ
                   5
         90
                      2+8 FAN OUT
                                       MEMORY
                   7
13W17
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                                    TO
13W16
         90
                      2+8 FAN OUT
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                   9
                                   TO
         90
                      2+8 FAN OUT
                                   TO MEMORY
13W11
             133
13W15
         90
             133
                      2+8 FAN OUT
                                    TO
                                       MEMORY
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13W15
        907
             133
                  18
                      2+7 FAN
                               QUT
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                                       MEMORY
        907
              133
                      2+7 FAN
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                                    TO
                                       MEMORY
13W11
13W16
        907
             133
                 22
                      2+7 FAN OUT
                                    TO MEMORY
13W17
        907
                      2+7 FAN OUT TO MEMORY
              133
                  24
             133
13W39
         90
                      2+8 FAN OUT TO MEMORY
                  26
         90
             133
                      2+8 FAN OUT
                                    TO MEMORY
13W14
                  28
                      2+9 FAN OUT
                                    TO MEMORY
13W15
         91
             134
                   3
         91
             134
                      2+9 FAN OUT TO MEMORY
13W11
                   5
                      2+10 FAN OUT TO MEMORY
13W14
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             134
                   7
                      2+10 FAN OUT TO MEMORY
         92
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             134
         92
                      2+10 FAN OUT TO MEMORY
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13W13
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             134
                      2+10 FAN OUT TO MEMORY
                  13
                      2+9 FAN OUT TO MEMORY
         91
             134
13W13
                  18
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             134
                      2+9 FAN OUT TO MEMORY
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                      2+9 FAN OUT TO MEMORY
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13W39
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                  22
         91
             134
                 24
                      2+9 FAN OUT TO MEMORY
13W14
                      2+9 FAN OUT
13W16
         91
             134
                  26
                                    TO MEMORY
         91
             134
                      2+9 FAN OUT TO MEMORY
                  28
13W17
                      2+11 FAN OUT TO MEMORY
13W13
             135
         93
                   3
                      2+11 FAN OUT TO
                                        MEMORY
         93
             135
13W12
                   5
                      2+11 FAN OUT TO
13W17
         93
             135
                   7
                                        MEMORY
13W16
         93
             135
                   9
                      2+11 FAN OUT
                                     TO
                                        MEMORY
         93
             135
                      2*11
                           FAN
                                OUT
                                     TO
                                        MEMORY
13W11
                  11
         93
             135
                      2+11 FAN
13W15
                  13
                                OUT
                                     TO
         92
             135
                      2+10 FAN
                                OUT TO
                                        MEMORY
13W15
                  18
             135
                      2+10 FAN
13W11
         92
                  20
                                     TO
                                        MEMORY
                                OUT
13W16
         92
              135
                  22
                      2+10 FAN
                                 OUT
                                     TO
                                        MEMORY
         92
             135
                  24
                       2+10 FAN
                                OUT
                                     TO MEMORY
13W17
         93
             135
                  26
                       2+11 FAN
                                        MEMORY
13W39
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                                     TO
                       2+11 FAN
                                        MEMORY
         93
              135
13W14
                  28
                                OUT
                                     TO
         94
                       2+12 FAN OUT
                                     TO
                                        MEMORY
13W15
             136
                   3
         94
                                OUT
                                        MEMORY
13W11
             136
                   5
                       2+12 FAN
                                     TO
         95
                                        MEMORY
                   7
                       2+13 FAN
                                     TO
13W14
             136
                                OUT
         95
                                     TO MEMORY
13W39
             136
                       2*13 FAN OUT
         95
13W12
              136 11
                       2+13 FAN OUT
                                     TO
                                    TO MEMORY
13W13
         95
             136 13
                      2#13 FAN OUT
         94
                       2+12 FAN OUT TO MEMORY
13W13
             136
                 18
         94
              136 20
                      2+12 FAN OUT TO MEMORY
13W12
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13W39
             136 22
                      2+12 FAN OUT TO MEMORY
13W14
         94
             136 24
                      2+12 FAN OUT TO MEMORY
13W16
         94
                      2+12 FAN
                               OUT TO MEMORY
             136 26
         94
13W17
                      2+12 FAN
                               OUT TO MEMORY
             136 28
13W13
         96
             137
                      2+14 FAN
                                OUT
                   3
                                    TO MEMORY
         96
13W12
             137
                      2+14 FAN
                   5
                                OUT TO MEMORY
             137
         96
13W17
                   7
                      2+14 FAN
                               OUT TO MEMORY
13W16
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             137
                      2+14 FAN OUT TO MEMORY
                   9
13W11
         96
             137
                      2+14 FAN OUT TO MEMORY
                 11
13W15
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             137
                      2+14 FAN
                 13
                                OUT TO MEMORY
             137
         95
                      2+13 FAN
13W15
                                OUT TO MEMORY
                 18
13W11
         95
             137 20
                      2+13 FAN OUT TO MEMORY
13W16
             137 22
                      2+13 FAN OUT TO MEMORY
         95
13W17
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             137 24
                      2+13 FAN OUT TO MEMORY
13W39
         96
             137
                 26
                      2+14 FAN OUT
                                    TO MEMORY
         96
             137
                      2+14 FAN OUT TO MEMORY
13W14
                 28
13W10
         90
             139
                      2+0 DATA TO MEMORY FAN OUT
         91
             139
                      2+1 DATA TO MEMORY FAN OUT
13W10
                   6
             139
13W10
         92
                      2+2 DATA TO MEMORY FAN OUT
                  7
13W10
         95
             139
                          DATA TO MEMORY FAN OUT
                 22
                      2+5
                          DATA TO MEMORY FAN OUT
         94
             139
13W10
                 25
                      2 + 4
13W10
         93
             139
                      2+3 DATA TO MEMORY FAN OUT
                 27
13W10
         96
             140
                      2+6 DATA TO MEMORY FAN OUT
         97
                      2+7 DATA TO MEMORY FAN OUT
13W10
             140
13W10
        98
             140
                  7
                      2+8 DATA TO MEMORY FAN OUT
                      2+11 DATA TO MEMORY FAN OUT
13W10
       901
             140
                 22
                      2+10 DATA TO MEMORY FAN OUT
13W10
             140
       900
                 25
13W10
         99
                      2+9 DATA TO MEMORY FAN OUT
             140
                 27
13W10
                      2+12 DATA TO MEMORY FAN OUT
       902
             141
13W10
                      2+13 DATA TO MEMORY FAN OUT
       903
             141
                   6
                      2+14 DATA TO MEMORY FAN OUT
13W10
       904
             141
                  7
       907
                      CLOCK (1 M SEC)
13W36
             J31
                   1
13W34
       906
             J31 13
13W36
       906
             J31
                 15
             J33
13W34
       903
                      INACTIVE
                  -3
       905
             J33
                  5
                      EMPTY
13W34
                   7
                      FULL
13W34
       904
             J33
13W34
       907
             J33
                   9
                      CLOCK (1 MS)
13W36
       902
             J33 10
                      ACTIVE
13W36
       903
             J33 21
                      INACTIVE
13W36
       905
             J33 22
                      EMPTY
       904
                      FULL
13W36
             J33
                 23
13W34
       902
             J33 28
                      ACTIVE
13W34
         92
             J35
                      2+2 INPUT DATA
                  3
         91
             J35
                      2+1 INPUT DATA
13W34
                   5
                          INPUT DATA
13W34
         90
             J35
                   7
                      2+0
             J35
         95
                          INPUT DATA
                      2 * 5
13W36
                   8
13W36
         94
             J35
                      2+4 INPUT DATA
                   9
         93
             J35
13W36
                 10
                      2+3 INPUT DATA
             J35 21
         92
                      2+2 INPUT DATA
13W36
13W36
         91
             J35 22
                      2+1 INPUT DATA
         90
             J35 23
13W36
                      2+0
                          INPUT DATA
             J35 24
         95
13W34
                      2+5 INPUT DATA
             J35 26
13W34
         94
                      2#4 INPUT DATA
         93
             J35 28
                      2+3 INPUT DATA
13W34
             J37
                      2+8 INPUT DATA
13W34
         98
13W34
         97
             J37
                  5
                      2+7
                          INPUT DATA
        96
             J37
                  7
                      2+6 INPUT DATA
13W34
13W36
             J37
       901
                      2+11 INPUT DATA
                  8
             J37
13W36
       900
                  9
                      2+10 INPUT DATA
             J37 10
         99
13W36
                      2+9 INPUT DATA
         98
             J37 21
                      2 8 INPUT DATA
13W36
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97
             J37 22
                      2+7 INPUT DATA
13W36
             J37 23
        96
                      2+6 INPUT DATA
13W36
13W34
       901
             J37
                 24
                      2+11 INPUT DATA
             J37
13W34
       900
                  26
                      2+10 INPUT DATA
        99
             J37
                      2+9 INPUT DATA
13W34
                 28
13W37
             J38
                      FUNCTION
       906
                  1
13W35
             J38
                      FUNCTION
       906
                   3
             J38 13
                      ACTIVE
13W35
       902
             J38
                      INACTIVE
13W37
       903
                 14
13W37
       902
             J38
                 15
                      ACTIVE
                      INACTIVE
             J38
13W35
       903
                 16
                      FULL
13W35
       904
             J38
                 26
             J38
                      FULL
13W37
       904
                 28
             J39
       905
                      EMPTY
13W37
                  1
13W35
       905
             J39
                      EMPTY
                  3
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       907
                      MASTER CLEAR
13W35
                 13
             J39
                 15
13W37
       907
                      MASTER CLEAR
                      2+0 OUTPUT DATA
13W37
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             J40
                  1
         90
13W35
             J40
                      2+0 OUTPUT DATA
                   3
                      2+1 OUTPUT DATA
         91
             J40 13
13W35
13W37
        92
             J40
                 14
                      2+2 QUTPUT DATA
        91
                      2+1 OUTPUT DATA
             J40
13W37
                 15
13W35
         92
             J40 16
                      2+2 OUTPUT DATA
         93
                      2+3 OUTPUT DATA
             J40 26
13W35
13W37
         93
             J40 28
                      2+3 OUTPUT DATA
13W37
         94
             J41
                  1
                      2+4 QUTPUT DATA
13W35
         94
             J41
                      2+4 OUTPUT DATA
                   3
        95
13W35
             J41 13
                      2+5 OUTPUT DATA
         96
                      2+6 QUTPUT DATA
13W37
             J41 14
13W37
         95
             J41 15
                      2+5 QUTPUT DATA
                      2+6 OUTPUT DATA
13W35
         96
             J41
                 16
         97
                      2+7 OUTPUT DATA
13W35
             J41 26
13W37
         97
             J41 28
                      2+7 OUTPUT DATA
         98
13W37
             J42
                      2+8 OUTPUT DATA
                      2+8 OUTPUT DATA
         98
13W35
             J42
                  3
        99
13W35
             J42
                 13
                      2+9 OUTPUT DATA
                      2+10 OUTPUT DATA
13W37
       900
             J42
                 14
        99
             J42 15
                      2+9 OUTPUT DATA
13W37
             J42 16
                      2+10 OUTPUT DATA
13W35
       900
13W35
       901
             J42 26
                      2+11 OUTPUT DATA
13W37
       901
             J42 28
                      2+11 OUTPUT DATA
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905
            C14 14 MEMORY MARGIN
14W10
14W18
        906
             G01 11
                      2+5 ADDRESS TO MEMORY
                       2+7 ADDRESS TO MEMORY
2+6 ADDRESS TO MEMORY
             G01 14
14W18
        908
14W18
        907
             G01 17
                      2+8 ADDRESS TO MEMORY
         90
14W18
             G01 18
         91
             G02 11
                       2+9 ADDRESS TO MEMORY
14W18
         92
                       2+10 ADDRESS TO MEMORY
14W18
             G02 14
14W18
             G02 17
         93
                       2+11 ADDRESS TO MEMORY
                      2+12 ADDRESS TO MEMORY
2+13 ADDRESS TO MEMORY
             G02 18
14W18
         94
         95
             G03 11
14W18
                       2+14 ADDRESS TO MEMORY
14W18
         96
             G03 14
         97
                       2+15 ADDRESS TO MEMORY
14W18
             G03 17
         98
             G03 18
                      2+16 ADDRESS TO MEMORY
14W18
                       2+3 CHASSIS 14 SELECT (101)
14W18
        904
             G 0 4
                      2+2 CHASSIS 14 SELECT (101)
2+4 CHASSIS 14 SELECT (101)
14W18
        903
             G 0 4
                  8
14W18
             G04 13
        905
         98
             G04 20
                      WRITE MEMORY
14W07
                      GO MEMORY
         97
             G04 21
14W07
14W18
        901
             G05 18
                       2+0 BANK SELECT
             G05 22
14W18
        902
                       2+1 BANK SELECT
         97
             G05 28
14W13
                       ACCEPT
14W18
        900
             G13 12
                       CLOCK
        900
14W06
             H01
                       2 + 0
14W03
        900
             H01
                       2+0 FAN OUT TO MEMORY
14W06
        901
             H01
                   8
                       2+1
             H01 19
                       2+1 FAN OUT TO MEMORY
14W03
        901
14W03
                       2+2 FAN OUT TO MEMORY
        902
             H01 24
             H01 28
                       2+2
14W06
        902
14W06
        903
             H02
                       2+3
                   1
                       2+3 FAN OUT TO MEMORY
14W03
        903
             H02
                   2
14W06
        904
             H02
                  8
                       2 * 4
                       2+4 FAN OUT TO MEMORY
14W03
        904
             H02 19
        905
                       2+5 FAN OUT TO MEMORY
14W03
             H02 24
14W06
        905
             H02 28
                       2+5
14W06
        906
             H03
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                       2+6
                       2+6 FAN OUT TO MEMORY
14W03
        906
             H03
14W06
        907
              H03
                       2 * 7
                       2+7 FAN OUT TO MEMORY
        907
14W03
             H03 19
14W03
         90
             H03 24
                       2+8 FAN OUT TO MEMORY
14W06
         90
              H03 28
                       2 * 8
         91
             H04
                       2+9
14W06
                       2+9 FAN OUT TO MEMORY
14W03
         91
              H04
         92
              H04
                       2+10
14W06
              H04 19
                       2+10 FAN OUT TO MEMORY
         92
44W03
                       2+11 FAN OUT TO MEMORY
14W03
         93
              H04 24
         93
              H04 28
14W06
                       2+11
14W06
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              H06
                       2+12
                   1
14W03
         94
             H06
                       2+12 FAN OUT TO MEMORY
         95
                       2+13
14W06
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                   8
                       2+13 FAN OUT TO MEMORY
2+14 FAN OUT TO MEMORY
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             H06 28
                       2 + 1 4
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                       2+15
                   1
                       2+15 FAN OUT TO MEMORY
        900
              H07
14W39
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        901
             H07
                       2+16
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                       2+16 FAN OUT TO MEMORY
2+17 FAN OUT TO MEMORY
14W39
        901
              H07
                  19
             H07 24
        902
14W39
             H07
                       2+17
14W07
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                  28
14W07
        903
              H08
                       2+18
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                   2 2+18 FAN OUT TO MEMORY
14W39
       903
             H08
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                      2+19
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                      2+19 FAN OUT TO MEMORY
14W39
       905
             H08 24
                      2+20 FAN OUT TO MEMORY
       905
             H08 28
14W07
                      2+20
                      2+21
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             H09
                      2+21 FAN OUT TO MEMORY
14W39
       906
                  2
             H09
14W07
       907
                   8
                      2+22
14W39
       907
             H09
                      2+22 FAN OUT TO MEMORY
                 19
                      2+23 FAN OUT TO MEMORY
             H09 24
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             H09 28
14W07
                      2+23
14W07
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             H11
                      2+24
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        91
14W39
                      2+24 FAN OUT TO MEMORY
             H11
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14W07
         92
                      2+25
             H11
                   8
        92
                      2+25 FAN OUT TO MEMORY
14W39
             H11
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         93
                      2+26 FAN OUT TO MEMORY
14W39
             H11:24
        93
14W07
             H11
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                      2+26
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14W07
             H12
                      2+27
             H12
14W39
         94
                      2+27 FAN OUT TO MEMORY
                  2
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14W07
             H12
                      2+28
                      2+28 FAN OUT TO MEMORY
2+29 FAN OUT TO MEMORY
14W39
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             H12 19
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                      2+29
                      2+30
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14W04
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             H13
                      2+31
14W08
       901
             H13
                  8
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14W04
       901
             H13
                      2+31 FAN OUT TO MEMORY
             H13
                      2+32 FAN OUT TO MEMORY
14W04
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       902
             H13 28
14W08
                      2+32
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       903
             H14
                      2+33
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                      2+33 FAN OUT TO MEMORY
14W04
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       904
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                      2+34
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                      2+34 FAN OUT TO MEMORY
14W04
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             H14 19
             H14 24
                      2+35 FAN OUT TO MEMORY
14W04
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             H14 28
                      2+35
             H16
                      2+36
14W08
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                      2+36 FAN OUT TO MEMORY
                  2
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                      2+37
       907
                      2+37 FAN OUT TO MEMORY
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                      2+38 FAN OUT TO MEMORY
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             H16 28
                      2+38
        91
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                      2+39 FAN OUT TO MEMORY
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                      2+40
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                      2+40 FAN OUT TO MEMORY
             H17 24
                      2+41 FAN OUT TO MEMORY
14W04
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                      2+41
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             H17 28
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             H18
                      2+42
                   1
                      2+42 FAN OUT TO MEMORY
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14W04
             H18
                   2
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             H18
                      2+43
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             H18
                 19
                      2+43 FAN OUT TO MEMORY
         95
             H18 24
                      2+44 FAN OUT TO MEMORY
14W04
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             H18 28
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                      2+45 FAN OUT TO MEMORY
       900
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                      2+46 FAN OUT TO MEMORY
14W05
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                 24
                      2+47 FAN OUT TO MEMORY
             H19
14W09
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                 28
                      2+47
                      2+48
14W09
       903
             H21
14W05
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             H21
                      2+48 FAN OUT TO MEMORY
14W09
       904
             H21
                  - 8
                      2+49
14W05
       904
             H21 19
                      2+49 FAN OUT TO MEMORY
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             H21 24
14W09
       905
             H21 28
                      2+50
                      2+51
14W09
       906
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             H22
                      2+51 FAN OUT TO MEMORY
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                      2+52 FAN OUT TO MEMORY
14W05
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                      2+53 FAN OUT TO MEMORY
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14W09
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                      2+53
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14W09
         91
             H23
                  1
                      2+54
        90
14W05
             H23
                      2+54 FAN OUT TO MEMORY
         92
14W09
             H23
                  A
                      2+55
        91
                      2+55 FAN OUT TO MEMORY
14W05
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                      2+56 FAN OUT TO MEMORY
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             H23 24
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                      2+56
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                      2+57
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                      2+57 FAN OUT TO MEMORY
                  2
        95
             H24
14W09
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                      2+58
         94
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                      2+58 FAN OUT TO MEMORY
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                 24
                      2+59 FAN OUT TO MEMORY
         96
14W09
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                 28
                      2+59
14W10
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             102
                      2+15 DATA TO MEMORY FAN OUT
                           DATA TO MEMORY FAN OUT
        91
14W10
             102
                      2+16
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                      2+17 DATA TO MEMORY FAN OUT
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14W10
                      2+20 DATA TO MEMORY FAN OUT
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                      2+19 DATA TO MEMORY FAN OUT
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                      2+18 DATA TO MEMORY FAN OUT
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                      2+21 DATA TO MEMORY FAN OUT
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                      2+24 DATA TO
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                                                OUT
                      2*27 DATA TO MEMORY FAN OUT
14W10
             104
       902
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14W10
             104
                      2+28 DATA TO MEMORY FAN OUT
       903
                   6
                                    MEMORY FAN OUT
                      2+29 DATA TO
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14W10
       904
14W14
       900
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                      2+15 FAN OUT
                                    TO MEMORY
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14W15
                      2+15 FAN OUT
                                    TO MEMORY
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             106
                  5
                      2+16 FAN OUT TO MEMORY
14W13
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                  7
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                  9
                      2+16 FAN OUT TO MEMORY
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                      2+16 FAN OUT TO MEMORY
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                      2+15 FAN OUT
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                      2+18 FAN OUT TO MEMORY
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                      2+19 FAN OUT TO MEMORY
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                            FAN
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                                 OUT TO MEMORY
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                      2+2 CHASSIS 15 SELECT (110)
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             G38
15W18
        905
             G38 13
                      2+4 CHASSIS 15 SELECT (110)
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             G38 20
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                      2+1 BANK SELECT
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15W04
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15W07
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15W39
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15W09
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        93
             136 26
                      2+42 FAN OUT TO MEMORY
15W17
                      2+42 FAN OUT TO MEMORY
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                      2+44 FAN OUT TO MEMORY
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        96
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             137
                      2+44 FAN OUT TO MEMORY
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             137
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                      2+44 FAN OUT
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15W15
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        96
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                      2+43 FAN OUT TO MEMORY
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                      2+44 FAN OUT TO MEMORY
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                      2+32 DATA TO MEMORY FAN OUT
             139
                      2+35 DATA TO MEMORY FAN OUT
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15W10
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                      2+34 DATA TO MEMORY FAN OUT
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        94
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                      2+36 DATA TO MEMORY FAN OUT
15W10
        96
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                                    MEMORY FAN OUT
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15W10
             140
                      2+38 DATA TO MEMORY FAN OUT
15W10
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             140
15W10
                                    MEMORY FAN OUT
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                      2+41 DATA TO
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                                    MEMORY FAN OUT
                      2+40 DATA TO
15W10
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                 25
       900
15W10
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                                   MEMORY FAN OUT
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                                    MEMORY FAN OUT
                      2+42
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15W10
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15W10
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                      2+44 DATA TO MEMORY FAN OUT
15W10
       904
             141
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       905
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16W10
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             G01 11
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             G01 14
                       2+6 ADDRESS TO MEMORY
                       2+7 ADDRESS TO MEMORY
16W18
        908
             G01 17
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                      2+8 ADDRESS TO MEMORY
16W18
             G01 18
16W18
         91
             G02 11
                      2+9 ADDRESS TO MEMORY
         92
                      2+10 ADDRESS TO MEMORY
16W18
             G02 14
                      2+11 ADDRESS TO MEMORY
16W18
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             G02 17
                      2+12 ADDRESS TO MEMORY
2+13 ADDRESS TO MEMORY
             G02 18
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             G03 11
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                      2+14 ADDRESS TO MEMORY
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             G03 14
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                      2+15 ADDRESS TO MEMORY
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             G03 17
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             G03 18
                      2+16 ADDRESS TO MEMORY
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                      2+3 CHASSIS 16 SELECT (111)
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        904
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        903
                  8
             G04 13
                       2+4 CHASSIS 16 SELECT (111)
        905
16W18
16W12
        98
             G04 20
                      WRITE
16W12
         99
             G04 21
                      GO
16W18
        901
             G05 18
                      2+0 BANK SELECT
                      2+1 BANK SELECT
             G05 22
16W18
        902
16W13
        97
             G05 28
                       ACCEPT
16W18
                      CLOCK
             G13 12
        900
16W06
        900
             H01
                       2+0
        900
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                       2+0 FAN OUT TO MEMORY
16W03
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                      2+1
16W06
        901
             H01
                   8
                      2+1 FAN OUT TO MEMORY
16W03
        901
             H01 19
                      2+2 FAN OUT TO MEMORY
16W03
        902
             H01 24
                      2+2
16W06
        902
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16W06
             H02
                       2+3
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                      2+3 FAN OUT TO MEMORY
16W03
        903
             H02
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16W06
        904
             H02
                      2+4
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                      2+4 FAN OUT TO MEMORY
16W03
        904
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                       2+5 FAN OUT TO MEMORY
        905
             H02 24
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                      2+6
                      2+6 FAN OUT TO MEMORY
16W03
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             H03
                      2+7 FAN OUT TO MEMORY
16W03
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             H03 19
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             H03 24
                       2+8 FAN OUT TO MEMORY
16W03
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                       2+9 FAN OUT TO MEMORY
16W03
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             H04
         92
             H04 8
                      2+10
16W06
             H04 19
                       2+10 FAN OUT TO MEMORY
16W03
         92
                       2+11 FAN OUT TO MEMORY
16W03
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              H04 24
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              H06
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                       2+12 FAN OUT TO MEMORY
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16W04
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        901
                       2+16 FAN OUT TO MEMORY
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                       2+17 FAN OUT TO MEMORY
16W04
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             H07 28
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                       2+18 FAN OUT TO MEMORY
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                       2+21 FAN OUT TO MEMORY
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                       2+22 FAN OUT TO MEMORY
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              H11
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                       2+26 FAN OUT TO MEMORY
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              H12
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                       2+27 FAN OUT TO MEMORY
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16W04
              H12
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16W04
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                       2+29 FAN OUT TO MEMORY
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16W05
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                       2+30 FAN OUT TO MEMORY
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                       2+40
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                   7
                       2+56 FAN
                                      TO MEMORY
16W16
                                      TO MEMORY
         92
              113
                       2+56 FAN
                                 OUT
                    9
         93
                       2+56 FAN
                                      TO MEMORY
16W15
              113
                                  OUT
```

```
2+56 FAN OUT TO MEMORY
         93
             113 13
16W14
                      2+55 FAN OUT
16W14
         92
             113
                  18
                                     TO MEMORY
             113
                                     TO MEMORY
         92
                       2+55 FAN OUT
16W15
                  20
16W16
         91
                       2+55 FAN OUT
                                     TO MEMORY
             113 22
                       2+55 FAN OUT
                                     TO MEMORY
16W17
         91
             113
                  24
16W39
         93
             113 26
                       2+56 FAN OUT
                                     TO MEMORY
16W13
         93
             113 28
                       2+56 FAN
                                 OUT
                                     TO MEMORY
                       2+57 FAN
                                      TO MEMORY
         94
16W14
             114
                                 OUT
             114
         94
                       2+57 FAN
                                OUT
                                     TO MEMORY
16W15
         95
                                     TO MEMORY
16W13
             114
                   7
                       2+58 FAN OUT
             114
                                     TO MEMORY
16W39
         95
                   9
                      2+58 FAN OUT
16W12
         95
             114 11
                       2 + 58
                           FAN
                                 OUT
                                     TO MEMORY
         95
                           FAN
                                     TO MEMORY
             114
                      2+58
                                OUT
16W11
                  13
16W11
             114
         94
                      2+57 FAN OUT
                                     TO MEMORY
                  18
         94
             114 20
                      2+57 FAN OUT
                                     TO MEMORY
16W12
16W39
         94
             114 22
                      2+57 FAN OUT
                                     TO MEMORY
16W13
         94
             114
                  24
                       2+57
                           FAN OUT
                                     TO MEMORY
                                     TO MEMORY
         93
             114
                       2+57 FAN OUT
16W16
                  26
         93
             114
                      2+57 FAN OUT
                                     TO MEMORY
16W17
                  28
         96
             115
                      2+59 FAN OUT
                                     TO MEMORY
16W11
                   3
         96
             115
                      2+59 FAN OUT
                                     TO MEMORY
16W12
16W17
         95
             115
                   7
                       2+59
                           FAN
                                 OUT
                                     TO MEMORY
         95
             115
                       2+59 FAN
                                     TO MEMORY
                   9
16W16
                                OUT
                                     TO MEMORY
         96
             115
                       2+59 FAN OUT
16W15
                 11
             115
                      2+59 FAN OUT
                                     TO MEMORY
16W14
         96
                  13
             115
         95
                                     TO MEMORY
16W14
                      2+58 FAN OUT
                  18
         95
                                     TO MEMORY
16W15
             115
                  20
                      2+58 FAN OUT
         94
                       2+58 FAN OUT
                                     TO MEMORY
16W16
             115
                  22
             115
         94
                       2+58 FAN OUT
                                     TO MEMORY
16W17
                  24
         96
                      2+59 FAN OUT TO MEMORY
16W39
             115 26
             115
                 28
16W13
         96
                      2+59 FAN OUT TO MEMORY
16W29
         91
             J01
                   5
                       2+1
         90
                       2+0 READ DATA ANALOG
16W29
             J01 12
         93
16W29
             J01 19
                       2+3
         92
16W29
             J01 26
                      2 + 2
         95
             J02
                      2+5
16W29
                   5
16W29
         94
             J02
                       2+4
                 12
         97
16W29
             102
                  19
                       2 * 7
         96
             J02 26
                       2+6
16W29
         99
                      2+9
16W29
             J03
                   5
         98
             J03 12
16W29
                      2+8
16W29
        901
             J03
                  19
                       2 - 11
                      2+10 READ DATA ANALOG
16W29
       900
             J03 26
                       WRITE CONTROL
16W28
       904
             J04
16W29
       903
             J04
                   5
                      CLOCK
             J04
                      GROUP CONTROL
16W28
       903
                   6
             J04
                       POSITION CONTROL
16W28
       902
                   8
                       REV MARK
             J04
16W29
        902
                  12
             J04 26
                       SECTOR MARK
16W29
       904
16W28
        905
             J04 27
                       CLOCK CONTROL
         98
             J05
                      2+8
16W28
         97
             J05
16W28
                      2+7
                   6
16W28
         96
             J05
                   8
                       4+6
             J05
                      2+11
16W28
        901
                 23
             J05
                       2+10 WRITE DATA
16W28
       900
                 25
16W28
         99
             J05 27
                       2+9
         92
16W28
             J06
                       2+2
         91
                      2+1
16W28
             106
                   6
16W28
         90
             J06
                       2 + 0
                          WRITE DATA
                   8
         95
                 23
                      2+5
             106
16W28
16W28
         94
             J06
                 25
                       2 + 4
16W28
         93
             J06 27
                       2+3
```

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```
16W24
        906
              K01
                        FUNCTION
16W24
        904
                        FULL
              K01
                    6
                        INACTIVE
16W24
        903
              K01
                        ACTIVE
MASTER CLEAR
16W24
        902
              K01 23
        907
                   25
16W24
              KO1
16W24
        905
              K01 27
                        EMPTY
16W24
         92
              K02
                        2+2 OUTPUT DATA
                        2+1 OUTPUT DATA
16W24
         91
              K02
                        2+0 OUTPUT DATA
2+5 OUTPUT DATA
16W24
         90
              K02
                    8
         95
16W24
              K02 23
                        2+4 OUTPUT DATA
16W24
         94
              K02 25
                        2+3 OUTPUT DATA
         93
              K02 27
16W24
16W24
         98
                        2+8 OUTPUT DATA
              K03
                        2+7 OUTPUT DATA
2+6 OUTPUT DATA
16W24
         97
              KD3
                    6
         96
16W24
              K03
                    8
              K03 23
                        2+11 OUTPUT DATA
16W24
        901
                        2+10 OUTPUT DATA
16W24
        900
              K03 25
         99
              K03 27
                        2+9 OUTPUT DATA
16W24
16W26
        902
              K04
                   4
                        ACTIVE
16W26
        904
              K04
                    6
                        FULL
              KO4
                        INACTIVE
16W26
        903
                    8
16W26
        905
              K04 27
                        EMPTY
         92
              K05
                        BIT 2
16W26
16W26
         91
              K05
                        BIT
                            1
                    6
16W26
         90
              K05
                    8
                        BIT
              K05 23
16W26
         95
                        BIT
              K05
                        BIT 4
16W26
         94
         93
              K05 27
                        BIT 3
16W26
                       BIT
16W26
         98
              Kn6
                            8
                    4
         97
                            7
                       BIT
16W26
              K06
                    6
         96
                        BIT 6
16W26
              K06
                    8
16W26
                       BIT 11
        901
              K06 23
                       BIT 10
16W26
        900
              K06 25
              K06 27
                       BIT
         99
16W26
16W27
         90
                        2+0
              LO1
                    5
16W27
         91
                    7
                        2+1
              L01
                        2+2
16W27
         92
              L01 10
         93
16W27
              L01 21
                        2+3
         94
                        2+4
16W27
              L01 24
16W27
         95
              L01 26
                        2+5
         96
                        2+6
16W27
              L02
                    5
         97
16W27
              L02
                        2+7
         98
16W27
              L02 10
                        2+8
16W27
         99
              L02 21
                        2+9
16W27
        900
              L02 24
                        2+10
16W27
        901
              L02 26
                        2+11
16W27
                        FUNCTION
        906
              L03
                    5
16W27
        902
              LO3
                        ACTIVE
                        FULL
16W27
        904
              L03 10
              L03 21
                        INACTIVE
16W27
        903
                        MASTER CLEAR
16W27
        907
              L03 24
              L03 26
                        EMPTY
16W27
        905
16W25
        903
              L07
                        INACTIVE
16W25
        904
              L07
                   17
                        FULL
                        2+6 INPUT DATA
2+7 INPUT DATA
16W25
         96
              L08
                    5
         97
16W25
              L08
                    7
16W25
         98
                        2+8 INPUT DATA
              L08 10
         99
16W25
              L08 21
                        2+9 INPUT DATA
                        2+10 INPUT DATA
16W25
        900
              L08 24
16W25
        901
              L08 26
                        2+11 INPUT DATA
         90
              L09
                        2+0 INPUT DATA
16W25
                    5
         91
                        2+1 INPUT DATA
              L09
16W25
```

```
16W25 92 L09 10 2+2 INPUT DATA
16W25 93 L09 21 2+3 INPUT DATA
16W25 94 L09 24 2+4 INPUT DATA
16W25 95 L09 26 2+5 INPUT DATA
16W25 906 L26 1
16W26 906 L26 3
16W26 907 L26 13
16W25 907 L26 15
```

```
2+0 READ DATA ANALOG
30W04
         90
             A01
                  9
                      2+1 READ DATA ANALOG
30W04
         91
             A01 15
30W04
         92
             A01 25
                      2+2 READ DATA ANALOG
30W04
         93
             A02
                      2+3 READ DATA ANALOG
                      244 READ DATA ANALOG
30W04
         94
             A02 15
30W04
         95
             A02 25
                      2+5 READ DATA ANALOG
                      2+6 READ DATA ANALOG
30W04
         96
             A03
         97
             A03 15
                      2+7 READ DATA ANALOG
30W04
30W04
         98
             A03 25
                      2+8 READ DATA ANALOG
                      2+9 READ DATA ANALOG
         99
30W04
             A 0 4
                  9
             A04 15
                      2+10 READ DATA ANALOG
30W04
       900
30W04
       901
             A04 25
                      2+11 READ DATA ANALOG
                      CLOCK
       905
             A17 12
30W03
                      2+0 WRITE DATA
2+1 WRITE DATA
30W03
         90
             A18
                  5
         91
             A18
30W03
30W03
         92
             A18 10
                      2+2 WRITE DATA
         93
             A18 21
                      2+3 WRITE DATA
30W03
         94
             A18 24
                      2+4 WRITE DATA
30W03
             A18 26
30W03
         95
                      2+5 WRÎTE DATA
                      2+6 WRITE DATA
         96
30W03
             A19
             A19
         97
                  7
                      2+7 WRITE DATA
30W03
                      2+8 WRITE DATA
30W03
         98
             A19 10
        99
             A19 21
                      2+9 WRÎTE DATA
30W03
                      2+10 WRITE DATA
2+11 WRITE DATA
             A19 24
30W03
       900
             A19 26
30W03
       901
             A20
                      POSITION CONTROL
30W03
       902
                  5
30W03
       903
             A20
                      GROUP CONTROL
                      WRITE CONTROL
30W03
       904
             A20 10
       902
             B12
                      REV. MARK
30W04
                  9
             B12 25
                      CLOCK
30W04
       903
```

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```
50W01
        901
              A 0 4
                        Y+2
                    1
50W01
        900
              A 0 4
                        Y*1
                     6
50W01
         99
               A 0 4
                     9
                         Y + 0
              A 0 8
        904
                         Y+5
50W01
                     1
        903
              A 0 8
                        Y+4
50W01
                     6
50W01
        902
               A 0 8
                     9
                        Y+3
        907
              A12
                         Y +8
50W01
                     1
        906
              A12
                         Y + 7
50W01
                     6
        905
50W01
               A12
                     9
                        Y+6
50W01
         92
              A16
                        X + 2
                     1
50W01
         91
              A16
                        X+1
                     6
         90
50W01
              A16
                     9
                        X + 0
50W01
         95
              A20
                        X*5
                     1
         94
                        X + 4
50W01
              A20
                     6
         93
              A20
                        X+3
50W01
                     9
50W01
         98
              A24
                        X + 8
                     1
              A24
         97
                        X + 7
50W01
                     6
          96
              A24
                     9
                        X+6
50W01
         90
                          KEYBOARD BIT 0 (TB2 1)
50W02
              A26
                     1
         91
                          KEYBOARD BIT 1(TB2 2)
50W02
              A26
                     2
50W02
          92
              A26
                     3
                          KEYBOARD BIT 2 (TB2 3)
          93
                         KEYBOARD BIT 3 (TB2 4)
50W02
              A26
                     4
                         KEYBOARD BIT 4 (TB2 5)
KEYBOARD BIT 5 (TB2 6)
50W02
          94
              A26
                     5
          95
50W02
               A26
                     6
          96
                          KEY DOWN (TB2 8)
               A26
50W02
                          KEY UP (TB2 7)
          97
               A26
50W02
                     8
                        UNBLANK LEFT CONSOLE OUNBLANK RIGHT CONSOLE O
          98
50W02
              B21
         99
              B21
50W02
                     6
                        MED. SIZE
SMALL SIZE
50W02
        901
               B22
                     1
50W02
        900
              B22
                     6
                        FOCUS AND ASTIGMATISM
50W01
        908
              B22
                        HORIZ. PUSH ANALOG
        905
              B23
50W02
                        HORIZ, PULL ANALOG
Vert. Push analog
50W02
        906
              B23
                     9
50W02
        903
              B24
                         VERT. PULL ANALOG
        904
              B24
50W02
```

and the

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```
81W00
       906
             C09
                      FUNCTION
                 3
             C09 13
81W00
       907
                      MASTER CLEAR
81W01
       907
             C09 15
                      MASTER CLEAR
             C09 16
                      CLOCK (1 MC)
81W02
       907
81W01
             C10
                      FULL
       904
                  1
81W00
       904
             C10
                  3
                      FULL
       905
                      EMPTY
             C10 13
81W00
                      ACTIVE
81W01
       902
             C10 14
                      EMPTY
81W01
       905
             C10 15
81W00
       902
             C10 16
                      ACTIVE
81W00
       903
             C10 26
                      INACTIVE
       903
             C10 28
                      INACTIVE
81W01
81W01
       901
             C11
                      DATA BIT 2+11
                 1
81W00
       901
             C11
                      DATA BIT 2+11
                      DATA BIT 2+10
             C11 13
81W00
       900
81W01
        99
             C11 14
                      DATA BIT 2+9
                      DATA BIT 2+10
             C11 15
81W01
       900
        99
             C11 16
                      DATA BIT 2#9
81W00
        98
                      DATA BIT 2+8
81W00
             C11 26
        98
             C11 28
                      DATA BIT 2+8
81W01
        97
             C12
                      DATA BÎT 2+7
81W01
                  1
        97
                      DATA BIT 2+7
             C12
81W00
                  3
81W00
        96
             C12 13
                      DATA BIT 2+6
81W01
        95
             C12 14
                      DATA BIT 2+5
81W01
        96
             C12 15
                      DATA BIT 2+6
        95
             C12 16
                      DATA BIT 2+5
81W00
        94
81W00
             C12
                 26
                      DATA BIT 2+4
        94
             012 28
                      DATA BIT 2+4
81W01
        99
             D09
                      DATA BIT 2+9
81W02
                  3
             D09
81W02
       900
                  5
                      DATA BIT 2+10
             D09
                  7
                      DATA BIT 2+11
81W02
       901
             D09
81W03
        96
                  8
                      DATA BIT 2+6
        97
             D09
                      DATA BIT 247
81W03
                  9
             D09 10
81W03
        98
                      DATA BIT 2*8
             D09 21
81W03
        99
                      DATA BIT 2+9
             D09 22
                      DATA BIT 2+10
81W03
       900
             D09 23
                      DATA BİT 2+11
81W03
       901
81W02
        96
             D09 24
                      DATA BIT 2+6
        97
             D09 26
                      DATA BIT 2+7
81W02
81W02
        98
             D09 28
                      DATA BIT 2+8
                      DATA BIT 2+3
81W02
        93
             D10
                  3
81W02
        94
                      DATA BIT 2+4
             D10
                  5
81W02
        95
             D10
                  7
                      DATA BIT 2+5
        90
                      DATA BIT 2+0
             D10
81W03
                  8
81W03
         91
             D10
                      DATA BIT 2+1
        92
                      DATA BIT 2+2
             D10 10
81W03
                      DATA BIT 2+3
81W03
        93
             D10 21
81W03
        94
             D10 22
                      DATA BIT 244
        95
             D10 23
                      DATA BIT 2+5
81W03
        90
             D10 24
                      DATA BIT 2+0
81W02
                      DATA BIT 2+1
81W02
        91
             D10 26
81W02
        92
                      DATA BIT 2+2
             D10 28
81W02
       903
             D11
                      INACTIVE
                      FULL
81W02
       904
             D11
                  5
       905
                      EMPTY
81W02
             D11
81W03
       903
             D11 21
                      INACTIVE
             D11 22
                      FULL
81W03
       904
81W03
       905
             D11 23
                      EMPTY
       907
                      CLOCK (1 MC)
81W03
             D11 26
81W03
       906
             D11 28
                      CLOCK (10 MC)
```

```
81W01 93 D12 1 DATA BIT 2*3
81W00 93 D12 3 DATA BIT 2*3
81W00 92 D12 13 DATA BIT 2*2
81W01 91 D12 14 DATA BIT 2*2
81W01 92 D12 15 DATA BIT 2*2
81W00 91 D12 16 DATA BIT 2*2
81W00 90 D12 26 DATA BIT 2*0
81W01 90 D12 28 DATA BIT 2*0
81W01 906 E09 1 FUNCTION
81W02 906 E12 12 CLOCK (10 MC)
```

```
FUNCTION
        906 009
82W01
             C09
                      FUNCTION
82W00
        906
                   3
        99
             D09
                      DATA BIT 249
82W02
                   3
             D09
                      DATA BIT 2+10
82W02
        900
                   5
                      DATA BIT 2+11
82W02
        901
             D09
                   7
82W03
             D09
                      DATA BIT 2+6
         96
                   8
         97
82W03
             D09
                   9
                      DATA BIT 2+7
                      DATA BIT 2+8
82W03
         98
             D09 10
82W00
        907
             C09
                  13
                      MASTER CLEAR
        907
             C09 15
                      MASTER CLEAR
82W01
             C09 16
82W02
        907
                      CLOCK (1 MC)
             D09 21
82W03
        99
                      DATA BIT 2+9
             D09 22
                      DATA BIT 2+10
82W03
        900
                      DATA BIT 2+11
DATA BIT 2+6
             D09 23
82W03
        901
82W02
         96
             D09
                 24
             D09 26
         97
                      DATA BIT 2+7
82W02
         98
             D09 28
                      DATA BIT 2+8
82W02
82W01
        904
             C10
                      FULL
                   1
                      FULL
        904
             C10
82W00
                   3
82W02
         93
             D10
                   3
                      DATA BIT 2+3
82W02
         94
             D10
                   5
                      DATA BIT 2+4
         95
             D10
                   7
                      DATA BIT 2+5
82W02
         90
             D10
                      DATA BIT 2+0
82W03
                   8
         91
82W03
             D10
                   9
                      DATA BIT 2+1
                      DATA BIT 2+2
82W03
        92
             D10 10
             C10 13
                      EMPTY
82W00
        905
                      ACTIVE
82W01
        902
             C10 14
                      EMPTY
82W01
        905
             C10 15
82W00
        902
             C10 16
                      ACTIVE
82W03
         93
             D10 21
                      DATA BIT 2+3
82W03
         94
             D10 22
                      DATA BÎT 2+4
         95
                      DATA BIT 2+5
82W03
             D10 23
82W02
             D10 24
         90
                      DATA BIT 2+0
                      INACTIVE
82W00
        903
             C10 26
        91
                      DATA BIT 2*1
             D10 26
82W02
             C10 28
                      INACTIVE
82W01
        903
        92
                      DATA BIT 2+2
82W02
             D10 28
             C11
82W01
        901
                      DATA BIT 2+11
                  1
                      DATA BIT 2+11
82W00
        901
             C11
                   3
82W02
        903
             D11
                   3
                      INACTIVE
        904
             D11
                   5
                       FULL
82W02
        905
                      EMPTY
82W02
             D11
        900
             C11 13
                      DATA BIT 2+10
82W00
82W01
        99
             C11 14
                      DATA BIT 249
                      DATA BIT 2+10
DATA BIT 2+9
82W01
        900
             C11 15
82W00
        99
             C11 16
        903
             D11 21
                       INACTIVE
82W03
82W03
        904
             D11 22
                      FULL
                      EMPTY
        905
82W03
             D11 23
         98
             C11 26
                      DATA BIT 2+8
82W00
82W03
        907
             D11 26
                      CLOCK (1 MC)
         98
             C11 28
                      DATA BIT 248
82W01
82W03
        906
             D11 28
                      CLOCK (10 MC)
82W01
         93
             D12
                      DATA BIT 2+3
                  1
         97
                      DATA BIT 2+7
82W01
             C12
                      DATA BIT 2+3
DATA BIT 2+7
82W00
         93
             D12
                   3
         97
82W00
             C12
                   3
             E12 12
                      CLOCK (10 MC)
82W02
        906
82W00
         92
             D12 13
                      DATA BIT 2+2
         96
             C12 13
                      DATA BIT 2+6
82W00
```

```
82W01 91 D12 14 DATA BÎT 2*1
82W01 95 C12 14 DATA BÎT 2*5
82W01 92 D12 15 DATA BÎT 2*2
82W01 96 C12 15 DATA BÎT 2*6
82W00 91 D12 16 DATA BÎT 2*1
82W00 95 C12 16 DATA BÎT 2*5
82W00 90 D12 26 DATA BÎT 2*0
82W00 94 C12 26 DATA BÎT 2*0
82W01 90 D12 28 DATA BÎT 2*0
82W01 90 D12 28 DATA BÎT 2*0
82W01 94 C12 28 DATA BÎT 2*0
```

## **COMMENT SHEET**

# CONTROL DATA 6601-H/6613-A Central Computer

Wire Lists

Customer Engineering Manual
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